

EXHIBIT 2001



- [54] **BUS INTERFACE SYSTEM AND METHOD**
- [75] Inventors: **Ronald H. Sartore**, San Diego; **Steven P. Larky**, Del Mar, both of Calif.
- [73] Assignee: **Cypress Semiconductor Corp.**, San Jose, Calif.
- [21] Appl. No.: **08/886,923**
- [22] Filed: **Jul. 2, 1997**
- [51] Int. Cl.⁷ **G06F 13/368**
- [52] U.S. Cl. **710/8; 710/9; 710/10**
- [58] Field of Search **379/395; 709/220, 709/250; 711/114, 115, 130; 713/100; 710/9**

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Primary Examiner—Thomas C. Lee
Assistant Examiner—Chien Yuan
Attorney, Agent, or Firm—Christopher P. Maiorana, P.C.

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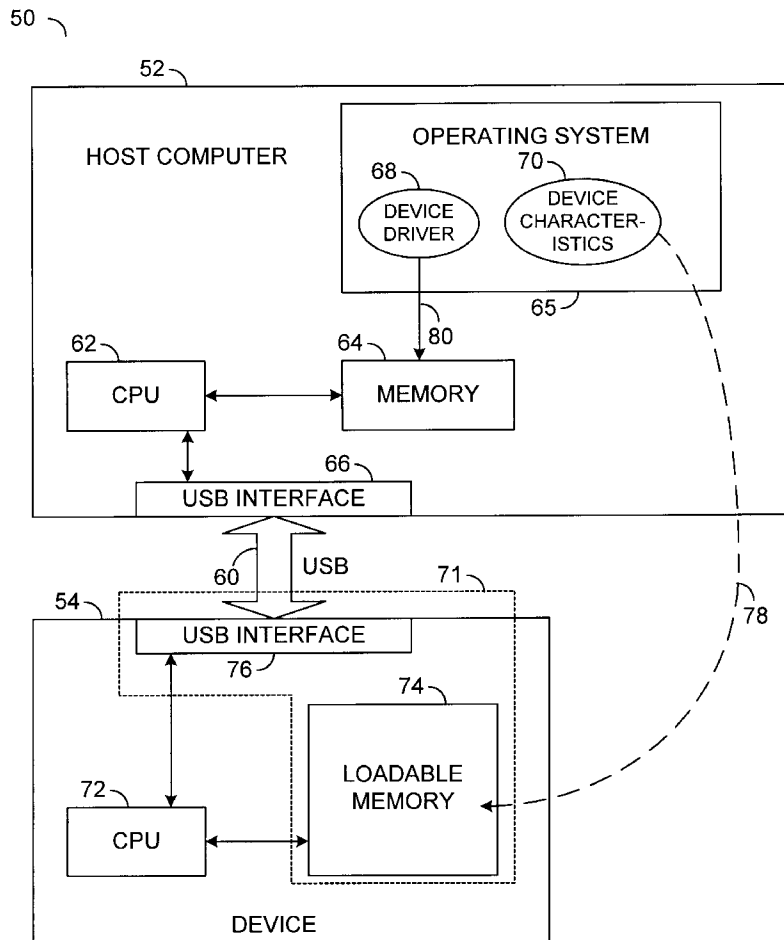
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[57] **ABSTRACT**

A system and method for reconfiguring a peripheral device connected by a computer bus and port to a host from a first generic configuration to a second manufacturer specific configuration is provided in which the configuration of a peripheral device may be electronically reset. A peripheral interface device for a standardized computer peripheral device bus and port is also provided in which a physical disconnection and reconnection of the peripheral device is emulated to reconfigure the bus and port for a particular peripheral device.

33 Claims, 7 Drawing Sheets



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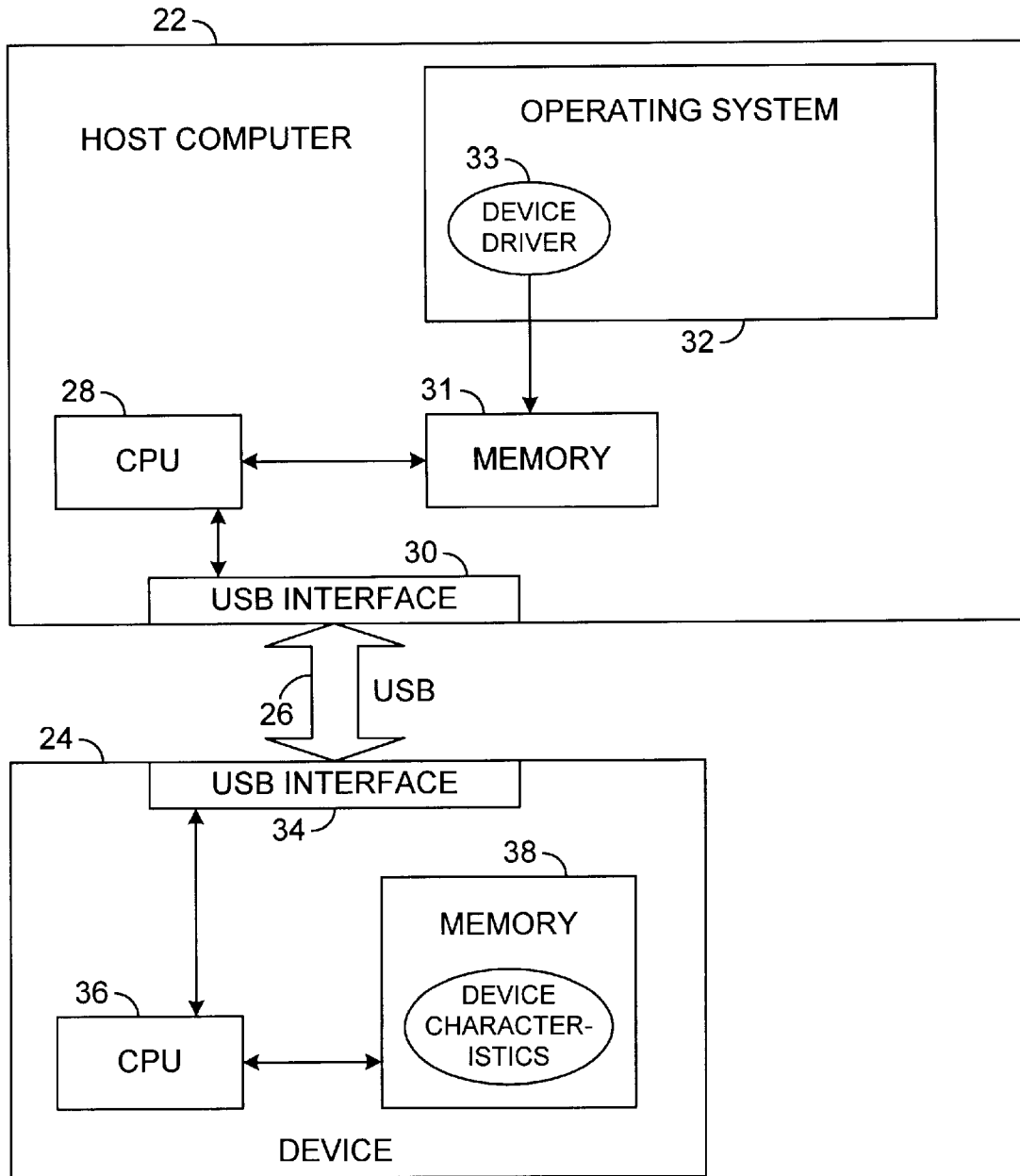


FIG. 1
(PRIOR ART)

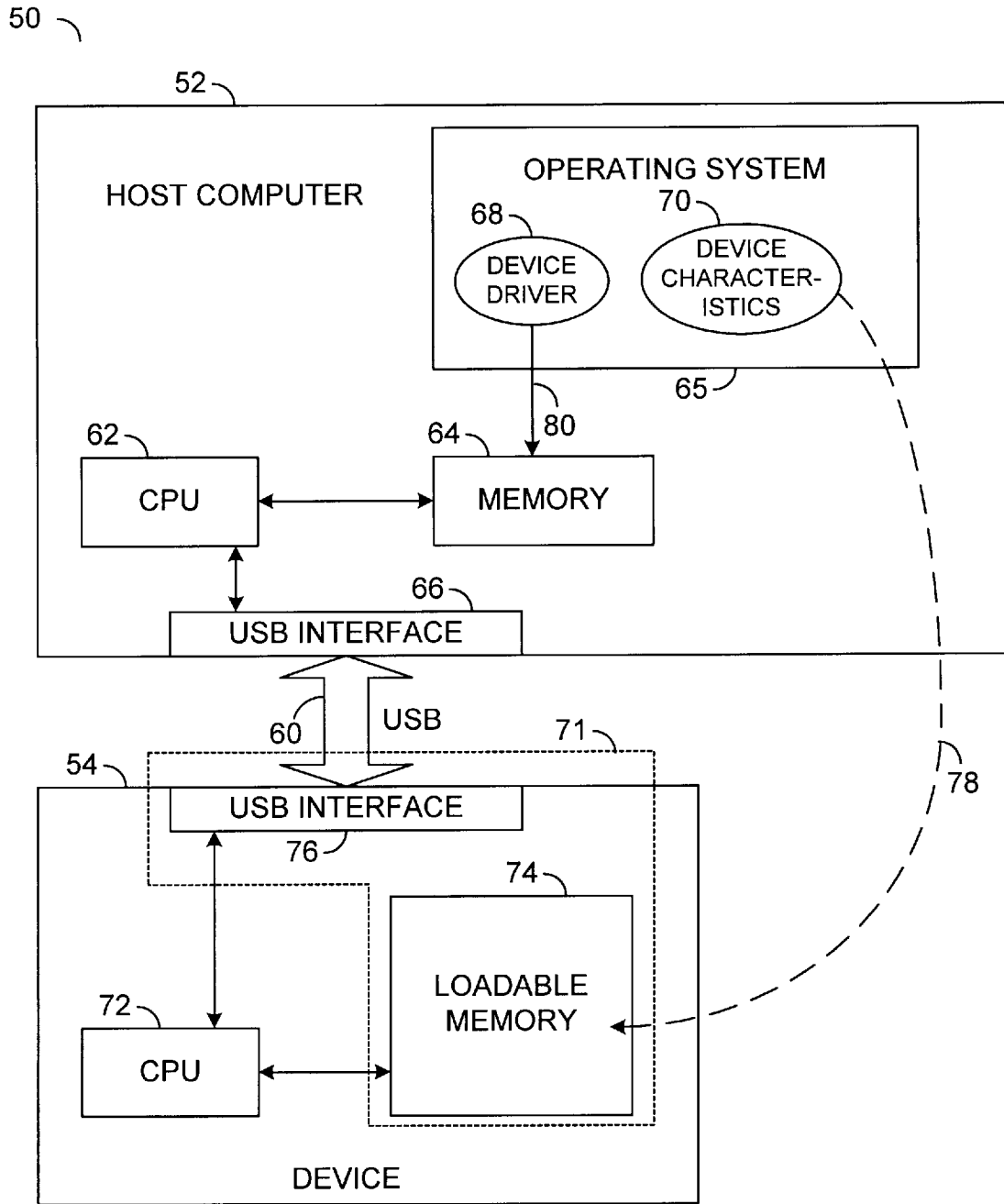


FIG. 2

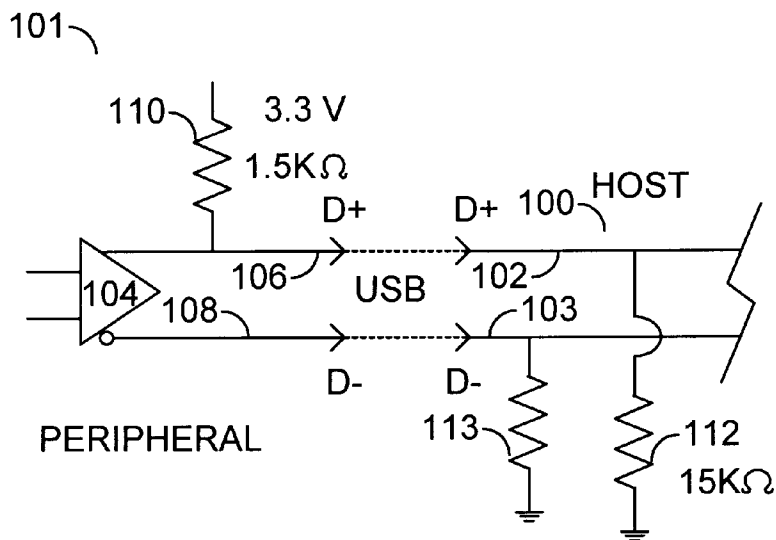


FIG. 3
(PRIOR ART)

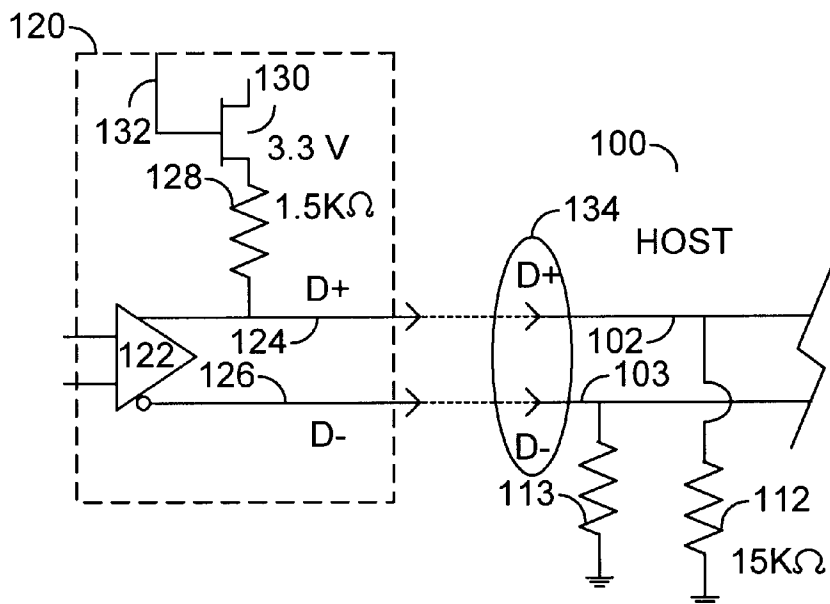


FIG. 4

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