

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

LG ELECTRONICS, INC.,
LG ELECTRONICS U.S.A., INC., and
LG ELECTRONICS MOBILECOMM U.S.A., INC.,
Petitioner

v.

CYPRESS SEMICONDUCTOR CORPORATION
Patent Owner

Case IPR2014-01386
Patent 6,012,103

DECLARATION OF JOHN GARNEY

EXHIBIT 2020

LG Elecs. v. Cypress Semiconductor
IPR2014-01386, U.S. Pat. 6,012,103

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IX. CONCLUSION48

I, John Garney, declare and state as follows:

I. INTRODUCTION

1. I have been retained by Kaye Scholer LLP at the rate of \$275 per hour to provide opinions in connection with the *Inter Partes* review of U.S. Patent Nos. 6,012,103 (the “’103 patent”). My compensation is not affected by the outcome of this proceeding.

2. I have no financial interest in any of the parties, or the ’103 patent.

II. QUALIFICATIONS

3. I received a Bachelor’s of Science in Mathematics and a Bachelor’s of Science in Computer Science from Purdue University in 1978. I received a Master’s of Science in Computer Science from Purdue University in 1980.

4. I was employed by Intel Corporation from 1980 through 2007 with two years (1988-1989) spent in a joint venture (BiiN) spun off by Intel/Siemens. I held a variety of positions while at Intel, starting as a Software Evaluation Engineer and finally as a Senior Staff Systems Architect in the Research and Development part of the Corporate Technology Group.

5. While employed at Intel Corporation as a software architect in 1991, I was Intel’s software representative to the Personal Computer Memory Card International Association (“PCMCIA”). As part of my responsibilities as Intel’s representative, I extensively reviewed the pre release 2.0 specification and discussed and debated clarifications and corrections to the specification in several full membership meetings.

6. I co-defined the Socket Services and Card Services portions of the

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