

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

CISCO SYSTEMS, INC. AND QUANTUM CORPORATION
Petitioner,

v.

CROSSROADS SYSTEMS, INC.
Patent Owner.

Case IPR2014-01226
Patent No. 6,425,035

DECLARATION OF JOHN LEVY, PH.D.

I, John Levy, make the following declaration based on my personal knowledge and, if called to testify before the Patent Trial and Appeal Board, could and would testify as follows:

I. INTRODUCTION

1. I have been retained in connection with *inter partes* review proceeding, IPR2014-01226, which concerns United States Patent No. 6,425,035 (the “’035 Patent”). This declaration contains my expert opinions concerning the ’035 Patent, the petition in this proceeding (the “Petition”), the prior art identified therein, and the facts alleged to support the Petition. I have been asked to evaluate and render an opinion concerning the grounds of unpatentability on which the present IPR has been instituted.

2. It is my understanding that The Patent Trial and Appeal Board (the “Board”) instituted the present *inter partes* review on the following alleged grounds of unpatentability:

- A. Claims 1-5 and 7-14 under 35 U.S.C. § 103(a) for obviousness over CRD-5500 Manual (Ex.1004) and HP Journal (Ex. 1006); and
- B. Claim 6 under 35 U.S.C. § 103(a) for obviousness over CRD-5500 Manual (Ex. 1004), HP Journal (Ex. 1006), and QLogic Data Sheet (Ex. 1007).

II. QUALIFICATIONS AND COMPENSATION

A. Background and Experience

3. I am the sole proprietor of John Levy Consulting, a consulting firm that specializes in consulting on managing development of high tech products, including computers and software. I have a Bachelor of Engineering Physics degree from Cornell University, a Master of Science degree in Electrical Engineering from California Institute of Technology, and a Ph.D. in Computer Science from Stanford University.

4. From 1965 to 1966, at Caltech, my field of study was information processing systems. My coursework included systems programming, including the construction of compilers and assemblers. From 1966 to 1972, during my graduate study at Stanford, my field of study was computer architecture and operating systems. My coursework included computer systems design, programming and operating systems. During my employment at Stanford Linear Accelerator Center while I was a graduate student at Stanford University, I was a programmer and I participated in the design and implementation of a real-time operating system for use in data acquisition, storage and display. My Ph.D. thesis research related to computer systems organization and programming of multi-processor computers. I developed and measured the performance of several parallel programs on a

simulated 16-processor system. I also studied file systems, disk and tape storage subsystems, and input/output.

5. I have been an employee and a consultant for over thirty years in the computer systems, software and storage industry. After earning my doctorate from Stanford University in Computer Science, I worked as an engineer at a number of leading companies in the computer industry, including Digital Equipment Corporation, Tandem Computer, Inc., Apple Computer, Inc., and Quantum Corporation.

6. From 1972 to 1974, at Digital Equipment Corporation, I supervised the development of an input/output channel for high-speed mass storage (disk, drum and tape), and its implementation for 7 different peripheral units and 3 different computer systems. From 1974 to 1975 I was project engineer leading the development of a new computer system. From 1975 to 1976, I supervised an operating system development group. During this time, I reviewed design changes and bug reports and fixes for two operating systems. While working for Digital Equipment Corporation, I wrote a long-term strategic plan for input/output buses and controllers and operating systems, including the conversion of most I/O buses to serial implementations. I am the author of a chapter on computer bus design in the book *Computer Engineering*, published in 1978 by Digital Press.

7. From 1977 to 1979, I was employed at Tandem Computer, Inc., where I worked on design of future multiprocessor systems. I also worked on problems related to distributed (networked) systems including rollback and recovery of distributed databases.

8. From 1979 to 1982, I was employed at Apple Computer, Inc., where I worked on the design of a new computer system, the Lisa, which was a precursor to the Macintosh. I also supervised hardware and software engineers in the development of a new local area network.

9. In 1980-81, I taught an upper-division course at San Francisco State University titled "Input/Output Architecture" which dealt with design of I/O channels, controllers, storage devices and their associated software.

10. From 1982 to 1992, I consulted for a variety of client companies, including Apple Computer, Quantum Corporation and Ricoh Co., Ltd., on project management and product development. Consulting work for Quantum included working as a temporary supervisor of a firmware development team for a new hard disk drive. During this time I co-authored a paper, cited in my attached CV, on the design of a file system for write-once optical disk drives, related to work I did for client Ricoh.

11. From 1993 to 1998, I was employed at Quantum Corporation, a manufacturer of hard disk drives, where I formed and managed a new group called

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