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1 INDEX:

2 EXAMINATION BY: PAGE

3 Mr. Vardanian.....7

4 PREVIOUSLY MARKED EXHIBITS:

5 Exhibit Intel 1002

6 Declaration of Uwe Kortshagen, Ph.D.

7 regarding Claims 1-4, 10-15, 17-18, 24-27,

8 and 29 of U.S. Patent No. 6,805,779

9 Exhibit Intel 1102

10 Declaration of Uwe Kortshagen, Ph.D.

11 regarding Claims 5, 6, 8, 19, 22, 23, and 43

12 of U.S. Patent No. 6,805,779

13

14 Exhibit TSMC 1201

15 U.S. Patent No. 6,805,779 B2

16 Exhibit TSMC 1202

17 Declaration of Uwe Kortshagen, Ph.D.

18 regarding Claims 30-40 of

19 U.S. Patent No. 6,805,779

20

21 Exhibit TSMC 1207

22 U.S. Patent No. 5,753,886

23 Exhibit TSMC 1214

24 International Application Published

25 Under the Patent Cooperation Treaty

Publication No. WO 83/01349

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Exhibit TSMC 1302

Declaration of Uwe Kortshagen, Ph.D.

regarding Claims 16, 28, 41, 42, 45 and 46

of U.S. Patent No. 6,805,779

Exhibit TSMC 1402

Declaration of Uwe Kortshagen, Ph.D.

regarding Claims 7, 9, 20, 21, 38 and 44

of U.S. Patent No. 6,805,779

Exhibit TSMC 1413

European Patent Application

No. 84850295.1

Date of filing 4/10/84

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