

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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ADVANCED MICRO DEVICES, INC., RENESAS ELECTRONICS CORPORATION, RENESAS ELECTRONICS AMERICA, INC., GLOBAL FOUNDRIES U.S., INC., GLOBALFOUNDRIES DRESDEN MODULE ONE LLC & CO. KG, GLOBALFOUNDRIES DRESDEN MODULE TWO LLC & CO. KG, TOSHIBA AMERICA ELECTRONIC COMPONENTS, INC., TOSHIBA AMERICA INC., TOSHIBA AMERICA INFORMATION SYSTEMS, INC., AND TOSHIBA CORPORATION,  
Petitioners,  
v.  
ZOND, LLC,  
Patent Owner.

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Case IPR2014-01047  
Patent 7,147,759 B2<sup>1</sup>

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Before KEVIN F. TURNER, DEBRA K. STEPHENS, JONI Y. CHANG, SUSAN L. C. MITCHELL, and JENNIFER M. MEYER,  
*Administrative Patent Judges.*

CHANG, *Administrative Patent Judge.*

ORDER  
Conduct of the Proceedings  
*37 C.F.R. § 42.5*

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<sup>1</sup> This Order addresses similar issues in the *inter partes* reviews, involving the following patents: U.S. Patent Nos. 6,805,779 B2, 6,806,652 B1, 6,853,142 B2, 7,147,759 B2, 7,604,716 B2, 7,808,184 B2, and 7,811,421 B2. For efficiency, we enter this Order in this case as representative. The parties may not use this style of filing in subsequent papers, without prior authorization.

Intel Corporation (“Intel”) filed petitions to institute an *inter partes* review (“Intel Proceedings”), challenging the following patents owned by Zond, LLC (“Zond”): U.S. Patent Nos. 6,805,779 B2, 6,806,652 B1, 6,853,142 B2, 7,147,759 B2, 7,604,716 B2, 7,808,184 B2, and 7,811,421 B2.

The following petitioners also filed petitions, requesting review of those patents (“Joinder Proceedings”), and motions for joinder, seeking joinder with Intel’s Proceedings:

(1) Taiwan Semiconductor Manufacturing Company, Ltd. and TSMC North America Corporation (collectively, “TSMC”);

(2) Fujitsu Semiconductor Limited and Fujitsu Semiconductor America, Inc. (collectively, “Fujitsu”);

(3) The Gillette Company (“Gillette”);

(4) Advanced Micro Devices, Inc., Renesas Electronics Corporation, Renesas Electronics America, Inc., GLOBALFOUNDRIES U.S., Inc., GLOBALFOUNDRIES Dresden Module One LLC & Co. KG, GLOBALFOUNDRIES Dresden Module Two LLC & Co. KG, Toshiba America Electronic Components, Inc., Toshiba America Inc., Toshiba America Information Systems, Inc., and Toshiba Corporation (collectively, “AMD”); and

(5) GLOBALFOUNDRIES U.S., Inc., GLOBALFOUNDRIES Dresden Module One LLC & Co. KG, GLOBALFOUNDRIES Dresden Module Two LLC & Co. KG (collectively, “Global”).

A list of *inter partes* reviews involving the aforementioned patents is provided in the Appendix of this Order.

On September 12, 2014, Intel and Zond filed a Joint Motion to Terminate and a true copy of their Written Settlement Agreement, made in connection with the termination, in accordance with 35 U.S.C. § 317(b) and 37 C.F.R. § 42.74(b), in each of Intel's Proceedings that involved the aforementioned patents. Upon consideration, we terminated Intel's Proceedings and entered judgment in each of the proceedings.

The termination of Intel's Proceedings renders the Motions for Joinder filed in the Joinder Proceedings moot. Given that, we hereby authorize TSMC, Fujitsu, Gillette, AMD, and Global to file a revised Motion for Joinder in each of their proceedings. As we articulated previously, having all of the Motions for Joinder at the time we decide the Petitions would help streamline the proceedings, in that we could decide the Motion for Joinder near the same timeframe. We also authorize Zond to file a revised Opposition to any revised Motion for Joinder. The parties should not file a revised motion or opposition if the parties are no longer seeking joinder or opposing joinder.

In consideration of the foregoing, it is hereby:

ORDERED that TSMC, Fujitsu, Gillette, AMD, and Global are authorized to file a revised Motion for Joinder, within *five business days* from this Order, in each of their proceedings involving the aforementioned patents;

FURTHER ORDERED that should TSMC, Fujitsu, Gillette, AMD, or Global file a revised Motion for Joinder, Zond is authorized to file an Opposition to the revised Motion for Joinder, within *five business days* from the filing of the revised Motion for Joinder, limited to ten pages; and

FURTHER ORDERED that no reply is authorized.

IPR2014-01047  
Patent 7,147,759 B2

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IPR2014-01047  
Patent 7,147,759 B2

## APPENDIX

### “Intel Proceedings”

U.S. Patent No. 6,805,779 B2  
IPR2014-00598, IPR2014-00686, IPR2014-00765, IPR2014-00820,  
IPR2014-00913

U.S. Patent No. 6,806,652 B1  
IPR2014-00843, IPR2014-00923, IPR2014-00945

U.S. Patent No. 6,853,142 B2  
IPR2014-00494, IPR2014-00495, IPR2014-00496, IPR2014-00497,  
IPR2014-00498

U.S. Patent No. 7,147,759 B2  
IPR2014-00443, IPR2014-00444, IPR2014-00445, IPR2014-00446,  
IPR2014-00447

U.S. Patent No. 7,604,716 B2  
IPR2014-00520, IPR2014-00521, IPR2014-00522, IPR2014-00523

U.S. Patent No. 7,808,184 B2  
IPR2014-00455, IPR2014-00456

U.S. Patent No. 7,811,421 B2  
IPR2014-00468, IPR2014-00470, IPR2014-00473

### “Joinder Proceedings”

U.S. Patent No. 6,805,779 B2  
IPR2014-00828, IPR2014-00829, IPR2014-00856, IPR2014-00859,  
IPR2014-00917, IPR2014-00918, IPR2014-01017, IPR2014-01019,  
IPR2014-01020, IPR2014-01022, IPR2014-01025, IPR2014-01070,  
IPR2014-01072, IPR2014-01073, IPR2014-01074, IPR2014-01076

U.S. Patent No. 6,806,652 B1  
IPR2014-00861, IPR2014-00864, IPR2014-01000, IPR2014-01003,  
IPR2014-01004, IPR2014-01066, IPR2014-01088, IPR2014-01089

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