UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY, LTD. (TSMC) and SAMSUNG ELECTRONICS., LTD Petitioner

v.

DSS TECHNOLOGY MANAGEMENT, INC. Patent Owner

> Patent 5,652,084 IPR2014-01030

Title: METHOD FOR REDUCED PITCH LITHOGRAPHY

ORAL DEPOSITION OF DR. CHRIS A. MACK MAY 14, 2015

ORAL DEPOSITION OF DR. CHRIS A. MACK, produced as a witness at the instance of the Petitioner, and duly sworn, was taken in the above-styled and numbered cause on May 14, 2015, from 9:03 a.m. to 1:08 p.m., before Larissa L. McPhearson, CSR in and for the State of Texas, reported by machine shorthand, at the offices of Nix, Patterson & Roach, LLP, 5215 North O'Connor Boulevard, Suite 1900, Irving, Texas 75039, pursuant to the Federal Rules of Civil Procedure.

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1	(On the record at 9:03 a.m.)
2	DR. CHRIS A. MACK,
3	having been first affirmed, testified as follows:
4	EXAMINATION
5	BY MR. CUNNING:
6	Q. Good morning, Dr. Mack. Could you state your
7	full name, please?
8	A. Chris Allen Mack.
9	Q. And what is your residence address?
10	A. 1605 Watchhill Road, Austin, Texas.
11	Q. And are you currently employed?
12	A. I work as a consultant.
13	Q. Okay. And do you
14	A. And part-time at the University of Texas at
15	Austin as adjunct faculty.
16	Q. Does your consulting business have a name?
17	A. No.
18	Q. So you just
19	A. I sometimes I apologize for speaking over
20	your any of your question. I sometimes use the name
21	lithoguru.com, which is my website address, as my
22	business name. That's an unofficial name, not an
23	official name, for my consulting business.
24	Q. Have you given a deposition before?
25	A. I have.

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1	Q. Okay. Well, you gave a deposition in a
2	litigation related to this proceeding, correct?
3	A. Yes.
4	Q. Okay. Other than that deposition, have you
5	been deposed before?
6	A. Yes.
7	Q. How many times?
8	A. Approximately 10 or 12.
9	Q. Okay. So you're somewhat familiar with the way
10	these things work, but I like to kind of go over the
11	procedures at the beginning to make sure everybody has
12	that fresh in their mind. Everything that we say is
13	being taken down by the court reporter, so for that
14	reason, it's important to give verbal answers rather
15	than nods or shakes of the head. Do you understand
16	that?
17	A. I do.
18	Q. Okay. And because the court reporter is trying
19	to take down both my questions and your answers, I will
20	try not to begin a question in the middle of your
21	answer. If you could try to wait until I finish my
22	questions before beginning your answers, that'll make it
23	easier for the court reporter. Is that fair?
24	A. Yes.
25	Q. Okay. If I ask you a question that you don't

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understand, I want to make sure that we understand each
other today, so if you can let me know, I will attempt
to clarify the question. Can you let me know if you
don't understand one of my questions?
A. Yes.
Q. And we'll probably take a break about every
hour, but there's nothing magic to that, so if you need
a break for any reason, you know, you can just let me
know and I'll accommodate that.
A. I will.
Q. And the only thing I would ask, though, if I
have a question that's pending, if you could answer that
question before we take a break. Can you agree to that?
A. Yes.
Q. Your attorneys may interpose objections to some
of my questions today. For the most part, that's just
for the record. If you understand the question,
you're you can go ahead and answer the question. Do
you understand that?
A. Yes.
Q. Is there any reason that you can't give full `
and truthful testimony today, or are you under the
influence of any medications, for instance?
A. No.
Q. And I apologize about that question, but you

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-1	never know if someone was in a car accident yesterday.
2	All right. Can you briefly describe your
3	educational background after high school?
4	A. I attended Rose-Hulman Institute of Technology
5	where I received four bachelor's degrees in chemistry,
6	chemical engineering, physics, and electrical
7	engineering. I attended the University of Maryland at
8	College Park where I received a master's degree in
9	electrical engineering, and I attended the University of
10	Texas at Austin where I received a Ph.D. in chemical
11	engineering.
12	Q. I grew up in Terre Haute, so I know
13	Rose-Hulman.
14	A. It is a fabulous school.
15	Q. All right. When did you begin working with
16	photolithography?
17	A. I began in photolithography in 1983.
18	Q. And what was the circumstances in which you
19	began working with photolithography?
20	A. I was working for the National Security Agency
21	in Fort Meade, Maryland and NSA has a fab for
22	manufacturing semiconductor devices and a research
23	organization to develop next generation processes for
24	that fab. I worked in the research organization.
25	Q. What type of photolithography equipment were

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1	you using at the time?
2	A. I began using what's called contact printers,
3	then moved on to the use of step-and-repeat lithography
4	tools.
5	Q. And for the transfer of the desired pattern to
6	the substrate for the contact printer, how does that
7	work?
8	A. With the contact printer, the mask is put in
9	direct contact with the photoresist covered wafer, or in
10	a slight variation called proximity printing, a small
11	gap in the order of tens of microns is maintained
12	between the mask and the photoresist coated wafer.
13	Light is shown through the mask to create a shadow of
14	the mask pattern that then exposes the photoresist.
15	Q. And for is the I'm sorry, the proximity
16	printing, is that a type of contact printing, or is that
17	a separate type of printing?
18	A. They are sometimes considered together because
19	they use the same often use the same tools, but they
20	are sometimes considered as separate types of pattern
21	patterning.
22	Q. And the step-and-repeat equipment that you
23	mentioned, how does that work for transferring the
24	pattern to the imaging layer?
25	A. A step-and-repeat system is a type of

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1	projection optical lithography where a lens is used to
2	project the image of a mask onto the photoresist coated
3	wafer.
4	Q. Are there other types of projection optical
5	lithography?
6	A. Yes.
7	Q. What other types of projection optical
8	lithography are you familiar with?
9	A. Well, another style of projection optical
10	lithography besides the step-and-repeat is the scanner
11	where well, if I could back up and explain what a
12	step-and-repeat is. That all of the projection
13	optical systems require a method of covering the entire
14	wafer which cannot be exposed all at one time. In a
15	stepper, a small portion of the resist coated wafer is
16	exposed at one time, then the wafer is stepped or moved
17	to a new location and then a repeat of that exposure
18	happens, step-and-repeat until the entire wafer is
19	exposed with multiple copies of the mask.
20	Another style of projection optical
21	lithography is the scanner. In a full wafer scanner,
22	the mask has an entire wafer's worth of patterns on it,
23	and then the entire mask and wafer are scanned past the
24	exposure region so that the entire wafer is exposed by
25	the entire mask.

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1	The next style of projection optical
2	lithography, the one that is most commonly used today,
. 3	is a hybrid of those two styles called the
4	step-and-scan. Within one exposure field, the mask and
5	the wafer are scanned past a slit which then exposes all
6	of the mask to that portion of the wafer. The wafer is
7	then stepped to a new location and the scanning
8	operation is repeated.
9	Q. Other than step-and-repeat, the last one that
10	you described, would that be called step-and-scan?
11	A. Step-and-scan is the common name for the last
12	one I described.
13	Q. Step-and-scan, and then just scanning
14	projection system. Any other types of projection
15	optical lithography that you're familiar with?
16	A. There are a number of what I would call minor
17	variations or alternate approaches that are not commonly
18	used. One, for example, is called maskless lithography
19	where we have a pattern that is created in some way
20	besides the use of a mask. Usually, it's a very small
21	region which is then scanned in some way on the wafer.
22	One example would be a very simple approach
23	of simply having a laser focused down to a small spot.
24	That spot is scanned in a raster scan approach across an
25	area, and the beam is turned on and off. This would be

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1	called a direct write lithography system.
2	Q. Are there other other than direct write, are
3	there other types of maskless lithography systems?
4	A. There are there is a style which uses
5	something called a digital multimirror. This is similar
6	to the digital multimirrors used in the digital light
7	projectors in movie theaters or home TVs where the
8	mirrors are basically turned on and off by tilting them
9	and that creates the pattern. The mirrors the
10	digital multimirror is illuminated, and an image of that
11	is then projected onto the wafer.
12	Q. Are you familiar with other types of maskless
13	lithography systems?
14	A. There is there are systems that use electron
15	beam lithography, beams of electrons rather than photons
16	to ex excuse me, to expose the resist coated wafer.
17	Besides direct write with the scanning spot, there is
18	something called a cell projection. A small cell, maybe
19	a it is a rectangle or some other primitive pattern
20	is projected onto the wafer and then more complex
21	patterns are built up through the use of combinations of
22	those primitive cells.
23	Q. Can you elaborate on that? When you say a
24	primitive pattern is projected onto the wafer, is the
25	are you projecting, you know, a limited set of features

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1	at a time?
2	A. Yes.
3	Q. Okay. So you would expose to do one portion of
4	the die and then do a second exposure for a different
5	portion of the die?
6	A. Typically, these primitive patterns are very
7	small and maybe with a fairly simple shape, for example,
8	a T pattern or an L pattern or simply a rectangle. Then
9	more complicated patterns are decomposed into the small
10	set of primitive patterns, and then one exposure at a
11	time, the more complicated pattern is built up.
12	Q. Okay. What is the light source, if it is a
13	light source, that's used with well, is it a photon?
14	Is it a light source, like a traditional optical light
15	source that's used with cell projection?
16	A. Typically, it is electron beams.
17	Q. Any other maskless systems that you're aware
18	of, maskless lithography systems?
19	A. There's actually quite a large variety of what
20	I would call research or well, let's just call them
21	research lithography approaches. One, for example, is
22	called dip-pen lithography. It is maskless. It uses
23	something like an atomic force microscope tip with a
24	chemically altered tip to chemically alter a substrate.
25	Q. Is that used in production?

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1	A. No.
2	Q. Was it used in a research context that the
3	well, in the 1994 time frame?
4	A. I don't believe so, but I'm not familiar with
5	when dip-pen lithography first began.
6	Q. What about cell projection, is that used in
7	commercial production today?
8	A. I believe that it is used in mask manufacturing
9	which also involves a lithography process, but not in
10	commercial wafer production.
11	Q. Cell projection was available in the 1994 time
12	frame?
13	A. I believe so.
14	Q. Or how about E-beam lithography, is well,
15	the E-beam lithography, the imaging, is that a version
16	of direct write?
17	A. Most of the E-beam lithography approaches are
18	direct write.
19	Q. So rather than using a laser to scan the
20	pattern into the resist, the projection equipment is an
21	E-beam?
22	A. That's correct.
23	Q. And how does the well, could you refer to
24	the would it be appropriate to call it a projection
25	apparatus, the that exposes the resist to the E-beam?
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1	A. In a scanning spot direct write lithography
2	system, we generally wouldn't refer to the system that
3	focuses the beam onto the resist coated wafer as a
4	projection system.
5	Q. Okay. Well, what would be the term that you
6	would use to refer to that tool, the E-beam tool?
7	A. We would generally call it a direct write
8	E-beam tool.
9	Q. Okay. So how do you program the pattern into
10	the direct write E-beam tool, if you don't use a mask?
11	A. You have what's called a beam blanker that
12	turns the beam on and off. The beam blanker is driven
13	by a database which contains the pattern information.
14	Q. So you program the series of instructions to
15	the database telling the direct write E-beam tool when
16	to expose and when to turn off the E-beam?
17	A. That's correct.
18	Q. And does the direct write E-beam tool, does it
19	move across the substrate?
20	A. There are two motions involved. While the
21	wafer is stationary, the beam is scanned over a very
22	small region to expose that small region. Then the
23	wafer is stepped to a new location and adjacent to the
24	previous location, and again, the beam is scanned over
25	the small area.

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1	Q. So the instructions that you program into the
2	database that tell the E-beam tool when to turn on and
3	off also program the E-beam tool how to scan across the
4	section that's being exposed?
5	A. Yes.
6	Q. And how does the how do you relay the
7	patterning information to a digital multimirror
8	apparatus?
9	A. Like with a direct write E-beam tool, it begins
10	with a description of the pattern that's desired to be
11	printed. Often for both tools, that database will be in
12	a file format called a GDS file. That serves as the
13	information that serves to control or program the
14	mirrors in the digital multimirror device, but it does
15	involve some translation of format.
16	Q. Does the digital multimirror device scan across
17	a portion of the wafer?
18	A. The systems I'm familiar with would not involve
19	any scanning.
20	Q. And what's the radiation source that's used
21	with the digital multimirror systems?
22	A. It would be a light source of some sort.
23	Q. And do you have direct experience working with
24	E-beam lithography?
25	A. I have limited experience with E-beam

1	lithography.
2	Q. What's your experience working with E-beam
3	lithography?
4	A. I have been involved in contracting to have
5	masks made by a mask maker who would use an E-beam
6	lithography tool for that purpose. I have been involved
7	in studying the physics of electron beam lithography and
8	in the development of a lithography simulator to
9	simulate electron beam lithography.
10	Q. I'm sorry, can I if we can go back, you say
11	that you've been involved with consulting to have masks
12	made that would be used with E-beam?
13	A. I didn't say consulting, but in some of my
14	various jobs, I've required the purchase of photomasks,
15	and that involved contracting with mask makers and
16	specifying the types of lithography that would be used
17	in the manufacture of those masks and understanding
18	their capabilities and sources of errors.
19	Q. So I just want to make sure I understand
20	because I thought, based on the earlier conversation,
21	E-beam was a maskless system, so is a mask created and
22	then the programming instructions for that mask are fed
23	into the machine?
24	MR. HOPEN: Objection to form.
25	A. No.

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1	Q. I'll ask it this way, why would you contract
2	with a mask maker for a photomask when dealing with
3	E-beam lithography?
4	A. Let me try to be a little bit more clear. The
5	photomask was being used for photolithography, optical
6	lithography. The making of the photomask involved the
7	use of E-beam lithography. So my experience with E-beam
8	lithography is mostly involved in the use of E-beam
9	lithography in the manufacture of masks.
10	Q. Do you have any experience with E-beam
11	lithography in the translation of patterns into
12	photoresist?
13	A. As I was saying, the manufacture of a photomask
14	is a lithography process that involves the use of
15	E-beams in exposing photoresist.
16	Q. Do you have experience using an E-beam
17	lithography system to pattern a semiconductor device?
18	A. I'm very familiar with that technology, but I
19	don't have direct experience in doing that.
20	Q. What about the cell projection technology, have
21	you used cell projection technology to manufacture
22	semiconductor devices?
23	A. No.
24	Q. The digital multimirror technology, have you
25	used the digital multimirror technology to manufacture
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1	semiconductor devices?
2	A. No.
3	Q. And the direct write lithography that you
4	mentioned, that would use a laser as a light source, so
5	it'd be an optical direct write lithography. Is that
6	correct terminology?
7	A. Yes.
8	Q. Okay. Have you used optical direct write
9	lithography to manufacture semiconductor devices?
10	A. No.
11	Q. Are you familiar with optical direct write
12	lithography?
13	A. I am.
14	Q. I'm going to hand you what's previously been
15	marked as DSS Exhibit 2007. And whenever I hand you a
16	document today, please feel free to flip through it to
17	the extent you need to do so to familiarize yourself
18	with a document. I may ask a question about a specific
19	portion, but you should read as much as you need to get
20	the context for the question.
21	A. Okay.
22	Q. Okay. My first question is whether or not you
23	recognize this?
24	A. Yes.
25	Q. And what is DSS 2007?

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1	Α.	It's my declaration in this IPR case.
2	Q.	On the last page of the document, page 11,
3	there's	a signature. Is that your signature?
4	Α.	Yes.
5	Q.	Did you sign the declaration on March 13th,
6	2015?	
7	Α.	Yes.
.8	Q.	Did you read the declaration before you signed
9	it?	
10	Α.	Yes.
11	Q.	Did you draft this declaration?
12	Α.	It was drafted in collaboration with the
13	attorney	s I was working with.
14	Q.	Okay. Who were the attorneys that you were
15	working	with?
16	Α.	Mostly Andriy and Anton, sitting here.
17	Q.	Did you
18	Α.	Mostly Andriy.
19	Q.	Did you work with anyone else in preparing this
20	declarat	ion?
21	Α.	I don't recall.
22	Q.	Other than attorneys, did you work with anyone
23	else in g	preparing the declaration?
24	Α.	No.
25	Q.	So you don't have like a research assistant or

1	anything	
2	Α.	No.
3	Q.	that would have had input into this?
4	Α.	No.
5	Q.	If you look at paragraph 16 of your
6	declarat:	ion
7	Α.	I promise you I've read this, but I've just now
8	noticed	that a couple of paragraphs are out of order in
9	their nur	mbering, but I
10	Q.	Ah.
11	Α.	I have found paragraph 16.
12	Q.	Yeah. Do you think paragraph 17 and 18 are
13	important	t?
14	Α.	And
15	Q.	I'm just
16	Α.	Well, all of the paragraphs, I believe, are in
17	their pro	oper order, but the numbering
18	Q.	Okay.
19	Α.	has been misapplied.
20	Q.	Okay. Well, the paragraph 16 on page 4.
21	Α.	I see it.
22	Q.	At the end of the paragraph, you say, I believe
23	that a pe	erson of ordinary skill in the art would have a
24	BS degree	e in engineering or similar field and several
25	years of	experience using photolithography methods for

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1	semiconductor fabrication. Do you see that?
2	A. Yes.
3	Q. What different methods of semiconductor
4	fabrication would a person of ordinary skill in the art
5	have had experience with?
6	A. Semiconductor fabrication is by and large
7	performed using step-and-repeat projection lithography
8	and step-and-scan projection lithography. So those are
9	the methods. One or both of those methods would be the
10	methods I'm referring to here.
11	Q. Would a person of ordinary skill in the art
12	have experience with E-beam lithography?
13	A. A person of ordinary skill in the art would be
14	familiar with E-beam lithography.
15	Q. And what about optical direct write
16	lithography, would a person of ordinary skill in the art
17	have had experience with optical direct write
18	lithography?
19	A. Optical direct write lithography is not
20	generally used in semiconductor fabrication, so I
21	believe a person of ordinary skill in the art would be
22	familiar with that technology, but not necessarily have
23	direct experience in its use.
24	Q. You reviewed the U.S. Patent 5,652,084 in
25	preparing your declaration?

1 Yes. Α. In your view, does the 084 patent claim methods 2 Ο. 3 of semiconductor fabrication that would include the use 4 of E-beam direct write lithography? 5 Α. I don't recall all the specifics -- methods mentioned in the 084 patent. Perhaps if I had a copy of 6 7 that patent, I could refresh my memory. 8 Q. Just so happens I have copies. MR. CUNNING: Let's mark this because this 9 10 appears to be a copy other than one that was used 11 previously, so can we mark this as TSMC-1014. 12 (Exhibit TSMC-1014 marked.) 13 Do you recognize TSMC-1014? 0. 14 Α. Yes. 15And that is the 084 patent that you considered Q. in preparing your declaration? 16 17 Α. Yes. 18 Q. So my prior question was whether, in your view, the 084 patent claimed methods of semiconductor 19 fabrication that included the use of E-beam direct write 20 21 lithography? 22 A. Well, I note that in column 3 in line 45, it 23 says, the term "radiation" may include ultraviolet 24 light, x-ray radiation, electron beam or E-beam radiation, vacuum radiation, or ion beam radiation, for 25

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1	example. So it's clear that electron beam is
2	contemplated, and it does say imaging layer may be
3	exposed to the first mask using any suitable form of
4	radiation. That is not direct write, though, that
5	includes a mask. Let me see if I find a reference to
6	direct write.
7	Q. Maybe I can help with that. If you look in
8	column 12
9	A. Yes.
10	Q it would be paragraph starting at around
11	line 20.
12	A. Yes.
13	Q. Does that mean to you that the 084 patent would
14	include lithography techniques that are maskless such as
15	direct write exposure?
16	MR. HOPEN: Objection to form.
17	A. Yes.
18	Q. When I want to back up to a sort of
19	traditional optical lithography technique that exposes a
20	pattern in the photoresist through a mask. Can you kind
21	of describe how that would work from start to finish?
22	A. Let me describe that in a basic projection
23	optical lithography
24	Q. Okay.
25	A system. Now, when you say describe it from

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1	start to finish, there are lots and lots of details.
2	Q. Okay.
3	A. So I don't know to what level of detail you
4	wish me to explain.
5	Q. Why don't you give me an overview, and then
6	I'll ask follow-up questions, as necessary.
7	A. Okay. We begin with a projection optical tool
8	that can project an image of a photomask onto a portion
9	of a resist coated wafer. The photomask is put or
10	mounted in the tool, and that photomask has alignment
11	marks or registration marks that allow the photomask to
12	be aligned to the optical system.
13	A wafer that has been previously coated
14	with a photoresist is also loaded onto a wafer table and
15	put into the system. That wafer is then aligned either
16	directly or indirectly to the photomask. A shutter is
17	opened which allows light to illuminate the photomask.
18	Light is transmitted through the clear portions of the
19	photomask blocked by the opaque portions of the
20	photomask. And as the light profligates away from the
21	photomask, it diffracts.
22	A projection lens, usually a very
23	complicated and large lens system, collects a portion of
24	the diffracted light and focuses it down to a plane that
25	is meant to coincide with the surface of the resist

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1	coated wafer. On that plane, that surface, an image of
2	the photomask is produced. That image then exposes the
3	photoresist, causes chemical change in response to the
4	amount of light that is exposing. At the end of the
5	exposure, the allotted exposure time, the shutter is
6	closed, and the wafer is removed. It then undergoes
7	further processing, for example, development to develop
8	and form patterns in the photoresist layer.
9	Q. In claim 1 of the 084 patent which is
10	TSMC-1014 do you have that?
11	A. Yes.
12	Q. Column 13.
13	A. Yes.
14	Q. The limitation 1(b) when it where it states,
15	patterning the first imaging layer in accordance with
16	the first pattern to form a first pattern layer having a
17	first feature you see that?
18	A. Yes.
19	Q. Is the process that you just described that
20	patterning step?
21	A. The process I described would fall under that
22	claim element.
23	Q. Okay. It says that the photomask has alignment
24	marks that allows you to align it with the optical
25	system. What do you mean by that?

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1	A. There are two major goals in accurate
2	patterning or forming a first pattern, a first pattern
3,	layer, for example, using the terminology of claim 1.
4	One is to produce the correct size of the feature or
5	shape of the pattern, and the second is to position that
6	pattern in the proper spot on the wafer. One of
7	ordinary skill in the art at the time frame of the 084
8	patent would be well aware that these are the two
9	criterion that lithographers worry about when assessing
10	the quality of a lithographic process.
11	A necessary step in achieving the proper
12	placement of the pattern on the wafer is the proper
13	placement of the photomask in the optical projection
14	tool. That is accomplished by measuring alignment marks
15	on the reticle after the reticle has been mounted in the
16	projection optical tool so that a measurement of the
17	exact position of that reticle in multiple degrees of
18	freedom can be made.
19	Q. What is a reticle?
20	A. I'm sorry, a reticle is a simply another
21	name for a photomask.
22	Q. So other than aligning the mask with the
23	optical system, are there other types of alignment that
24	are necessary for a projection photolithography system
25	to accurately transfer the pattern to the wafer?

1	A. Well, there are actually quite a large number,
2	but the most important one that is done every single
3	time that lithography tool is used is the alignment of
4	the wafer. This can be done indirectly where the wafer
5	is aligned to the projection optical system or a
6	coordinate system associated with the projection optical
7	system.
8	In that case, because the reticle has been
9	properly aligned to the projection optical system and
10	the wafer is properly aligned to the projection optical
11	system, the result is a proper alignment of the wafer to
12	the reticle. Ultimately, the wafer can be aligned
13	directly to the reticle without going through the
14	intermediate step of the coordinate system of the
15	projection optical system.
16	Q. If you misalign the mask to the optical system,
17	will you transfer the same pattern to the wafer?
18	A. I'm not sure what pattern you're referring to.
19	Q. The pattern in the mask.
20	A. The pattern on the mask, when you say the same
21	pattern, you can think of that independent of everything
22	else and all right. I have to back up. I apologize.
23	I think the correct answer is that very much depends on
24	the situation.
25	Q. Okay.

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1	A. In a simplest thought experiment, simplest
2	scenario, you have resist coated wafer that is not at
3	all influenced by any of the existing patterns on the
4	wafer projecting an image of that mask into that resist
5	coated wafer, would then be produce patterns in the
6	resist that were independent of the patterns underneath
7	it. This will not always be the case, though.
8	Q. Why will it not always be the case?
9	A. Existing patterns on the wafer often involve
10	topography, that is, the wafer is not perfectly flat
11	after it has been previously patterned. When coating
12	photoresist onto this topography wafer, the coating of
13	the photoresist will have variations in the thickness of
14	the photoresist that depend upon the topography on the
15	wafer. In some cases, the variation and the thickness
16	of the photoresist can be quite extreme with very thin
17	regions of photoresist on the high parts of the wafer
18	and very thick regions of photoresist in the low parts
19	of the wafer.
20	If I'm projecting a pattern onto this
21	wafer, I could get very difficult results depending on
22	whether the pattern is being projected onto the high
23	part of the wafer with a thin photoresist or the low
24	part of the wafer with a thick photoresist.
25	Q. And presumably in your design that I see and

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1	particularly the lithography for this layer, you would
2	taken that into account and have a desired placement
3	of the pattern so that you get accurate transfer; is
4	that correct?
5	A. That's correct.
6	Q. So if you misalign the mask and expose the
7	pattern to a slightly different portion of the wafer,
8	you would get a different pattern transfer?
9	A. That's possible.
10	Q. Let's take a more simple system, the we're
11	going to coat on a layer of photoresist. It's our first
12	coating of photoresist. The wafer has not been
13	previously patterned, and there you know, actually,
14	let's do this I apologize. Give me one second.
15	MR. CUNNING: I'll mark as TSMC1-1015
16	Patent Owner DSS Technology, Inc.'s Petition Response
17	to Petition.
18	MR. O'DELL: That's TSMC-1015?
19	MR. CUNNING: Yes, 1015.
20	MR. O'DELL: Okay. I thought I heard an
21	extra 1.
22	THE REPORTER: You did.
23	MR. O'DELL: Okay.
24	MR. CUNNING: I apologize.
25	(Exhibit TSMC-1015 marked.)

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1	Q.	Have you seen this document before?
2	Α.	Yes.
3	Q.	Did you provide input into the preparation of
4	the pate	nt owner DSS Technology's response to the
5	petition	?
6	Α.	Outside of my declaration, no.
7	Q.	Turn to page 3 of the document.
8	Α.	Yes.
9	Q.	You see the figure depicted on page 3?
10	Α.	Yes.
11	Q.	Is that figure a depiction of an optical
12	projectio	on type of photolithography that we similar
13	to the o	ne we've been discussing?
14	Α.	Yes.
15	Q.	On the wafer that's coated with a photoresist,
16	there's a	a what appears to be a grid with a series of
17	small sq	uares. You see that?
18	Α.	Yes.
19	Q.	Does each one of those represent an individual
20	die?	
21	Α.	Possibly.
22	Q.	So at least in some circumstances, the pattern
23	that is d	depicted in the mask in figure let's label
24	DSS-2006	at page 2 could correspond to a layer that
25	would be	used on a complete die?

1	A. Yes.
2	Q. Now, if you were to misalign the mask with the
3	optical projection system, you would expose could you
4	expose that pattern such that it did not fall completely
5	within a die?
6	A. I'm sorry, I don't understand your question.
7	Q. Okay. If we shift the mask a small degree to
8	the left or right, it could fall half on a die and half
9	off of a die, correct?
10	A. Are you referring to a die that already exists
11	on the wafer a previous pattern on the wafer?
12	Q. No. So right now, what they're depicting is
13	what's being depicted in DSS 2006 at page 2, that's
14	the figure on page 3 of TSMC-1-1
15	A. Yes.
16	Q 1015?
17	A. Yes.
18	Q. Is showing the projection of this pattern that
19	is shown in the mask down onto an individual die on the
20	wafer?
21	A. Well, I think this shows the printing of one
22	layer of a die where approximately half of the wafer has
23	already undergone the exposure
24	Q. Uh-huh.
25	A and half of the wafer has not yet undergone

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1	the exposure that's being depicted in the figure.
2	Q. And the idea is to repeat that pattern, that
3	layer for each die on the wafer?
4	A. Well, let's assume a scenario where the mask
5	only includes one die.
6	Q. Okay.
7	A. It's pretty typical to include more than one
8	die on a mask, but I think for simplicity of our
9	discussion, in describing a scenario where there's only
10	one die on the reticle we'll make it easier.
11	Q. Okay.
12	A. So every time the wafer steps to a new
13	location, an image of that layer of the die is projected
14	and exposed into the photoresist.
15	Q. And for that die, if you were to move the mask,
16	you would project a different pattern into that die?
17	A. Well, you would project the same pattern but
18	somewhat misaligned or a slightly different position as
19	it was projected onto the wafer.
20	Q. If the mask is off alignment, is it possible
21	that a portion of the image a portion of the pattern
22	would be on die and a portion of the pattern would be
23	off die?
24	A. I'm not following exactly what you're referring
25	to. So the reticle has a pattern of one layer of a die.

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1	Q. Uh-huh.
2	A. The wafer unless this is the very first
3	lithography step that's being used, the wafer includes
4	many prior patterns
5	Q. Uh-huh.
. 6	A from prior lithography and etching steps
7	already on the wafer. So we are projecting an image of
8	this layer of the photomask onto patterns that have been
9	previously produced on that wafer.
10	Q. Okay. Let's take a simpler example where this
11	is the very first layer that is being projected onto the
12	wafer.
13	A. Okay.
14	Q. Yeah. It's important where you project the
15	pattern onto the wafer, correct?
16	A. Yes.
17	Q. Okay. And if you project it in the wrong place
18	on the wafer, is it possible that a portion of the
19	pattern would be on die and a portion of the pattern
20	would be off die?
21	A. Well, the pattern is the die.
22	Q. Yes. But the wafer is divided into a sort of
23	grid space of multiple individual dies, correct?
24	A. It will be when we're finished.
25	Q. Okay. So is it important to project each

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1	successive layer into the same space so that they are on
2	the same die?
3	A. Yes.
4	Q. And if the mask is off alignment, wouldn't it
5	be possible to project the image onto instead of onto
6	one die where a portion is on, say, die one and another
7	portion is on die two?
8	A. When projecting a pattern of, let's say, one
9	complete die
10	Q. Uh-huh.
11	A one layer of one complete die onto the
12	wafer, it is spaced apart from the adjacent patterns of
13	dies by a street or a region called a kerf.
14	Q. Okay.
15	A. This region eventually will be the area where a
16	saw will cut the wafer to separate each individual die
17	from each other.
18	Q. That'll happen when they're diced up in the
19	packaging house?
20	A. That's exactly right.
21	Q. Okay.
22	A. The distance between one die and the next is
23	sufficiently large to accommodate the width of the
24	diamond tipped saw
25	Q. Okay.

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1	A that's typically used for this cutting
2	operation and on the order of a hundred microns.
3	Q. Okay.
4	A. That distance far exceeds any
5	Q. The size of the die?
6	A. No.
7	Q. Okay.
8	A. That distance far exceeds any misalignment that
9	can happen in the die so that if the reticle were
10	misaligned by any normal, reasonable amount that is
11	possible in a tool, it would still not be a sufficient
12	level of misalignment so that the pattern of one die
13	could possibly fall on top of a pattern of an adjacent
14	die.
15	Q. Okay. Then let me ask it this way: If the
16	mask was misaligned or the reticle was misaligned,
17	couldn't you project the pattern to where it was
18	partially on the die and partially on the curve (sic)?
19	A. The kerf, that's correct.
20	Q. And in that scenario, you would have a partial
21	transfer of the pattern to the die?
22	A. I think a more accurate statement would be you
23	would have a complete transfer of the pattern in
24	misaligned form.
25	Q. On the die itself, would you have a complete
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1	transfer of the desired pattern?
2	A. Depending on what you mean desired.
3	Misalignment is not desired, but you would have a
4	complete transfer of the pattern.
5	Q. Would it be on the die?
6	A. Yes.
7	Q. The kerf is part of the die?
8	A. The portion of the kerf that doesn't get cut
9	off by the saw will be on the final die that is packaged
10	and used.
11	Q. Okay. What if you project the portion a
12	portion of the pattern onto the part of the kerf that is
13	going to be cut off?
14	A. That would be a magnitude of misalignment far
15	in excess of what one would expect to see in a
16	manufacturing environment.
17	Q. It's possible?
18	A. Many things are possible.
19	Q. Okay. So if the mask is not is let's
20	just say it's deliberately shifted. Couldn't you
21	transfer a different pattern to the die using the same
22	mask?
23	A. I don't know what you mean by a different
24	pattern.
25	Q. Okay. If, when you're patterning the die, you

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1	want in this example where we're using one pattern per
2	reticle, you want to transfer that complete pattern to
3	the die, right? I mean, that is that correct that
4	that is the goal?
5	A. Yes.
6	Q. Okay. And the goal of I think you mentioned
7	there were two primary goals. One was to get the
8	correct size, and the other was to get proper placement
9	of the pattern on the wafer, correct?
10	A. Yes.
11	Q. Okay. And so if you transfer the pattern such
12	that it is partially on the die and partially off the
13	die, have you met one of the primary goals?
14	A. I thought we were referring to the very first
15	patterning step where there's no existing patterns
16	Q. Uh-huh.
17	A on the wafer.
18	Q. Uh-huh.
19	A. The in this very first patterning step, the
20	die is defined by the pattern that's projected onto the
21	wafer, so the outline of the die at this point in the
22	manufacturing process is the pattern that you've just
23	projected and printed onto the wafer.
24	Q. Okay. But you're doing this multiple times,
25	correct?

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1	A. Eventually, we will do this many, many times.
2	Q. I mean, even in the wafer contains many
3	dies, so the wafer in a step-and-repeat system, every
4	which is I what is depicted here at page 3,
5	correct? This is the this would be a step-and-repeat
6	system?
7	A. Yes.
8	Q. Okay. So we scan and transfer this pattern
9	from the reticle to an individual die, and then the
10	wafer with the photoresist moves indexes to the next
11	spot to repeat the pattern on the next die; is that
12	correct?
13	A. Yes.
14	Q. Okay. If the wafer let's talk about
15	movement of the wafer stage. If the wafer moves too
16	far, you would get a transfer of a pattern that's
17	possibly on die and off die, correct?
18	A. Are we now referring to not the first level of
19	exposure but some subsequent layer?
20	Q. No. Let's stick with the first level.
21	A. All right. At the first level, there are no
22	die on the wafer to project upon. There's only a wafer.
23	Q. Well, isn't the wafer mapped out into portions
24	where you intend to place each die so you can build .
25	successfully successively on those layers?

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1	A. Yes.
2	Q. Okay. So we've done 150 dies and they're all
3	in their predetermined grid space, and the next one,
4	something happens. The wafer stage moves further than
5	it is supposed to move. Wouldn't that end up with a die
6	that is or a pattern that is transferred now
7	partially on die and partially off die?
8	A. Well, again, there are no die yet. Only after
9	we perform many lithography steps with many other
10	processes in between do we build up layer by layer, the
11	die. So I still don't understand what you mean by being
12	off die if there is no die.
13	Q. So prior to let's look at, again, the figure
14	on page 3, and we've got the individual repeated
15	patterns in a grid on the left half of the wafer,
16	correct?
17	A. Yes.
18	Q. This spacing pattern, isn't that is that
19	intended to be repeated across the wafer?
20	A. It is intended to be a regular grid, yes.
21	Q. Okay. And so if you then now place one of
22	these patterns at an irregular place on that grid,
2,3	wouldn't it be on die and off die?
24	A. Well, it would be on grid versus off grid.
25	Q. Okay. And the grid is designed to define the

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1	spaces for the individual dies; is that correct?
2	A. Yes.
3	Q. For looking at the portion of the grid where
4	the pattern was transferred where only a partial portion
5	of the pattern was transferred, wouldn't that be
6	different than if you had exposed it correctly on grid?
7	A. Well, again, I don't know what you mean by
8	partial printing, partial transfer. We move the wafer
9	and project an image of the entire reticle, the entire
10	die onto the wafer. That process is never perfect, it
11	is never perfectly on grid. There's going to be some
12	level of error every single time. If those errors are
13	sufficiently small, then we get high yielding, well
14	working chips when we're done.
15	When those errors become sufficiently
16	large, we suffer from yield loss from die that do not
17	work properly because the patterns are mispositioned
18	relative to each other.
19	Q. Okay. So let's start with a middle layer.
20	We've already we've patterned at least one layer
21	underneath, and we have the reticle misaligned for one
22	of the dies. Couldn't you then have a pattern that was
23	transferred to a portion of the pattern was on die
24	and a portion of the pattern was off die?
25	A. If you deliberately misaligned far in excess of

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1	what a lithography tool used in semiconductor production
2	is capable of, it would be possible to misalign a
3	pattern so that some of the features in printing will be
4	printed in the kerf rather than in the region that we
5	would call a die.
6	Q. Okay. And the in that scenario, the pattern
7	that was printed on the die would be different than the
8	pattern that had been printed on a previous die where
9	the reticle was aligned properly?
10	A. Well, if you're saying that if I misaligned by
11	some huge amount so that a portion of the reticle
12	pattern is printed in the kerf, then I think it's
13	obvious to say that not all of the reticle pattern is
14	printed in the die.
15	Q. Okay. And the pattern that did get printed in
16	the die would be different than the pattern that was in
17	the reticle?
18	A. That portion could potentially be the same or
19	different.
20	Q. Okay. If you were to rotate the reticle by 45
21	degrees, would you print the same pattern in the die?
22	A. I'm aware of no lithography tool that will
23	enable you to rotate the reticle by 45 degrees.
24	Q. But if you did that, would you print the same
25	pattern in the die?

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<ul> <li>2 say a square for simplicity, would print on top of an</li> <li>existing square representing the prior patterns in such</li> <li>4 a way that the corners of the newly printed image would</li> <li>5 fall outside of the die area</li> <li>0. It would be different</li> <li>A that exists on the wafer at that time.</li> <li>0. It would be a different pattern?</li> <li>A. It would be the pattern rotated by 45 degrees.</li> <li>0. Okay. And do you consider that the same?</li> <li>1. A. It depends on your perspective.</li> <li>0. Well, would it function the same?</li> <li>A. Would the die function the same?</li> <li>A. No.</li> <li>Q. So would that be seen as an accurate or</li> <li>faithful pattern transfer?</li> <li>A. For the purpose of chip making, no.</li> <li>Q. Okay. So if your perspective is a lithography</li> <li>method in for the purpose of semiconductor</li> <li>manufacturer, would you have transferred the same</li> <li>pattern to the imaging layer?</li> <li>A. Again, it I think you're transferring the</li> <li>pattern in the wrong place.</li> <li>Q. And if you transfer the pattern in the wrong</li> </ul>	1	A. Rotated by 45-degree rectangle or maybe we'll
<ul> <li>a existing square representing the prior patterns in such</li> <li>a way that the corners of the newly printed image would</li> <li>fall outside of the die area</li> <li>Q. It would be different</li> <li>A that exists on the wafer at that time.</li> <li>Q. It would be a different pattern?</li> <li>A. It would be the pattern rotated by 45 degrees.</li> <li>Q. Okay. And do you consider that the same?</li> <li>A. It depends on your perspective.</li> <li>Q. Well, would it function the same?</li> <li>A. Would the die function the same?</li> <li>A. No.</li> <li>Q. So would that be seen as an accurate or</li> <li>faithful pattern transfer?</li> <li>A. For the purpose of chip making, no.</li> <li>Q. Okay. So if your perspective is a lithography</li> <li>method in for the purpose of semiconductor</li> <li>manufacturer, would you have transferred the same</li> <li>pattern to the imaging layer?</li> <li>A. Again, it I think you're transferring the</li> <li>pattern in the wrong place.</li> <li>Q. And if you transfer the pattern in the wrong</li> </ul>	2	say a square for simplicity, would print on top of an
4a way that the corners of the newly printed image would5fall outside of the die area6Q. It would be different7A that exists on the wafer at that time.8Q. It would be a different pattern?9A. It would be the pattern rotated by 45 degrees.10Q. Okay. And do you consider that the same?11A. It depends on your perspective.12Q. Well, would it function the same?13A. Would the die function the same?14Q. Yes.15A. No.16Q. So would that be seen as an accurate or17faithful pattern transfer?18A. For the purpose of chip making, no.19Q. Okay. So if your perspective is a lithography20method in for the purpose of semiconductor21manufacturer, would you have transferred the same22A. Again, it I think you're transferring the23Q. And if you transfer the pattern in the wrong	3	existing square representing the prior patterns in such
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<ul> <li>Q. It would be different</li> <li>A that exists on the wafer at that time.</li> <li>Q. It would be a different pattern?</li> <li>A. It would be the pattern rotated by 45 degrees.</li> <li>Q. Okay. And do you consider that the same?</li> <li>A. It depends on your perspective.</li> <li>Q. Well, would it function the same?</li> <li>A. Would the die function the same?</li> <li>Q. Yes.</li> <li>A. No.</li> <li>Q. So would that be seen as an accurate or</li> <li>faithful pattern transfer?</li> <li>A. For the purpose of chip making, no.</li> <li>Q. Okay. So if your perspective is a lithography</li> <li>method in for the purpose of semiconductor</li> <li>manufacturer, would you have transferred the same</li> <li>pattern to the imaging layer?</li> <li>A. Again, it I think you're transferring the</li> <li>pattern in the wrong place.</li> <li>Q. And if you transfer the pattern in the wrong</li> </ul>	5	fall outside of the die area
7A that exists on the wafer at that time.8Q. It would be a different pattern?9A. It would be the pattern rotated by 45 degrees.10Q. Okay. And do you consider that the same?11A. It depends on your perspective.12Q. Well, would it function the same?13A. Would the die function the same?14Q. Yes.15A. No.16Q. So would that be seen as an accurate or17faithful pattern transfer?18A. For the purpose of chip making, no.19Q. Okay. So if your perspective is a lithography20method in for the purpose of semiconductor21manufacturer, would you have transferred the same22pattern to the imaging layer?23A. Again, it I think you're transferring the24Q. And if you transfer the pattern in the wrong	6	Q. It would be different
<ul> <li>Q. It would be a different pattern?</li> <li>A. It would be the pattern rotated by 45 degrees.</li> <li>Q. Okay. And do you consider that the same?</li> <li>A. It depends on your perspective.</li> <li>Q. Well, would it function the same?</li> <li>A. Would the die function the same?</li> <li>Q. Yes.</li> <li>A. No.</li> <li>Q. So would that be seen as an accurate or</li> <li>faithful pattern transfer?</li> <li>A. For the purpose of chip making, no.</li> <li>Q. Okay. So if your perspective is a lithography</li> <li>method in for the purpose of semiconductor</li> <li>manufacturer, would you have transferred the same</li> <li>pattern to the imaging layer?</li> <li>A. Again, it I think you're transferring the</li> <li>pattern in the wrong place.</li> <li>Q. And if you transfer the pattern in the wrong</li> </ul>	7	A that exists on the wafer at that time.
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<ul> <li>Q. So would that be seen as an accurate or</li> <li>faithful pattern transfer?</li> <li>A. For the purpose of chip making, no.</li> <li>Q. Okay. So if your perspective is a lithography</li> <li>method in for the purpose of semiconductor</li> <li>manufacturer, would you have transferred the same</li> <li>pattern to the imaging layer?</li> <li>A. Again, it I think you're transferring the</li> <li>pattern in the wrong place.</li> <li>Q. And if you transfer the pattern in the wrong</li> </ul>	15	A. No.
<ul> <li>faithful pattern transfer?</li> <li>A. For the purpose of chip making, no.</li> <li>Q. Okay. So if your perspective is a lithography</li> <li>method in for the purpose of semiconductor</li> <li>manufacturer, would you have transferred the same</li> <li>pattern to the imaging layer?</li> <li>A. Again, it I think you're transferring the</li> <li>pattern in the wrong place.</li> <li>Q. And if you transfer the pattern in the wrong</li> </ul>	16	Q. So would that be seen as an accurate or
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20 method in for the purpose of semiconductor 21 manufacturer, would you have transferred the same 22 pattern to the imaging layer? 23 A. Again, it I think you're transferring the 24 pattern in the wrong place. 25 Q. And if you transfer the pattern in the wrong	19	Q. Okay. So if your perspective is a lithography
21 manufacturer, would you have transferred the same 22 pattern to the imaging layer? 23 A. Again, it I think you're transferring the 24 pattern in the wrong place. 25 Q. And if you transfer the pattern in the wrong	20	method in for the purpose of semiconductor
22 pattern to the imaging layer? 23 A. Again, it I think you're transferring the 24 pattern in the wrong place. 25 Q. And if you transfer the pattern in the wrong	21	manufacturer, would you have transferred the same
<ul> <li>A. Again, it I think you're transferring the</li> <li>pattern in the wrong place.</li> <li>Q. And if you transfer the pattern in the wrong</li> </ul>	22	pattern to the imaging layer?
24 pattern in the wrong place. 25 Q. And if you transfer the pattern in the wrong	23	A. Again, it I think you're transferring the
Q. And if you transfer the pattern in the wrong	24	pattern in the wrong place.
	25	Q. And if you transfer the pattern in the wrong

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1 place, have you transferred the same pattern? I think it depends on what you mean by the same 2 Α. 3 pattern. 4 0. Okay. With the perspective being using 5 photolithography for semiconductor manufacturing, the 6 placement of the pattern is important, correct? 7 Α. Yes. 8 Q. So if your placement is wrong, have you 9 faithfully transferred the same pattern? 10 Α. A person of ordinary skill in the art in 11 semiconductor manufacturing would know that faithful 12 reproduction of the pattern involves both the shape and size of the pattern and its placement. If the placement 13 14 error was sufficiently large to result in poor 15 functioning of the die, you would say that you have not faithfully patterned or properly patterned the reticle. 16 17 0. What if the placement error was sufficiently large and at the edge of the wafer such that a portion 18 19 of the pattern was on the wafer and a portion of the 20 pattern was off of the wafer? 21 Α. Well, this happens on a regular basis. This 22 is, in fact, typical of manufacturing. The edge die is 23 what we call the die that are close to the edge of the 24 wafer are often printed so that a portion of the die pattern falls off the edge of the wafer. 25 This is

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1	typical.
2	Q. And why is that done?
3	A. For two reasons. I think I mentioned earlier
4	that it is quite common for a reticle pattern to contain
5	more than one die. Two die, four die, six, eight, 12
6	die. Often the size of the reticle that can be imaged
7	at one time on the wafer is much larger than the size of
8	the chip or the die that we are making.
9	Because of that, I can print the entire
10	reticle onto the edge of the wafer and have a few of
11	those die be whole and intact and completely on the
12	wafer while a few others fall off the edge and are not
13	completely intact. Because I want those few die on the
14	wafer, I want to make them and I want to sell them, I go
15	ahead and print even though only a portion of the
16	reticle pattern is landing on top of the resist coated
17	wafer. That's the first reason.
18	The second reason is that the uniformity of
19	all of the processing, etching deposition, as well as
20	lithography processes like developing, are enhanced when
21	the patterns that we're printing completely cover the
22	entire wafer. If I left edged portions unpatterned,
23	that would produce a nonuniformity in pattern density
24	that has negative impacts on many process steps in
25	semiconductor manufacturing.

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1	Q. The die that are near the edge that only get a
2	portion of the pattern transferred to them is that
3	accurate? There's only a portion of the pattern in the
4	reticle that gets transferred to some of the edge die?
5	A. As I said, that's very common.
6	Q. Okay. Is that pattern that is transferred to
7	that portion of the wafer the same as the pattern that
8	is transferred to the die where the entire image is
9	transferred?
10	A. The portion that is transferred is the same,
11	and the portion that is not transferred is obviously
12	different than a portion that is transferred.
13	Q. Right. There's a there are portions that's
14	the same, but not the entire pattern is transferred?
15	A. If I have a die that I cut off at, say,
16	halfway
17	Q. Uh-huh.
18	A and only half of the die is patterned
19	because it's on the edge of the wafer, then the other
20	half is not printed.
21	Q. So and so that if you cut off the die
22	halfway, that would be a different pattern than the full
23	die?
24	A. Well, the pattern that's printed would be the
25	same.

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1	Q. The portion that's printed, but it but as
2	compared to the full pattern in the reticle, it would be
3	different?
4	A. So if by pattern you mean the entire reticle,
5	the entire die pattern, then half of a die pattern is
6	not the same as all of the die pattern.
7	Q. If
8	MR. CUNNING: Well, actually, how long have
9	we been going? I don't have my watch on.
10	MR. HOPEN: About an hour and 20.
11	MR. CUNNING: Okay.
12	Q. You want to take a break?
13	A. That would be great.
14	Q. Okay. I apologize for going a little over an
15	hour.
16	(Break taken from 10:21 a.m. to 10:38 a.m.)
17	Q. Okay. So you had mentioned that often the mask
18	has multiple dies contained on the same mask. Is that
19	correct?
20	A. Yes.
21	Q. And describe for me how that works, how is a
22	multiple die mask used in a typical let's just use
23	step-and-repeat optical projection system.
24	A. It's used in essentially the same way as a
25	simple die mask would be used.

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1	Q. And what is the pattern in the context of claim
2	1, if you're talking about patterning the first imaging
3	layer in accordance with the first pattern to form a
4	first pattern layer having a first feature? Is it all
5	of the different patterns contained in the mask that is
6	the pattern?
7	A. My reading of the 084 pattern patent, excuse
8	me, in claim 1, in particular, doesn't require that the
9	first pattern layer encompass all of one die.
10	Q. Okay. So if you are using a multiple die mask,
11	which of the dies would be the how would you
12	determine what is what the pattern is when you use a
13	multiple die mask to pattern a semiconductor wafer?
14	A. The pattern used to form the first pattern
15	layer is silent on the issue of how big that pattern is,
16	whether it encompasses an entire die or even multiple
17	die. It could be that this first pattern layer is
18	multiple die with kerfs in between and test patterns in
19	the kerf, or it could be a portion of one die. For
20	example, we might be printing not an entire die, even,
21	but just, say, the memory array of a flash memory device
22	where, instead of printing the a layer of the entire
23	die, we only print the patterns associated with a
24	portion of that die. That's certainly possible as well.
25	Q. How do you print the patterns only the

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1 patterns associated with a portion of the die? 2 Α. That would depend on the lithography approach 3 chosen. Using a mask, how would you do that? Would 4 Q. you -- would the mask only have that portion in it, or 5 could the mask have additional portions? 6 7 Α. In general, the mask would only have that 8 portion in it. There is one variation on that being all 9 projection lithography tools include what are called 10 reticle blades. 11 Ο. Uh-huh. 12 Α. These are blades that are positioned very near 13 the photomask, and they're movable so that portions of 14 the photomask can be covered up by the blade and not 15 projected onto the wafer. 16 Q. So if you were to blade off a portion of the photomask and project a portion of the photomask onto 17 the wafer and then blade off a different portion of the 18 19 photomask and project a different portion onto the 20 wafer, would you have projected the same pattern onto 21 the wafer or different patterns? 22 Α. If you were to use reticle blades to block a 23 portion of the photomask and reveal a separate portion, that would result in a pattern represented by the 24 25 portion of the photomask that was not covered by the

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1	reticle blades. If I then move the reticle blades so
2	that some other portion of the reticle that was
3	previously covered is now revealed, when I project that
4	portion, it would be a different pattern.
5	Q. So what's important in determining the pattern
6	is the exposure that takes place on the wafer?
7	A. I'd say that's a fair statement.
8	Q. In other words, the mask doesn't necessarily
9	define the pattern?
10	A. When you're projecting an image onto the wafer,
11	the mask certainly does define the pattern.
12	Q. Well, but is it it's a part of it, but is it
13	also the portion of the mask that you would expose?
14	A. It is possible to only use a portion of the
15	mask when projecting that mask onto the wafer, though,
16	in typical usage, the entire mask is being used to
17	project that pattern onto the wafer.
18	Q. And in the case of blading off portions of the
19	mask, a single mask can produce different patterns?
20	A. I think, as we discussed a few moments ago, if
21	you blade off one portion and project that pattern onto
22	the wafer, then in a second lithography operation move
23	the blades to reveal a completely different portion that
24	was covered by the blades, that new portion would be a
25	different pattern.

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1	Q. And that would be an example of using a single
2	mask to create two different patterns?
3	A. Yes.
4	Q. And that was that something that was
5	understood and by persons of ordinary skill in the
6	art at the time of the filing of the 084 patent
7	A. Yes.
8	Q that technique?
9	A. I wouldn't call it a technique. That's not
10	what's normally done. It's possible to do.
11	Q. Also in the case of a single mask that has
12	multiple dies on the mask, you could use that mask to
13	create multiple different patterns?
14	A. In what sense?
15	Q. Well, if you expose how many die are
16	typically included in a multi-die mask?
17	A. Anywhere from 2 to 36.
18	Q. Okay. And each one of those could be a
19	different pattern, correct?
20	A. I don't know what you mean by a different
21	pattern.
22	Q. Each die included on the mask could be
23	patterned differently?
24	A. Typically, all of the die are identical. There
25	is, however, the possibility of having different die

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1	that on the same mask.
2	Q. Okay. And then in that scenario, you could use
3	a single mask to create different patterns, depending on
4	which die you expose?
5	A. Well, generally, you would expose the entire
6	mask at one time. The point of having multiple die on
7	the mask is to expose them all at once.
8	Q. Are there multi-die masks that are used where
9	you have different layers included on the same mask?
10	A. It is possible.
11	Q. In that scenario, do you expose all of the
12	masks at the same time?
13	A. No.
14	Q. And how does the exposure take place in the
15	scenario where you have different layers represented
16	on multiple different layers on the mask?
17	A. Well, normally, we use the phrase a "different
18	layer" to represent patterning that occurs at a
19	different step in the fabrication process.
20	Q. Uh-huh.
21	A. If I had two layers, let's say, on one
22	photomask and those two layers represented different
23	places in the overall sequence of processing steps, say
24	one of them was the polygate layer and another one was
25	the metal one layer, I would, again, using the reticle

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1	blades, cover up the metal one layer when I was printing
2	the polygate layer, and then I would use the reticle
3	blades again to cover up the polygate layer and open up
4	the metal one layer when I was performing that printing
5	operation.
6	Q. And that technique was something that was
7	within the skill of a person of ordinary skill in the
8	art at the time of the 084 patent?
9	A. Yes.
10	Q. If you look at figure 2 of the 084 patent do
11	you have that in front of you?
12	A. Yes.
13	Q. And what is your understanding of what is
14	represented in figure 2 of the 084 patent?
15	A. Just to make sure I get everything right, I'll
16	refer back to the specification that discusses figure 2.
17	Q. Okay.
18	A. So layer 220 is an imaging layer, and this is
19	the first imaging layer that's formed over the
20	semiconductor wafer. Layer 210 is some other material
21	that might be already deposited onto the semiconductor
22	wafer on 200. It says layer 210 may include a
23	dielectric, for example. Then it says imaging layer
24	220 I'm now at column 3, line 57 imaging layer 220
25	may be exposed to radiation through a first mask having

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1	opaque feature 222 and having clear features 221 and
2	223.
3	So at the top of figure 2, we have the
4	first mask, and this is where we are forming the first
5	pattern in the first imaging layer.
6	Q. Okay. So in between the first mask and the
7	first imaging layer 220, there may be additional
8	material? For example, with a projection system there
9	are lenses in there; is that correct?
10	A. That's correct.
11	Q. Okay. And the arrows in figure 2 represent the
12	radiation source?
13	A. That's correct.
14	Q. So what's important is the because the
15	lenses are going to change the magnification of the
16	pattern, it's the pattern as it reaches the imaging
17	layer?
18	A. That's what's important to the imaging step,
19	yes.
20	Q. This first mask that's depicted in figure 2, is
21	it your understanding that this is a cross-sectional
22	diagram, correct?
23	A. Yes.
24	Q. Okay. So is it your understanding that the
25	mask can continue to the left and the right, there may

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1	be other features that are not depicted?
2	A. There are two ways to interpret such a diagram
3	in my experience in looking at patents, but I think the
4	way that is most appropriate here is to consider that
5	the mask could have features to the left or to the right
6	that are not being depicted here.
7	Q. And the sizes of the features that are depicted
8	here, is that limiting? In other words, feature 223,
9	for example, is it your understanding that the 084
10	patent in featuring feature 223 is limited to
11	patterning rectangular shapes?
12	A. No.
13	Q. Is it limited to patterning shapes that operate
14	in a straight line?
15	A. No.
16	Q. Okay. Are the sizes of the features depicted
17	in is this to scale? Are these sizes important?
18	A. Well, there's two answers to that question.
19	Those two questions, actually
20	Q. Okay.
21	A that you gave me. The first answer is, in
22	general, we don't interpret figures in a patent as being
23	to scale, and so I do not and have not interpreted these
24	figures as being necessarily to scale. Second, there is
25	a requirement a size requirement in, for example,

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1	claim 1, but that has to do with the combined first and
2	second pattern layer that form a single pattern layer,
3	and the requirement is that the first and second
4	features are formed relatively closer to one another
5	than is possible through a single exposure to radiation.
6	So that is a size requirement and restriction in
7	practicing claim 1.
8	Q. Okay. Turn to Figure 4. And what's your
9	understanding of what's being depicted in figure 4?
10	A. This figure is depicting forming a second
11	pattern with a second mask in a second imaging layer.
12	Q. How do you know it's a second mask?
13	A. Let's see, it begins describing figure 4 in
14	column 5, line 57. But in column 6, line 26 oh,
15	actually before up in 20 or 18. Imaging layer 240
16	may be exposed to radiation through a second mask having
17	opaque features 242 and 244 and clear features 241, 243,
18	and 245. Those numbers appear on the mask in figure 4,
19	so the patent refers to that mask as the second mask.
20	Q. It says it may be exposed through a second
21	mask, correct?
22	A. This is describing an embodiment that uses a
23	mask.
24	Q. Uh-huh. But it doesn't say that I mean,
25	that's not restrictive language, correct?

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1	A. The claim is broader than simply through the
2	use of photomasks.
3	Q. Okay. How do you know that the you stated
4	that the mask used in figure 2 may have additional
5	features to the left and the right in the mask. Isn't
6	it possible that the mask depicted in figure 4 could be
7	the same mask where you've bladed off that first
8	portion?
9	A. I don't know what you mean.
10	Q. If I were to have one mask that had features
11	221, 222, and 223 in it and features 241 through 245,
12	couldn't I pattern the first imaging layer as depicted
13	in figure 2 by blading off features 241 to 245 and
14	exposing features 221 to 223?
15	A. So I think I understand your question. We
16	discussed earlier the possibility of having two layers,
17	one layer for the polygate layer, for example, and one
18	layer for metal one on the same mask. Would have two
19	patterns on one photomask.
20	Q. Uh-huh.
21	A. In this double patterning scheme, as it's
22	described in the 084 pattern excuse me, patent, it
23	would also be possible to have two patterns on one mask.
24	You'd have to first pattern that was used to image in
25	the first imaging layer on that mask and then separately

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1	a second pattern that represents a the patterns
2	required for patterning the second imaging layer to form
3	the second pattern. It would be possible to have both
4	of those two patterns on one mask in the same way and
5	then bladed off, as we described before.
6	Q. And that would be covered by the claim of the
7	084 patent?
8	A. Yes.
9	Q. By claim 1 of the 084 patent?
10	A. Yes. Assuming, of course, that it met all the
11	other limitations.
12	Q. Let's talk about the go back to the direct
13	write exposure. So if I wanted to program I'm sorry,
14	if I want to pattern the first imaging layer using a
15	direct write apparatus, I believe you said you program a
16	series of coordinates into a database to tell the laser
17	or the E-beam writing tool where to turn on and turn
18	off?
19	A. This would not be done manually, but a GDS file
20	that contains the original database of all the patterns,
21	what shapes and sizes you want them to be and the
22	locations you want them to be on the wafer, would form
23	the input database which is then converted into the data
24	that the writer would need to write those patterns on
25	the wafer.

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1	Q. Okay. That's going to have well, is it
2	correct, then, that that input program would give the X,
3	Y coordinates for where the E-beam let's use E-beam
4	as an example the E-beam apparatus is to scan to on
5	the wafer?
6	A. Yes.
7	Q. And then a programming instruction on once
8	it's in the proper X, Y plane of when to turn on and
9	when to turn off?
10	A. Yes.
11	Q. If I have a pattern of repeating features and I
12	want to now do a second pattern where I shift all of the
13	features some distance to the left, could I program the
14	E-beam apparatus and just give it an instruction saying,
15	you know, add one micron to every X coordinate?
16	A. I am unfamiliar with the software capabilities,
17	whether it would allow that. In general operation, you
18	would supply a different database, different GDS file
19	with all of the locations for the second set of patterns
20	that you would like patterned programmed into that
21	database.
22	Q. If the second set of pattern is if the only
23	difference is that every feature has been shifted some
24	specified distance to the left, is it the same pattern
25	or a different pattern?

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1	A. In what sense?
2	Q. If I want to pattern a first imaging layer with
3	a set of transistors and then I want to pattern a second
4	imaging layer where I put additional transistors into
5	the spaces between the transistors from the first
6	imaging layer and I do that with an E-beam apparatus,
7	the only difference in the second pattern is I shifted
8	everything some specified distance to the left, is that
9	a different pattern or is it the same pattern?
10	A. If you consider the way a lithography excuse
11	me, a lithographer considers patterns as they're placed
12	onto a wafer
13	Q. Uh-huh.
14	A a pattern that's placed in a different
15	position is a different pattern.
16	Q. Okay. If I have two masks that have an
17	identical pattern in them or two copies of the same
18	mask, is are the patterns between the two masks the
19	same or different?
20	A. I think your hypothetical describes them as
21	identical, so identical would be the same.
22	Q. If I were to let's take Jinbo, for example.
23	My understanding of your criticism of Jinbo is that, in
24	your view, Jinbo uses a single mask between the two
25	patterning steps. Is that accurate?

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1	MR. HOPEN: Objection to form.
2	A. Jinbo discloses only a single mask.
3	Q. Okay. If Jinbo had used a second mask with the
4	same pattern, then shifted the wafer stage, would that
5	be would that cure the defect in your view of Jinbo?
6	A. Jinbo very purposefully doesn't do that, and
. 7	with a very valid and useful reason, and that is the
8	accuracy with which the patterning can be carried out is
9	much higher if the photomask is loaded and aligned to
10	the stepper for the first patterning step and then is
11	not changed or moved for the second patterning step.
12	This produces a much preferred result of higher accuracy
13	of the combined pattern. That benefit is lost if one
14	were to take out the first photomask and then replace it
15	with a second photomask.
16	Even though you are attempting in real
17	life attempting to re-create the first photomask with
18	the second photomask, that re-creation will never be
19	perfect, right. Your hypothetical earlier said they
20	were identical, but it is impossible to make two masks
21	that are identical. I'm not being nitpicky, either.
22	These are real concerns that lithographers spend a lot
23	of time worrying about.
24	Q. Uh-huh.
25	A. Errors in the manufacture of the photomask is a

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1	significant portion of the overall sets of errors that
2	result in the printed wafers. Jinbo describes a method
3	of printing two patterns that are closer to each other
4	than could be obtained with a single patterning step by
5	very specifically using one mask and keeping it in the
6	scanner throughout both the first and the second
7	exposures resulting in a more accurate pattern than
8	would be obtained through the use of two masks.
9	Q. Okay. So if I had a mask that was designed to
10	be identical to the first mask within the tolerances
11	that are allowed in the industry and I perform the
12	method of Jinbo, but instead of using the same mask, I
13	swapped out the identical mask, would that be the method
14	that's described in the 084 patent?
15	A. You haven't given me a complete description of
16	the method to know whether or not it practices all the
17	elements of the claim, so I can't answer the question.
18	Q. Okay. The criticism that I understand that
19	you've raised with Jinbo is that Jinbo uses a single
20	mask. That's your interpretation of Jinbo, correct?
21	A. Correct.
22	Q. Okay. Because it uses a single mask, it's your
23	argument that, in the second patterning step, Jinbo has
24	not patterned with a second pattern, it's the same as
25	the first pattern?

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1	A. That's correct.
2	Q. Okay. So following the same steps of Jinbo but
3	prior to doing the second patterning step, removing the
4	first mask and inserting an identical mask to the
5	tolerances that can be created as normally accepted in
6	the industry and using that second mask for the second
7	patterning step, would that be the method described in
8	the 084 patent?
9	A. Well, Jinbo includes shifting the wafer.
10	Q. Right.
11	A. And that's not discussed at all in the 084
12	patent.
13	Q. Okay. The does changing the masks between
14	the patterning steps affect your opinion of whether or
15	not Jinbo discloses the method of the 084 patent?
16	A. Well, the 084, in my opinion, requires a second
17	pattern
18	Q. Uh-huh.
19	A that is not identical to the first patent.
20	Q. Okay.
21	A. So if you had physically two masks that were
22	otherwise identical and practiced what is described in
23	Jinbo, you would still not have a second pattern. You
24	would have the identical first pattern but just used on
25	a second mask.

Α. The result would be a method that neither gave the benefits of Jinbo nor the benefits of the 084 patent. If you had a second mask, so rather than 0. shifting the wafer stage, you create a second mask and you shift the features in the mask by the same amount that Jinbo specifies to shift the wafer stage, is that a different pattern? Α. Yes. You said that the 084 patent doesn't teach 0. shifting the wafer stage; is that correct? It doesn't teach shifting the wafer stage Α. between the first and the second patterning steps. Ο. Does it teach alignment of the photomask with the optical equipment? Α. No. Does it teach alignment of the wafer stage with 0. the optical equipment and the reticle? These would be operations that were standard in Α. the industry at the time of the 084 patent. Q. Does it provide a description of how to implement the 084 patent using direct write exposure? Α. None of the embodiments that are explained in

25 some detail involve direct write.

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Q.

Okay.

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1	Q. Do you believe that the 084 patent would enable
2	a person of ordinary skill in the art to practice its
3	method using a direct write exposure?
4	A. Yes.
5	Q. Do you think it would enable a person of
6	ordinary skill in the art to practice the method of the
7	084 patent using radiation sources other than
8	traditional photolithographic radiation sources?
9	A. I would suppose it would depend on what source
10	you're talking about.
11	Q. Ultraviolet light. Does the 084 patent provide
12	enough information to teach a person of ordinary skill
13	in the art how to practice the invention using an
14	ultraviolet light radiation source?
15	A. Yes.
16	Q. Same question for x-ray radiation.
17	A. Yes.
18	Q. And for E-beam radiation?
19	A. Yes.
20	Q. For vacuum UV radiation?
21	A. Yes.
22	Q. And for ion beam radiation?
23	A. Yes.
24	Q. Turn back to figure 2 for a second. Actually,
25	I just want to what are the oh, never mind.

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1	Going back to figure 2, so we discussed the
2	possibility that there would be additional equipment in
3	between the reticle and the first imaging layer,
4	correct?
5	A. Yes.
6	Q. And that it's actually it's likely that
7	there would be magnification optics, for example?
8	A. Yes.
9	Q. And in the magnification optics, the arrows
10	that are depicted would not necessarily be perpendicular
11	to the surface of the wafer?
12	A. I interpret the arrows being depicted as doing
13	two things. One, showing the regions of the mask that
14	are transparent, so the arrows are propagating through
15	the mask and that represents the transparent area. And
16	then the arrows point to the portions of the resist
17	layer 220 that nominally will be exposed to light. I
18	don't think that the arrows represent the true direction
19	that the light is traveling in a physical sense.
20	Q. The light, in fact, is diffracted as it passes
21	through the reticle; is that correct?
22	A. That is correct.
23	Q. And so the important what's important for
24	the pattern transfer is the radiation pattern as it
25	reaches the imaging layer, correct?

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1	A. Only the radiation pattern that reaches the
2	imaging layer is at all effective at causing imaging.
3	Q. Okay. Let's turn back to your declaration in
4	paragraph 27 on page 6.
5	A. Yes.
6	Q. Can you read the first sentence there.
7	A. The 084 patent explicitly discloses that the
8	invention includes a technique of, quote, patterning by
9	exposure to radiation in accordance with two separate
10	patterns, end quote.
11	Q. And you're quoting from the abstract of the 084
12	patent?
13	A. That's correct.
14	Q. Now, you say that the 084 patent includes a
15	technique of patterning by exposure to two separate
16	patterns, correct? You didn't say it was limited to.
17	You used the word "include."
18	A. I don't believe I used the word "include." Oh,
19	sorry. Yes, explicitly discloses that the invention
20	includes a technique of yes, that's correct.
21	Q. Okay. Well, is it your view that the 084
22	patent is limited to a technique patterning by exposure
23	to radiation in accordance with two separate patterns?
24	A. I believe the claim 1 is so limited.
25	Q. And that the and that's why you quoted the

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1	abstract, you believe it supports that limitation?
2	A. Yes.
3	Q. Okay. Can you read the full quote including
4	the language that you omitted from the abstract?
5	A. For another embodiment, a single imaging layer
6	is patterned by exposure to radiation in accordance with
7	two separate patterns.
8	Q. So the language "for another embodiment,"
9	doesn't that suggest that the previously described
10	embodiments are not so limited?
11	A. If all we read was an abstract, I can see how
12	you might conclude that, but this sentence, when read in
13	conjunction with the rest of the specification, makes
14	clear that the limitation of two separate patterns
15	applies to the first embodiment described earlier in the
16	abstract.
17	Q. So you think that was a fair characterization
18	of the abstract by omitting the "for another embodiment"
19	language?
20	A. Yes.
21	Q. And you say in the your declaration you
22	continue that the 084 patent consistently states that
23	the first pattern is embodied in the first mask and the
24	second pattern is embodied in the second mask. You see
25	that?

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1	A. Yes.
2	Q. But the wouldn't you agree that the 084
3	patent covers maskless lithography systems?
4	MR. HOPEN: Objection to form.
5	A. Yes, it does.
6	Q. So is it then inaccurate to say that the O4
7	084 patent consistently states that the first pattern is
8	embodied in the first mask and the second pattern is
9	embodied in the second mask?
10	A. No.
11	Q. And how so?
12	A. I didn't imply I didn't mean to imply in the
13	statement that the embodiment of using photomasks to
14	create the pattern is the only embodiment described and
15	claimed in the 084 patent, but in embodiments that use
16	photomasks inconsistently describes that the first
17	pattern is obtained from the first mask and the second
18	pattern is obtained from the second mask.
19	Q. You cite to figures 7 well, 2, 4, 7, 9, 13,
20	and 15 in the accompanying descriptions?
21	A. Yes.
22	Q. Are you aware that figure the embodiment
23	corresponding to figures 7, 9, 13, and 15 are not
24	claimed in the 084 patent?
25	A. I don't recall.

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1	Q. Did you know that those figures were prosecuted
2	and claimed in a divisional application from the 084
3	patent?
4	A. I do recall something like that, yes.
5	Q. Did you review the divisional patent in forming
6	your analysis?
7	A. I at very early in my work, I did review
8	that divisional patent, but not specifically when
9	forming my opinions and putting them down in this
10	declaration.
11	Q. Did you review the file history for the
12	divisional application?
13	A. I reviewed the file history for the 084. I
14	don't recall whether that included the file history for
15	the divisional.
16	Q. Are you aware that the patentee attempted to
17	claim a second pattern different from the first pattern
18	in the divisional application?
19	A. I don't recall.
20	Q. So you didn't consider that they attempted to
21	add that claim amendment and then withdrew the claim
22	amendment?
23	MR. HOPEN: Objection, form.
24	A. I do recall something about that, but I can't
25	remember the details.

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1	Q. If you would turn to a question I asked you a
2	little bit ago about programming a pattern in for a
3	direct write lithography system and I asked if you could
4	create a second pattern by providing an instruction that
5	told the tool to just index every feature a specified
6	distance in one direction. Do you recall that question?
7	A. I do.
8	Q. And I believe your answer was you were not
9	aware of whether or not the system tools had that
10	capability; is that correct?
11	A. That's correct.
12	Q. So assuming I'm going to ask you to make an
13	assumption and assume that the tool does have that
14	capability. Is it is there a distinction for you
15	between programming individually all of the features
16	into the tool for the second pattern versus telling the
17	tool to just index every feature some distance to the
18	left?
19	A. So I presume that you're asking me this
20	question in the context of our discussion of the first
21	pattern and the second pattern.
22	Q. Uh-huh.
23	A. So I said earlier that, if the second pattern
24	had the same shape and size of the first pattern but was
25	located in a different location, that that would be a

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1	separate pattern, a second pattern that is different
2	from the first pattern. If I loaded in my database a
3	set of information about where the patterns go and then
4	through programming modified the location of all those
5	patterns by indexing all of them a certain amount, I
6	will have created a second pattern that is different
7	from the first pattern.
8	Q. Okay. I'll give you an exhibit that's
9	previously been marked as TSMC-1004. Do you recognize
10	this?
11	A. Yes.
12	Q. And what is it?
13	A. It is a Japanese patent application that we
14	have referred to as Jinbo.
15	MR. CUNNING: I apologize. Can we take a
16	brief five-minute break?
17	MR. HOPEN: Sure.
18	(Break taken from 11:26 a.m. to 11:39 a.m.)
19	Q. Okay. Do you have Jinbo in front of you still?
20	A. I do.
21	Q. Okay. Do you agree that Jinbo provides a
22	disclosure that would enable a person of ordinary skill
23	in the art to create a composite resist pattern with a
24	feature spacing that's beyond the resolution limit of
25	the photolithography equipment used?
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1	A. I have not studied Jinbo to sufficient detail
2	to make a legal determination of enablement.
3	Q. Okay. Do you think a person of ordinary skill
4	in the art could replicate the method disclosed in
5	Jinbo, the specific embodiment?
6	A. I have not studied Jinbo in any detail with
7	respect to forming an opinion on that. The best I could
8	offer would be an off-the-cuff opinion, not a reasoned
9	opinion based on analysis.
10	Q. And what's your off-the-cuff opinion?
11	A. Yes.
12	Q. Do you agree that Jinbo achieves the same
13	result of the 084 patent of creating a final resist
14	pattern having dimensions beyond the resolution limits
15	of the lithographic equipment?
16	A. Yes. The same result in that sense.
17	Q. And looking at page 3 of TSMC-1004
18	A. Oh, I'm sorry.
19	Q. Yeah, that's Jinbo. I'm sorry.
20	A. Page 3?
21	Q. Uh-huh.
22	A. Okay.
23	Q. Under the section at the bottom headed
24	"operation," it reads, with construction with the
25	construction of this invention, a final a first

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1	resist pattern and a second resist pattern are arranged
2	in a prescribed relationship on a substrate. For
3	example, a space of the second resist pattern is
4	arranged in an area corresponding to a space section of
5	the first resist pattern on the substrate for forming
6	resist patterns, and the patterns can become the final
7	resist pattern. Do you see that?
8	A. Yes.
9	Q. Jinbo refers to the patterns that are created
10	as a first resist pattern and a second resist pattern,
11	correct?
12	A. Yes.
13	Q. And Jinbo accomplishes creating a first resist
14	pattern and a second resist pattern, in your view, using
15	a single mask?
16	A. Yes.
17	Q. So it is possible to create a first resist
18	pattern and a second resist pattern where the features
19	are located in different places using a single mask?
20	A. Yes.
21	Q. Accepting your premise that Jinbo teaches the
22	use of a single mask in between the first and second
23	patterning steps, would a person of ordinary skill in
24	the art at the time that the 084 patent was filed have
25	been able to replicate the method taught in Jinbo but

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1	using a second mask where they patterned the features in
2	the mask shifted a distance to the left rather than
3	moving the wafer stage?
4	A. That's a difficult question. That is not
5	necessarily obvious. Jinbo achieves results, for
6	example, as shown in Figure 1(E). And my copy is a
7	little bit blurry, but if I'm reading this correctly, we
.8	have a feature that's labeled as 15a.
9	Q. Uh-huh.
10	A. And then next to it, a feature that's labeled
11	as 13b. Am I reading that correctly?
12	Q. I believe that's correct, yeah.
13	A. And then
14	Q. I'm following you.
15	A. And then next to it a feature that's labeled as
16	15a. One of the major difficulties in making a double
17	patterning process like this work, either the double
18	patterning process that's described in Jinbo or the
19	double patterning process that's described in the 084 is
20	the fact that positional errors in the printing of
21	pattern 13b feature 13b, excuse me, results in
22	dimensional errors in the gap between 13b and 15a.
23	So we see to the right of 13b a gap between
24	that feature and the feature printed in the first resist
25	labeled as 15a and a gap to the left between feature 13b

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1	and feature 15a. Any positional errors in the printing
2	of feature 13b say it's moved a little bit to the right
3	would make the right side gap too small and the left
4	side gap too large.
5	Jinbo has a clever solution to that
6	problem. That solution is to use a single photomask
7	mounted in the projection lithography tool and aligned
8	and then not moved between the first and the second
9	patterning steps. Instead, the position of the stage is
10	used to accurately position the wafer underneath the
11	first pattern to create the second resist pattern. This
12	solution to a difficult problem is not what happens in
13	the 084 patent.
14	The 084 patent uses separate masks, two
15	separate patterns and suffers. As a result, the more
16	difficult problem of getting the second pattern properly
17	positioned underneath the first pattern, though it adds
18	the benefits of extra flexibility in having different
19	patterns possible than is possible in Jinbo.
20	Q. In Jinbo, between the first and the second
21	pattern, the wafer is removed from the wafer stage,
22	correct?
23	A. Yes.
24	Q. So the wafer still has to be placed back onto
25	the wafer stage and aligned with the exposure apparatus

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1	and the reticle in between the two patterning steps?
2	A. Yes.
3	Q. Is that correct?
4	A. That's correct.
5	Q. In the 084 patent, you describe that it
6	confronts and solves a more difficult problem of
7	aligning the second pattern over the first pattern when
8	it uses a second mask. Is that a fair characterization
9	of your testimony?
10	A. No, I don't believe I said that it confronts
11	and solves that problem.
12	Q. Okay. Well, what was it that you said?
13	A. The method described in the 084 patent will
14	suffer from greater errors in, for example, the
15	dimensional gap between a first pattern placed in close
16	proximity to a second pattern as compared to Jinbo.
17	Q. Does the 084 patent anywhere in its
18	specification address those dimensional error the
19	issue of the errors that are being countered by placing
20	the second pattern over the first pattern?
21	A. No.
22	Q. Does it tell a person of ordinary skill in the
23	art how to solve the problem of the greater dimensional
24	errors that would be encountered by placing a second
25	pattern over the first pattern?

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1	A. A person of ordinary skill in the art was well
2	aware and was well established standard practice in
3	the industry that the what we call overlay errors,
4	the positioning accuracy error of one layer relative to
5	another layer, influences the closest distance that
6	you're allowed to put two layers to each other. This
7	was ubiquitous practice in understanding in the
8	semiconductor industry in the 1994 time frame.
9	We had what we called design rules that
10	said you're not allowed to print one pattern any closer
11	than X or Y or some number to a previously printed
12	pattern because of the inaccuracies in overlay. This
13	method of taking into account the inevitable overlay
14	errors was a standard part of the practice of
15	lithography for decades before the 084 patent was filed
16	and was part of the knowledge of a person of ordinary
17	skill in the art at the time of the 084 patent.
18	Q. And that knowledge was separate and apart from
19	the 084 patent?
20	A. Yes.
21	Q. The 084 patent doesn't make any contribute
22	contribution to that knowledge?
23	A. That's correct.
24	Q. And in the 1994 time frame, are you do you
25	recall what you mentioned that there were design

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1	rules that would specify, you know, how close you could
2	print one feature to another feature. Do you know what
3	those distances were?
4	A. No.
5	Q. So returning back to my question about a person
6	of ordinary skill in the art who has Jinbo in front of
7	them and decides, instead of shifting the wafer stage, I
8	want to attempt to create a second mask and I'll move
9	all the features over by a distance .3 microns in the
10	mask. Would the person of ordinary skill in the art at
11	the time have been able to do that?
12	A. Without going too far afield of your question,
13	I think you're referring to, when you said a shift of
14	.3 microns, a particular place in the Jinbo
15	specification that I think is a typographical error.
16	Q6 microns?
17	A. Yes.
18	Q. Okay.
19	A. So I think we all understand that it's supposed
20	to be .6 microns
21	Q. Uh-huh.
22	A in the Jinbo patent, and that's the proper
23	distance that the wafer should be shifted. So let me
24	try to repeat your question
25	Q. Uh-huh.

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1	A and see if I understood it. You asked would
2	it be within the capabilities of a person of ordinary
3	skill in the art to create a second mask that was
4	otherwise identical to the first mask, though its
5	patterns were shifted by .06 microns?
6	Q. I think it's .6 microns, isn't it?
7	A. Oh, point yes, of course, .6 microns.
8	Q. Yes.
9	A. And my answer is, yes, I believe that would be
10	within the scale of a person of ordinary skill in the
11	art.
12	Q. And a person at the time, a person of
13	ordinary skill in the art would have a reasonable
14	expectation of success in designing that mask and
15	practicing the method disclosed in Jinbo?
16	A. That is less clear. Because the actual
17	application of the hypothetical that you just stated, I
18	create a second mask that has not only the same features
19	but are not only shifted by an amount .6 microns would
20	result in a larger distribution, a larger range of
21	errors in the gaps between, say, Feature 13b and feature
22	15a, as shown in Figure 1(E) of Jinbo.
23	This distribution could easily have been,
24	at the time of Jinbo, too large for the application that
25	Jinbo was the result that Jinbo is trying to achieve.

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1	Since Jinbo doesn't teach that method, it's unclear if
2	it was achievable.
3	Q. At the time that the 084 patent was filed,
4	would that have been within the scale of a person of
5	ordinary skill in the art?
6	A. Would what have been in the scale?
7	Q. Taking the method described in Jinbo and
8	instead of shifting the wafer stage by .6 microns in the
9	X direction, creating a second mask, shifting the
10	features in the mask .6 microns, and creating a second
11	pattern with the features placed into the spaces between
12	the features in the first pattern?
13	MR. HOPEN: Objection to form.
14	A. Well, I think I testified earlier that it was
15	within the scale of a person of ordinary skill in the
16	art to be able to create that mask and use it. What I
17	was unclear about was whether or not, especially for the
18	dimension specific dimensions described in Jinbo, the
19	result would be adequate. That, I'm not sure of.
20	At the time of the 084 patent, because the
21	084 patent invents the use of a second mask with a
22	second pattern, it was within the capabilities of a
23	person of ordinary skill in the art at that time since
24	it was invented at that time.
25	Q. Okay. So just prior to the filing of the 084

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1	patent, without the benefit of the disclosure of the 084
2	patent, would a person of ordinary skill in the art have
3	been able to take the method described in Jinbo and
4	create a second mask with the feature shifted at
5	.6 microns and place the features within the spaces
6	created by the first patterning layer?
7	MR. HOPEN: Objection, form.
8	A. I suspect it would have been possible just
9	before the filing of the 084 patent to modify Jinbo in
10	such a way.
11	Q. Okay. And the difficulties that you described
12	regarding differences in positional placement tolerances
13	for overlay of a second mask over the layer created by a
14	first mask, the 084 patent provides no information in
15	one skill one of skill in the art as to how to solve
16	those problems?
17	A. That's correct.
18	Q. Were would there be would a person of
19	ordinary skill in the art have a reason to create two
20	separate masks with different patterns rather than a
21	single mask shifted?
22	A. Yes.
23	Q. And what would be some of the reasons that a
24	person of ordinary skill in the art would have desired
25	to create a second mask with a separate pattern rather

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1	than a use the first mask shifted in the X direction,
2	for example?
3	A. Well, first and foremost, the shift of the
4	wafer, as described in Jinbo, occurs and can occur only
5	in one direction. Thus, the benefit of getting patterns
6	closer to each other as possible in a single patterning
7	step is only achieved in one direction. It is in the
8	direction of the shift. Our wafers and devices and
9	patterns that we're trying to print are, in fact, too
10	dimensional, and we would get no benefit in the other
11	direction, the direction not shifted when applying the
12	teachings of Jinbo.
13	The 084, on the other hand, by using the
14	second mask, would enable us to get features closer to
15	each other that could be achieved with a single
16	patterning step in both the X direction and the Y
17	direction. That's the first major advantage. The
18	second limitation of Jinbo that is overcome by the 084
19	patent is that all of the patterns being printed in the
20	second photoresist must be identical to the first
21	pattern being printed and the wafer is shifted by one
22	amount. This amount is optimal for patterns that have a
23	single pitch.
24	Now, there are certainly some applications
25	where that is exactly what is needed and that is all

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1	that is needed, so Jinbo has value.
2	Q. All right.
3	A. But the 084, by enabling a second mask, also
4	allows a variety of pitches to be used and to benefit
5	from the invention.
6	Q. At the time of Jinbo, was it standard in the
7	industry to have resist patterns that did not consist
8	merely of repeating features with a single pitch?
9	A. There are many devices and device layers which
10	predominantly are made up of single layer with single
11	features going in one direction with one pitch, in
12	particular, the memory array of a memory cell. However,
13	there were even more examples of device layers that had
14	more complicated features than just lines and spaces of
15	one pitch and one direction.
16	Q. And that was true at the time that Jinbo was
17	filed?
18	A. Yes.
19	Q. Was it in manufacturing a chip even back in
20	1994, was it well known and conventional to use
21	different masks in the same lithography tool at
22	different times?
23	A. Yes.
24.	Q. And would you agree that, even in Jinbo, there
25	is a second patterning step, a second exposure of the
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<ul> <li>patterning step?</li> <li>A. Yes.</li> <li>Q. Have you reviewed the claim construction order</li> <li>that the district court issued in the companion</li> <li>litigation that DSS has filed against TSMC and Samsung</li> <li>related to the 084 patent?</li> <li>A. I have briefly read it. I haven't reviewed it</li> <li>in any detail.</li> <li>MR. CUNNING: Can we mark as TSMC-1016 <ul> <li>MR. LYTVYN: Do you have an extra copy for</li> </ul> </li> <li>us?</li> <li>MR. CUNNING: yes claim construction</li> <li>memorandum and order.</li> <li>(Exhibit TSMC-1016 marked.)</li> <li>Q. Do you recognize TSMC1-1016? I'm sorry,</li> <li>TSMC-1016.</li> <li>A. It looks familiar.</li> <li>Q. Okay. And this is the claim construction</li> <li>memorandum order issued in DSS Technology, Inc. vs.</li> <li>Taiwan Semiconductor Manufacturing Company, Limited, et</li> <li>al., Civil Action No. 2:14-CV-199; is that correct?</li> <li>A. Yes.</li> <li>Q. Okay. Sorry. Give me a second.</li> <li>MR. CUNNING: Why don't we do this, give</li> </ul>	1	resist, that occurs later in time than the first
<ul> <li>A. Yes.</li> <li>Q. Have you reviewed the claim construction order</li> <li>that the district court issued in the companion</li> <li>litigation that DSS has filed against TSMC and Samsung</li> <li>related to the 084 patent?</li> <li>A. I have briefly read it. I haven't reviewed it</li> <li>in any detail.</li> <li>MR. CUNNING: Can we mark as TSMC-1016</li> <li>MR. LYTVYN: Do you have an extra copy for</li> <li>us?</li> <li>MR. CUNNING: yes claim construction</li> <li>memorandum and order.</li> <li>(Exhibit TSMC-1016 marked.)</li> <li>Q. Do you recognize TSMC1-1016? I'm sorry,</li> <li>TSMC-1016.</li> <li>A. It looks familiar.</li> <li>Q. Okay. And this is the claim construction</li> <li>memorandum order issued in DSS Technology, Inc. vs.</li> <li>Taiwan Semiconductor Manufacturing Company, Limited, et</li> <li>al., Civil Action No. 2:14-CV-199; is that correct?</li> <li>A. Yes.</li> <li>Q. Okay. Sorry. Give me a second.</li> <li>MR. CUNNING: Why don't we do this, give</li> </ul>	2	patterning step?
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25 MR. CUNNING: Why don't we do this, give	24	Q. Okay. Sorry. Give me a second.
	25	MR. CUNNING: Why don't we do this, give

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1 me --2 MR. HOPEN: Scott, did we get notice for 3 this -- for this depo? 4 MR. CUNNING: I don't know what you're 5 asking me. 6 MR. HOPEN: For this claim construction. 7 MR. CUNNING: I don't know what you're 8 asking me. 9 MR. HOPEN: Did we get notice of this 10 exhibit on our list of exhibits on this deposition? 11 MR. CUNNING: Oh, on the deposition 12 notice --13 MR. HOPEN: Yeah. 14 MR. CUNNING: -- was the exhibit called 15 out? 16 MR. HOPEN: I just don't recall seeing 17 this. 18 MR. CUNNING: I don't believe that it was. I don't know that -- I don't believe it's required to 19 20 list --21 MR. LYTVYN: It is. 22 MR. HOPEN: All right. We just -- we'll 23 just put it on --MR. CUNNING: Okay. You have an objection? 24 25 MR. HOPEN: I've got an objection on that,

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1	but go ahead.
2	MR. CUNNING: I have a different
3	interpretation of the rule, but let's go off the record.
4	I've got just a couple of questions on this. Let me
5	organize, and then I think we'll be that way, I can
6	proceed through the last bit fairly efficiently and
7	probably be done pretty soon.
8	MR. HOPEN: Great.
9	(Break taken from 12:06 p.m. to 12:14 p.m.)
10	Q. Welcome back. I won't promise I only have one
11	question because no attorney that says that is telling
12	the truth. I'll say I only have a few questions left.
13	If you can turn to page 18 of TSMC-1016
14	which is the claim construction order for the district
15	court. And at the top of the page there in bold, do you
16	see the court's construction of first pattern and second
17	pattern?
18	A. Yes.
19	Q. Okay. If you were to apply the district
20	court's construction of first pattern and second
21	pattern, would Jinbo read on the claim 1 of the 084
22	patent?
23	A. In forming my opinions that I put down in my
24	declaration
25	Q. Uh-huh.

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1	A I used the construction provided in the
2	by the PTAB.
3	Q. Uh-huh.
4	A. All my opinions as set forth in the declaration
5	are based on that construction. I have not attempted to
6	analyze Jinbo in view of the construction presented in
7	this document and have formed no opinion about that.
8	Q. And under the PTABs construction, it's your
9	view that the first pattern must be different than the
10	second pattern, correct?
11	MR. HOPEN: Objection, form.
12	A. I mean, it depends a little bit on what you
13	mean by different, but basically, yes.
14	Q. And as construed by the district court here in
15	TSMC-1016, there's no requirement that the first pattern
16	is different than the second pattern; is that correct?
17	A. This construction that we just read does not
18	describe that requirement.
19	Q. So if you applied the district court's
20	construction, would Jinbo disclose each limitation of
21	the of claim 1 of the 084 patent?
22	A. To answer that question would require a fair
23	amount of analysis that I haven't done. The
24	construction in this document involves all of many of
25	the terms in claim 1, and I would have to review all of

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1 those constructions before answering your question. 2 Q. All right. 3 MR. CUNNING: I don't have any further questions for the witness. 4 5 MR. HOPEN: Are you closing your cross? MR. CUNNING: Yes. 6 7 MR. HOPEN: All right. 8 MR. CUNNING: With the reservation that I 9 may ask questions if you choose to examine the witness. 10 MR. HOPEN: Okay. I think we're going to 11 want to take a quick break. Maybe it's a good time to 12 have lunch --13 MR. CUNNING: Okay. 14MR. HOPEN: -- and probably do redirect. 15 MR. CUNNING: Okay. 16 (Break 'taken from 12:17 p.m. to 12:59 p.m.) 17 EXAMINATION BY MR. LYTVYN: 18 19 Q. Okay. So first question is, refer to -referring to the 084 patent -- I'm not sure what exhibit 20 21 number this one is. 22 Α. I have it. 23 0. So you were talking about abstract earlier and 24 how there's a sentence that begins, for the embodiment. 25 Could you, Dr. Mack, describe the embodiments disclosed

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1	in abstract?
2	A. Especially with regard to what the abstract
3	points out, there's two basic flavors or embodiments
4	that are discussed in the 084 which have many
5	similarities but one major difference, and the major
6	difference is whether or not two imaging layers are used
7	or one imaging layer. So in the first embodiment, we
8	use two separate imaging layers, and in the second
9	embodiment, we use only one imaging layer.
10	Q. Okay. Is there any difference in how these two
11	embodiments use radiation to pattern the resist layers?
12	A. The embodiments are described using almost
13	identical language when discussing the two separate
14	patterns that are being used to form patterns in the
15	imaging layer, phrases like using a first mask to form
16	the first pattern and using a second mask to form the
17	second pattern. The language is essentially identical
18	when describing all the embodiments.
19	Q. Okay. Next question, if we can look at pattern
20	owner's DSS response to the petition. I'll just refer
21	to the same diagram Scott referred to earlier on page 3.
22	A. Okay.
23	Q. So let's also take a look at the 084 patent,
24	claim 1.
25	A. Okay.

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1	Q. So the step B and E include language where
2	we're patterning an imaging layer in accordance with
3	either first pattern or second pattern step E. So
4	keeping this language in mind for all consistency
5	purposes, could you describe what happens when we change
6	the alignment of different elements of the projection
7	device as depicted in the diagram on the page 3 of the
8	patent owner's response?
9	A. Well, one thing that came out in my earlier
10	testimony under direct examination was the idea that the
11	pattern that's being exposed into the photoresist is
12	what really matters, and so the step the phrase
13	"pattern" can be used in multiple ways. Here, it's used
14	as a verb patterning as in the process of forming the
15	pattern in the first imaging layer. And then we could
16	also use the word "pattern" to discuss what's on the
17	mask. We could also use the word "mask" to discuss what
18	is being exposed into the photoresist or what the final
19	result is at the end of exposure and development in the
20	photoresist.
21	But specifically in claim 1, patterning
22	pattern is used three times, right, so patterning refers
23	to imaging layer, I'm referring to 1(b), is the act of
24	performing the patterning step in accordance with a
25	first pattern. So this first pattern is the pattern

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1	that's being projected onto the photoresist to form a
2	first pattern layer which is the result of the
3	patterning step at the end.
4	Now, if I your question was what happens
5	if I start moving things around, like move the wafer
6	Q. Correct.
7	A Jinbo describes moving the wafer by a
8	certain amount. Well, you can using this figure to
9	help us visualize this, the figure on page 3 of the
10	patent owner's response to petition, you can think of
11	the pattern that's being projected onto the wafer as the
12	distribution of light as it's showing focusing down to a
13	small square. If I move the wafer back and forth, that
14	doesn't change that pattern at all. It's the same
15	pattern. The wafer's moving, but the pattern that's
16	being projected onto the wafer remains the same.
17	If I go up to the reticle and I move the
18	reticle around, that changes the light distribution that
19	exposes the wafer and potentially changes the pattern
20	that's being projected onto the wafer, so moving the
21	reticle and moving the wafer result in provide
22	different results when thinking about the claim language
23	in the 084 patent.
24	Q. Okay. Perfect. Now, keeping that in mind,
25	let's flip over to Jinbo. It's Exhibit TSMC-1004.

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1	A. Okay.
2	Q. And does Jinbo disclose altering the light
3	distribution that's being projected onto a wafer, in
4	your opinion?
5	A. I think Jinbo is very careful and clear to make
6	sure that the reader of this patent understands that the
7	light distribution that's projected onto the wafer is
8	not changed between the first patterning step and the
9	second patterning step.
10	Q. Okay. And a quick follow-up on that, during
11	cross-examination different techniques were discussed
12	about changes as far as the reticle, blading, or mask
13	shifting. What's your understanding of a person in the
14	ordinary skill in the art after reading the disclosure
15	of Jinbo? Are such techniques covered by the method of
16	Jinbo?
17	A. Well, I don't think Jinbo discloses using
18	masking blades, Jinbo doesn't disclose moving the mask
19	or changing the mask in any way.
20	Q. Okay. And I know on the cross exam you were
21	asked a question if such techniques were known in the
22	art and if they were possible. I'm going to ask a
23	slightly different question. Are those techniques in
24	any way necessary to carry out the method of Jinbo to
25	achieve the intended result?

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1 Α. No. 2 MR. LYTVYN: No further questions. Close 3 redirect. 4 FURTHER EXAMINATION BY MR. CUNNING: 5 6 When you move -- you were asked about moving Q. the wafer versus moving the reticle and exposing the 7 8 pattern onto an imaging layer. I believe you said that 9 moving the reticle changes the pattern, but moving the 10 wafer doesn't change the pattern. Is that a fair 11 summary of your testimony? 12 A. First of all, as I said, we use the word 13 "pattern" in different ways, so I was specifically 14 referring to the pattern that's being projected at the 15 bottom of the projection lens. 16 Ο. When you move the wafer, the pattern that is 17 being projected at the bottom of the projected lens is 18 projected into a different location on the wafer, 19 correct? 20 Α. Yes. 21 Q. Okay. 22 MR. CUNNING: I don't have any further 23 questions. Nothing else. 24 MR. LYTVYN: 25 (Deposition concluded at 1:08 p.m.)

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1	1 CHANGES AN	ND SIGNATURE
2	2 WITNESS NAME: DR. CHRIS A. M	MACK DATE: MAY 14, 2015
3	3 PAGE LINE CHANGE	REASON
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19	9 I declare under pe	enalty of perjury that the
20	0 foregoing is true and correc	ct with the above changes, if
21	1 any.	
22	2 Executed on (date)	•
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25	5 Signa	

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1 STATE OF TEXAS )

2	I, Larissa L. McPhearson, Certified Shorthand
3	Reporter in and for the State of Texas, hereby certify
4	to the following:
5	That the witness, DR. CHRIS A. MACK, was duly sworn
6	by me and that the transcript of the oral deposition is
7	a true record of the testimony given by the witness and
8	the statements of counsel;
9	That the witness requested a review of the
10	deposition. Changes, if any, will be provided to the
11	parties after the 30-day period has expired.
12	That the time used by counsel for the parties is as
13	follows:
14	Mr. Scott Cunning - 02 Hours:37 Minutes
15	Mr. Andriy Lytvyn - 00 Hours:08 Minutes
16	I further certify that I am neither counsel for,
17	related to, nor employed by any of the parties or
18	attorneys in the action in which this proceeding was
19	taken, and further that I am not financially or
20	otherwise interested in the outcome of the action
21	Certified to by me this 19th day of Max 2015:
22	Larissa L. McPhearson Texas CSP 811
23	Expiration Date: 12/31/16
24	1812 West Sam Houston Parkway North Houston Teras
25	(713)840-8484
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