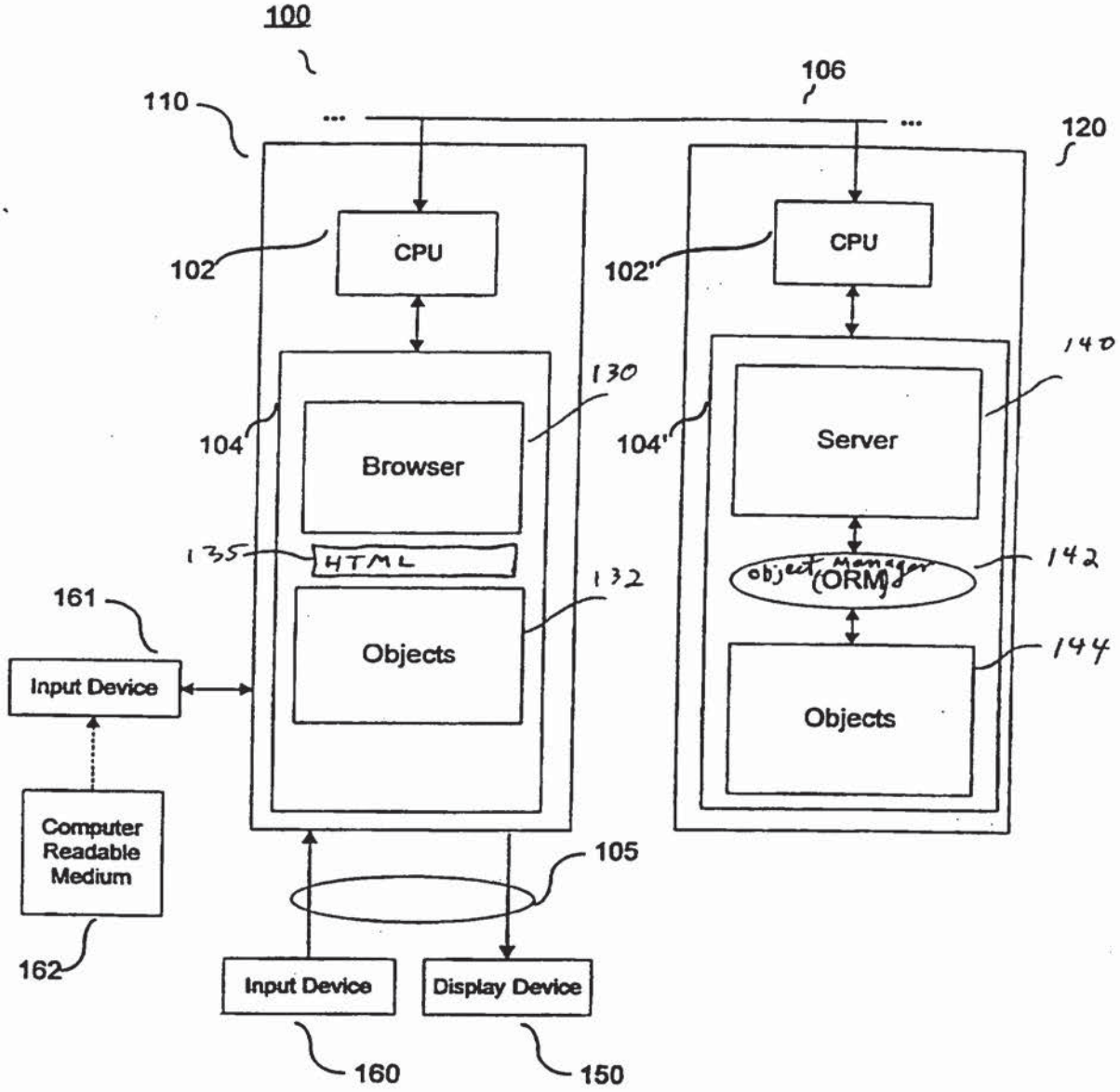


Fig. 1

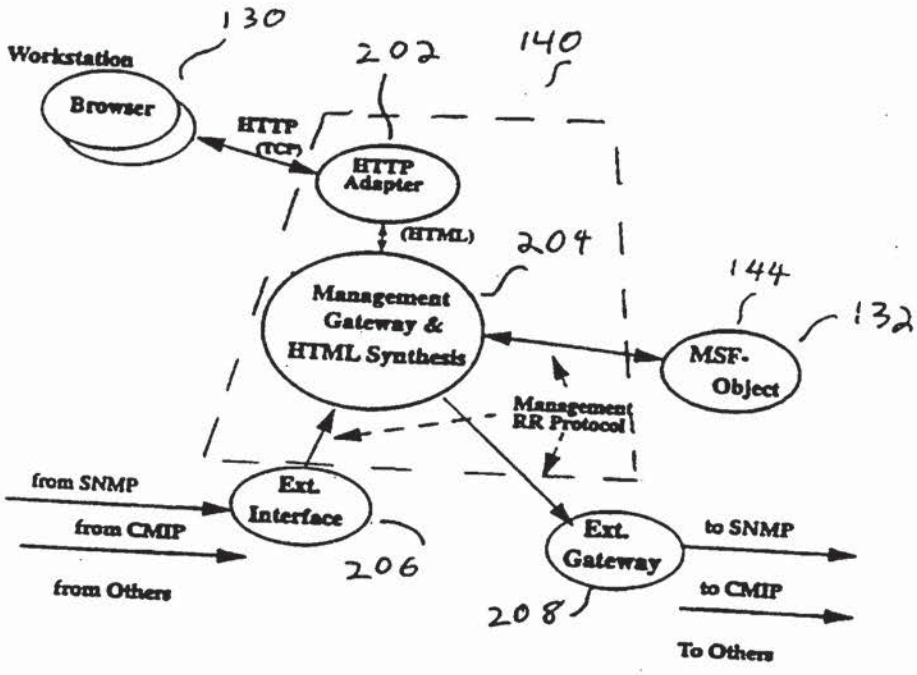


Fig. 2

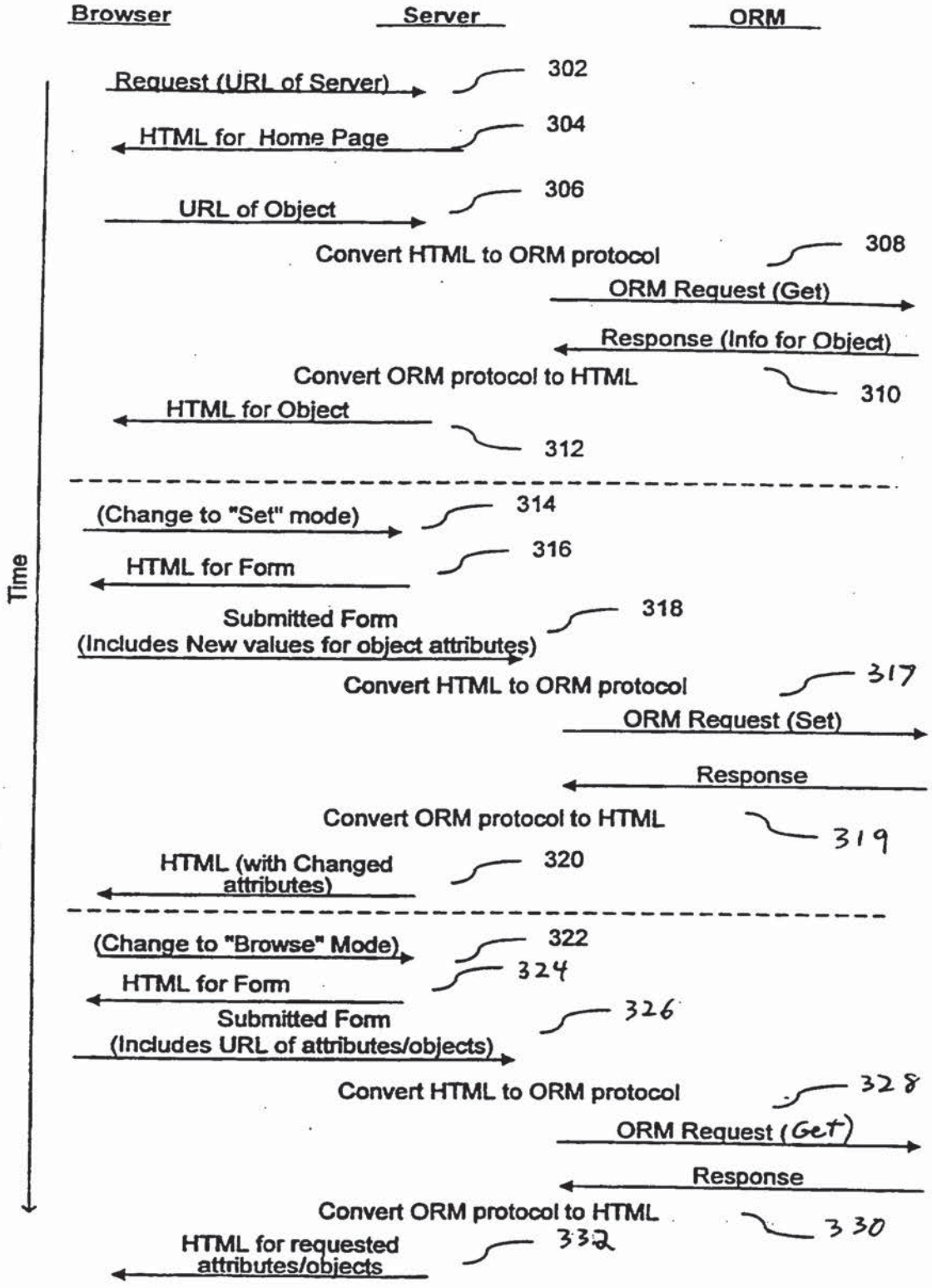


Fig. 3

A System as a Tree of Managed Entities:

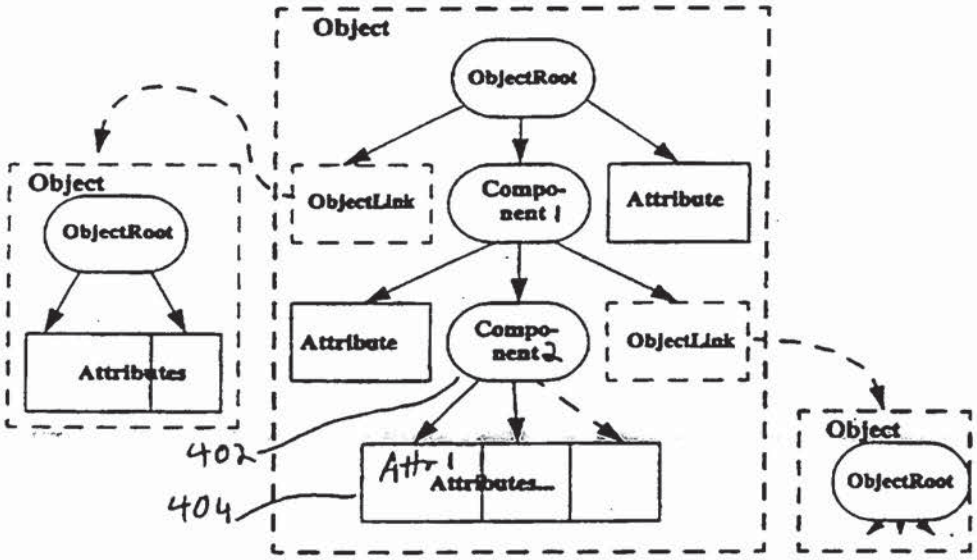


Fig. 4

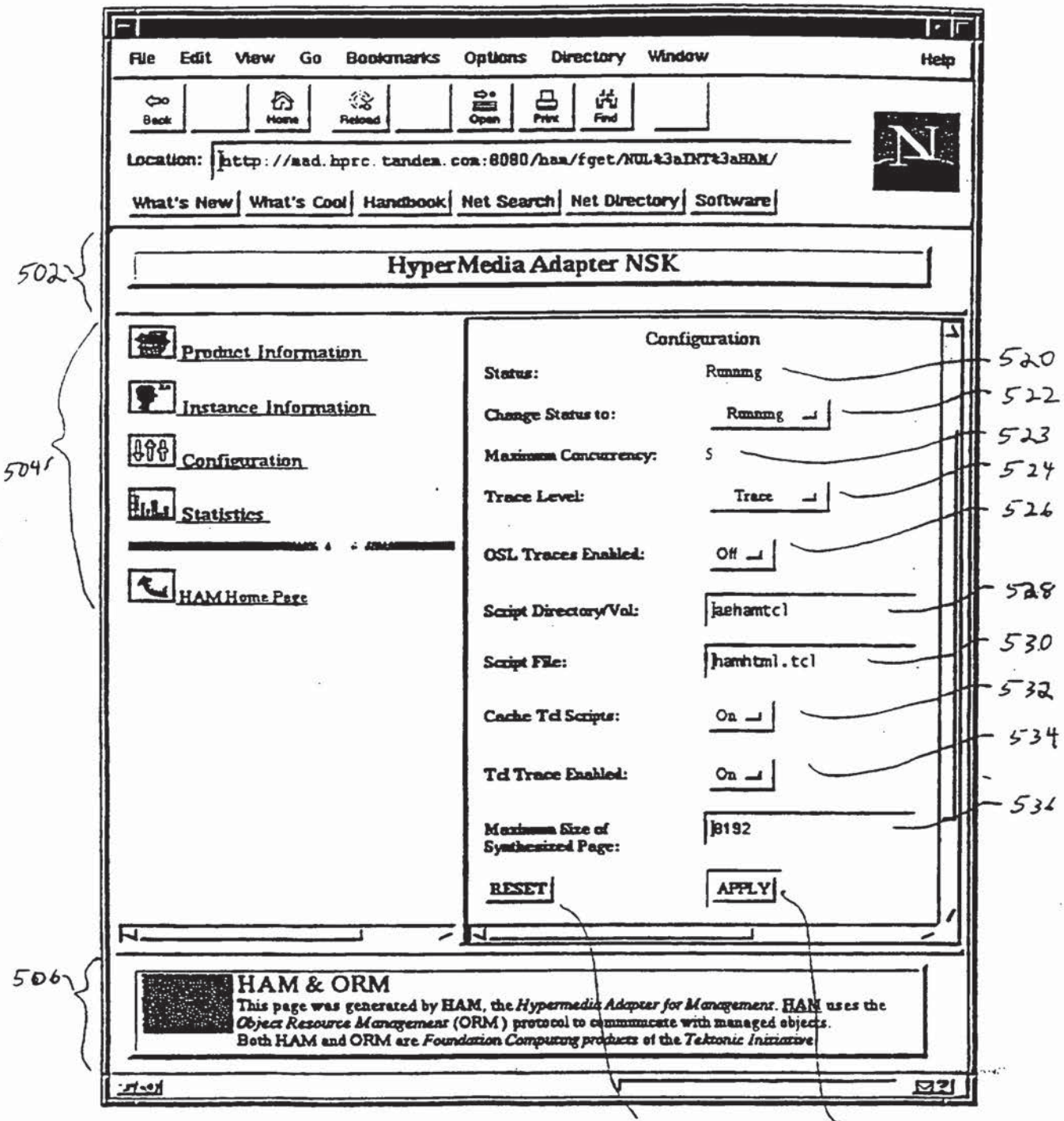


Fig. 5

hamset.html

```
<HTML>
<HEAD>
<TITLE>Alter Configuration of HyperMedia Adapter NSK@TCP:168.87.28.7/8062:HAM </TI
TLE>
<BASE HREF="http://mad.hprc.tandem.com:8080/ham/set/TCP%3a168.87.28.7%2f8062%3aHAM
%2fHyperMedia%20Adapter%20NSK%2fConfiguration/">
</HEAD>

<BODY BACKGROUND="/icons/icstgry.gif">
<H2>
<IMG ALT="" SRC="/icons/pccnfig.gif">
Alter Configuration</H2>
<H3>of HyperMedia Adapter NSK</H3>
<P><CENTER><IMG SRC="/icons/pcbrblu.gif"></CENTER><P>
<FORM METHOD=POST ACTION="http://mad.hprc.tandem.com:8080/ham/cset/TCP%3a168.87.28
.7%2f8062%3aHAM%2fHyperMedia%20Adapter%20NSK%2fConfiguration/">
<TABLE >
<P><CAPTION><STRONG><FONT SIZE=4>Configuration</FONT></STRONG></CAPTION>
<TR><TH ALIGN=LEFT><P>Status:</TH>
<TD ALIGN=LEFT>Running</TD>
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>Change Status to:</TH><TD >
<SELECT NAME="Change Status to">
<OPTION SELECTED> Running
<OPTION> Suspended
<OPTION> Aborted
<OPTION> Stopped
</SELECT>
</TD></TR>
<TR><TH ALIGN=LEFT><P>Maximum Concurrency:</TH>
<TD ALIGN=LEFT>5</TD>
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>Trace Level:</TH><TD >
<SELECT NAME="Trace Level">
<OPTION SELECTED> Warnings
<OPTION> Errors
<OPTION> Info
<OPTION> Debug
<OPTION> Trace
<OPTION> Everything
</SELECT>
</TD></TR>
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>OSL Traces Enabled:</TH><TD >
<SELECT NAME="OSL Traces Enabled">
<OPTION SELECTED> Off
<OPTION> On
</SELECT>
</TD></TR>
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>Script Directory/Vol:</TH><TD >
<INPUT NAME="Script Directory/Vol"
TYPE=TEXT VALUE="aehamtcl">
</TD></TR>
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>Script File:</TH><TD >
<INPUT NAME="Script File"
TYPE=TEXT VALUE="hamhtml.tcl">
</TD></TR>
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>Cache Tcl Scripts:</TH><TD >
<SELECT NAME="Cache Tcl Scripts">
<OPTION SELECTED> On
<OPTION> Off
</SELECT>
</TD></TR>
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>Tcl Trace Enabled:</TH><TD >
<SELECT NAME="Tcl Trace Enabled">
<OPTION SELECTED> Off
<OPTION> On
</SELECT>
</TD></TR>
```

620, 622, 623, 624, 626, 628, 630, 632, 634

Fig 6(a)

hamset.html

```
<TR VALIGN=BASELINE>
<TH ALIGN="LEFT"><P>Maximum Size of Synthesized Page:</TH><TD >
<INPUT NAME="Maximum Size of Synthesized Page"
TYPE=TEXT VALUE="8192">
</TD></TR>
<TR><TH ALIGN=LEFT><P><INPUT TYPE="reset" VALUE="RESET"></TH>
<TD ALIGN=LEFT><INPUT TYPE="submit" VALUE="APPLY"></TD></TR>
</TABLE>
</FORM>
<P>Go to <A HREF="http://mad.hprc.tandem.com:8080/ham/cget/TCP%3a168.87.28.7%2f806
2%3aHAM%2fHyperMedia%20Adapter%20NSK%2fConfiguration/"><STRONG>Browse-Only Mode</ST
RONG></A> of
<STRONG>Configuration</STRONG>
<P>
<P><CENTER><IMG SRC="/icons/pcbrblu.gif"></CENTER><P>
<BR><A HREF="http://mad.hprc.tandem.com:8080/ham/get/" TARGET="_top">
<IMG SRC="/icons/icback.gif"> HAM Home Page</A>
</BODY></HTML>
```

636, 640, 650

Fig 6 (b)

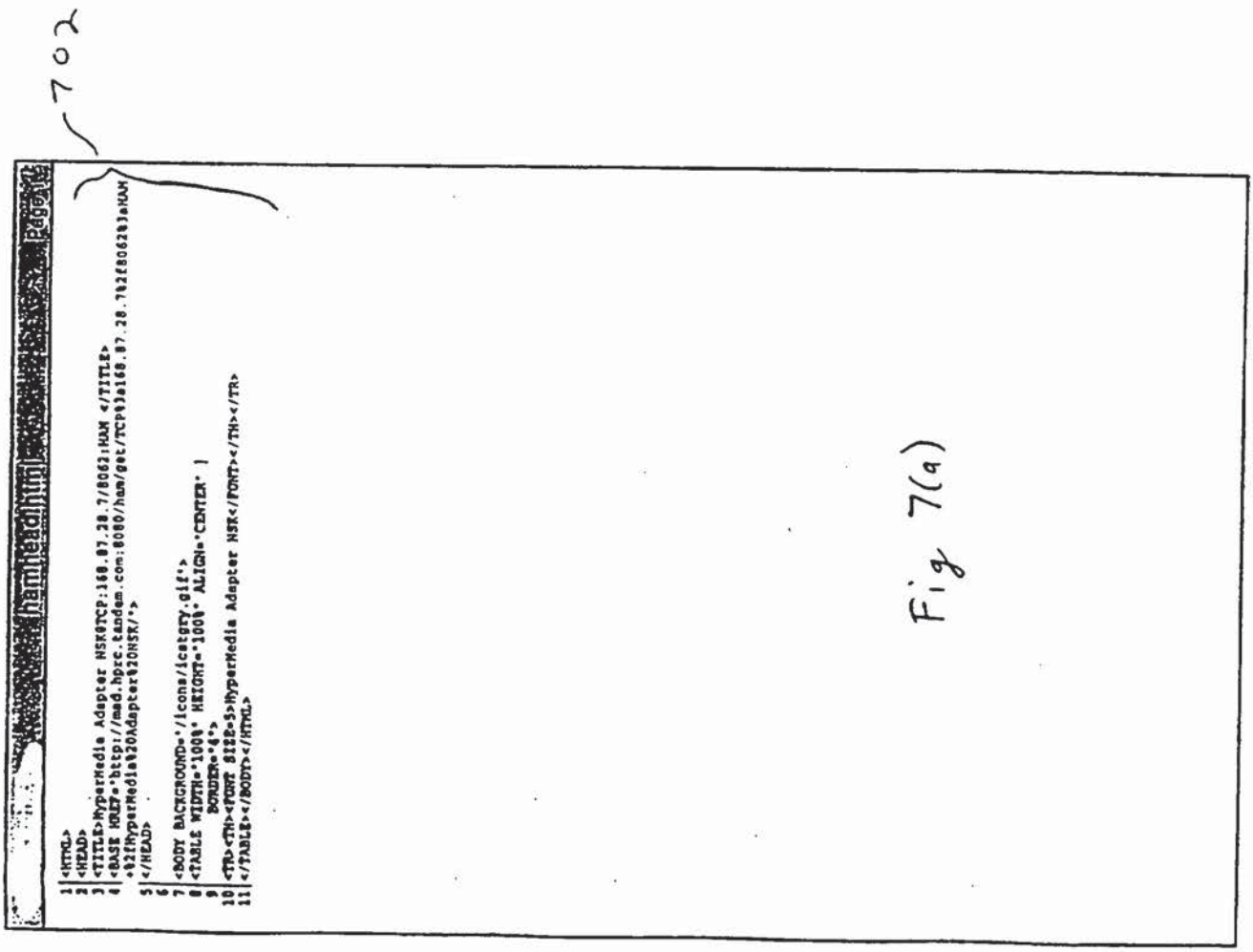
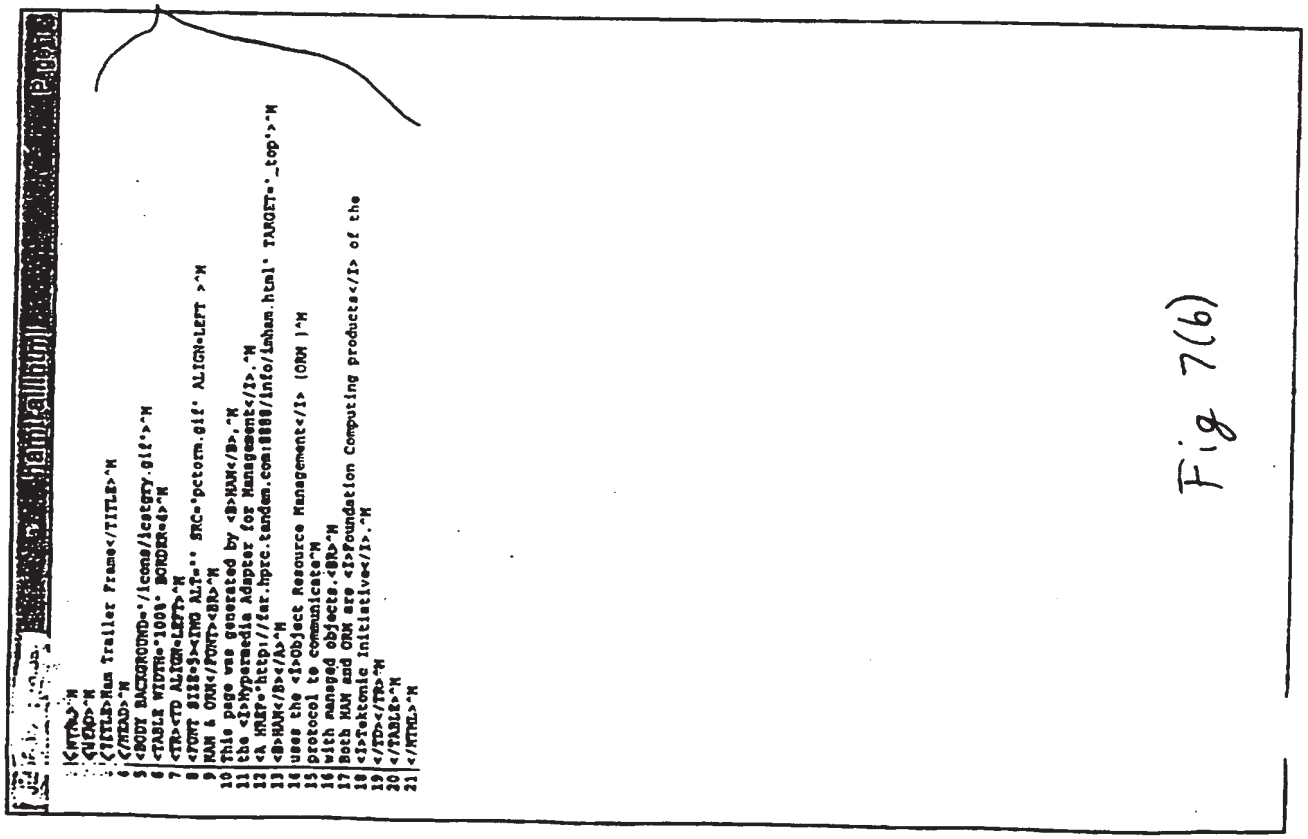


Fig 7(a)



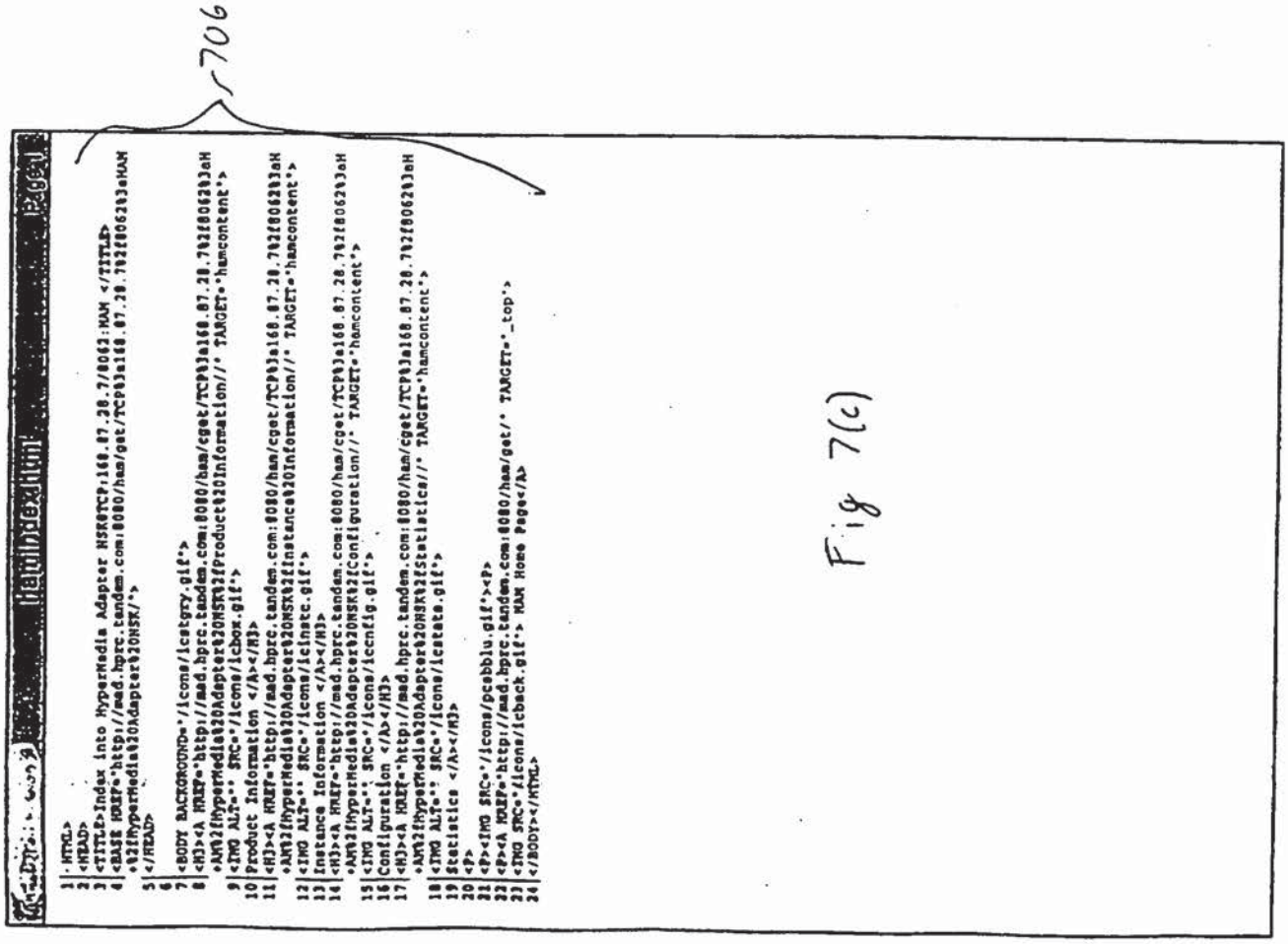


Fig 7(c)

ORM Protocol examples

```
1|A request as issued to create the index frame as seen in
2|the left frame of the window dump
3|
4|>>>>>
5|OrmVersion:1.0                                   820
6|OrmCat:HyperMedia Adaptor NSR                    822
7|Info                                             824
8|Component
9|Object
10|
11|<<<<<
12|
13|
14|And its response:
15|
16|>>>>>
17|OrmVersion:1.0
18|OrmCat:HyperMedia Adaptor NSR
19|Component:Product Information
20|Component:Instance Information
21|Component:Configuration
22|Component:Statistics
23|
24|<<<<<
25|
26|
27|
28|A Browse Request as issued for the frame on the right side
29|of the window dump (compare homeat.html)
30|>>>>>>
31|OrmVersion:1.0
32|OrmCat:HyperMedia Adaptor NSR/Configuration       830
33|Info
34|Component
35|Object
36|
37|<<<<<<
38|And its response
39|>>>>>>
40|OrmVersion:1.0
41|OrmCat:HyperMedia Adaptor NSR/Configuration
42|Attribute:Status
43|Value:Running
44|Mode:RO
45|Field:Enum
46|Range:Suspended,Aborted,Stopped
47|Attribute:Change Status to
48|Value:Running
49|Mode:WO
50|Field:Enum
51|Range:Suspended,Aborted,Stopped
52|Attribute:Maximum Concurrency
53|Value:5
54|Mode:RO
55|Field:Int
56|Range:1...10
57|Attribute:Trace Level
58|Value:Trace
59|Mode:RW
60|Field:Enum
61|Range:Errors,Warnings,Info,Debug,Everything
62|Attribute:OSL Traces Enabled
63|Value:Off
64|Mode:RW
65|Field:Enum
66|Range:On
67|Attribute:Script Directory/Val
68|Value:cohomtcl
69|Mode:RW
70|Field:String
71|Attribute:Script File
72|Value:homhtml.tcl
73|Mode:RW
```

802 804 806 808

Fig. 8(a)

```
Jul 9 1998 17:22                ham:trace                Page 2

74|Field:String
75|Attribute:Cache Tcl Scripts
76|Value:On
77|Mode:RW
78|Field:Enum
79|Range:Off
80|Attribute:Tcl Trace Enabled
81|Value:On
82|Mode:RW
83|Field:Enum
84|Range:Off
85|Attribute:Maximum Size of Synthesized Page
86|Value:0192
87|Mode:RW
88|Field:Int
89|Range:2048 . 16184
90|
91|<<<<<<<
92|
93|A Set/Change Request as a result from the
94|HTML FORM processing:
95|
96|>>>>>
97|OrmVersion:1.0
98|OrmCat:HyperMedia Adaptor NSR/Configuration      840
99|Attribute:Change Status to
100|Value:Running
101|Attribute:Trace Level
102|Value:Trace
103|Attribute:OSL Traces Enabled
104|Value:Off
105|Attribute:Script Directory/Val
106|Value:cohomtcl
107|Attribute:Script File
108|Value:homhtml.tcl
109|Attribute:Cache Tcl Scripts
110|Value:On
111|Attribute:Tcl Trace Enabled
112|Value:On
113|Attribute:Maximum Size of Synthesized Page
114|Value:0192
115|OrmCat:HyperMedia Adaptor NSR/Configuration
116|Info
117|
118|<<<<<
119|
120|Response similar to Get Request above
121|
122|
```

808 810

Fig. 8(b)

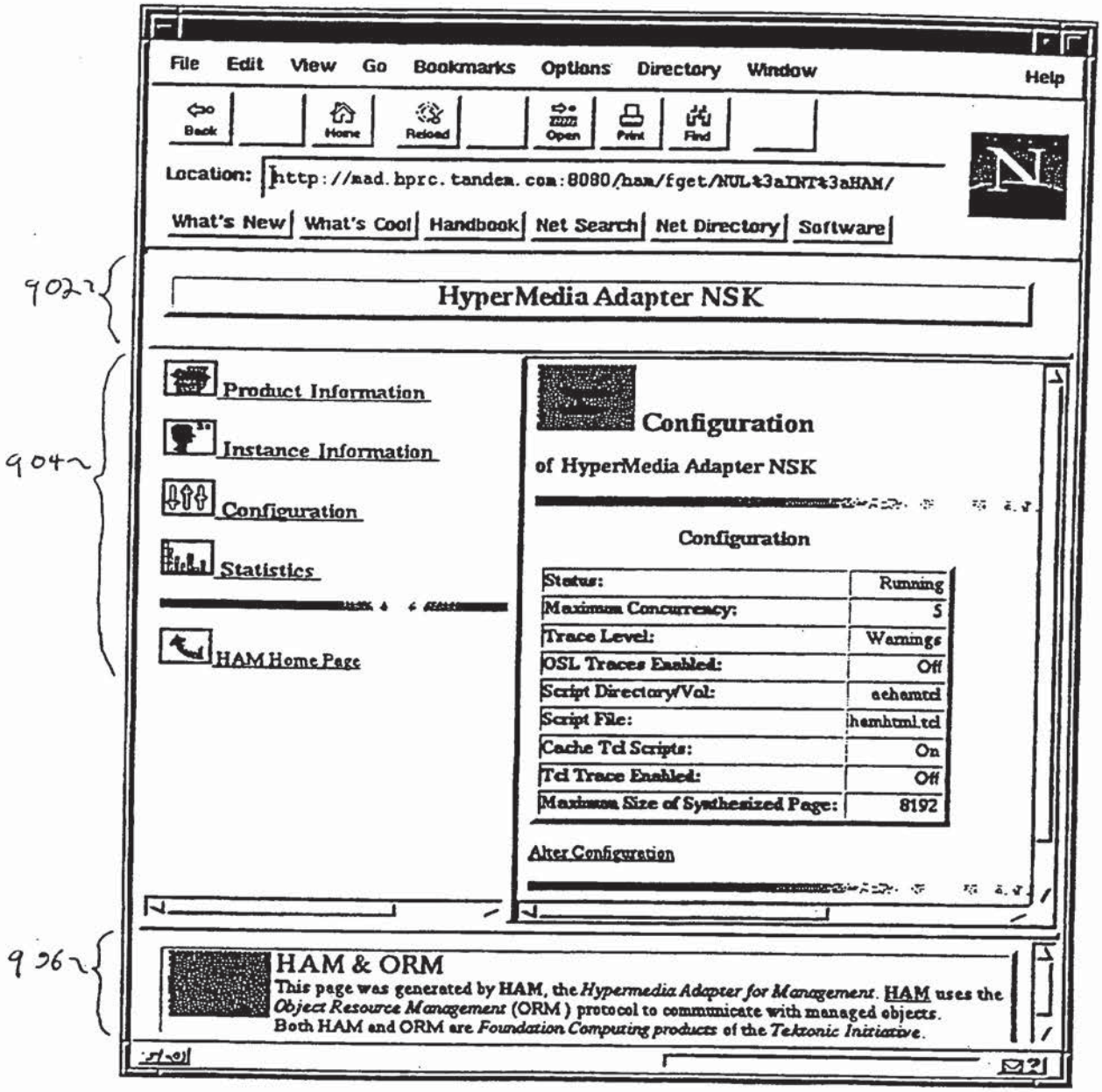
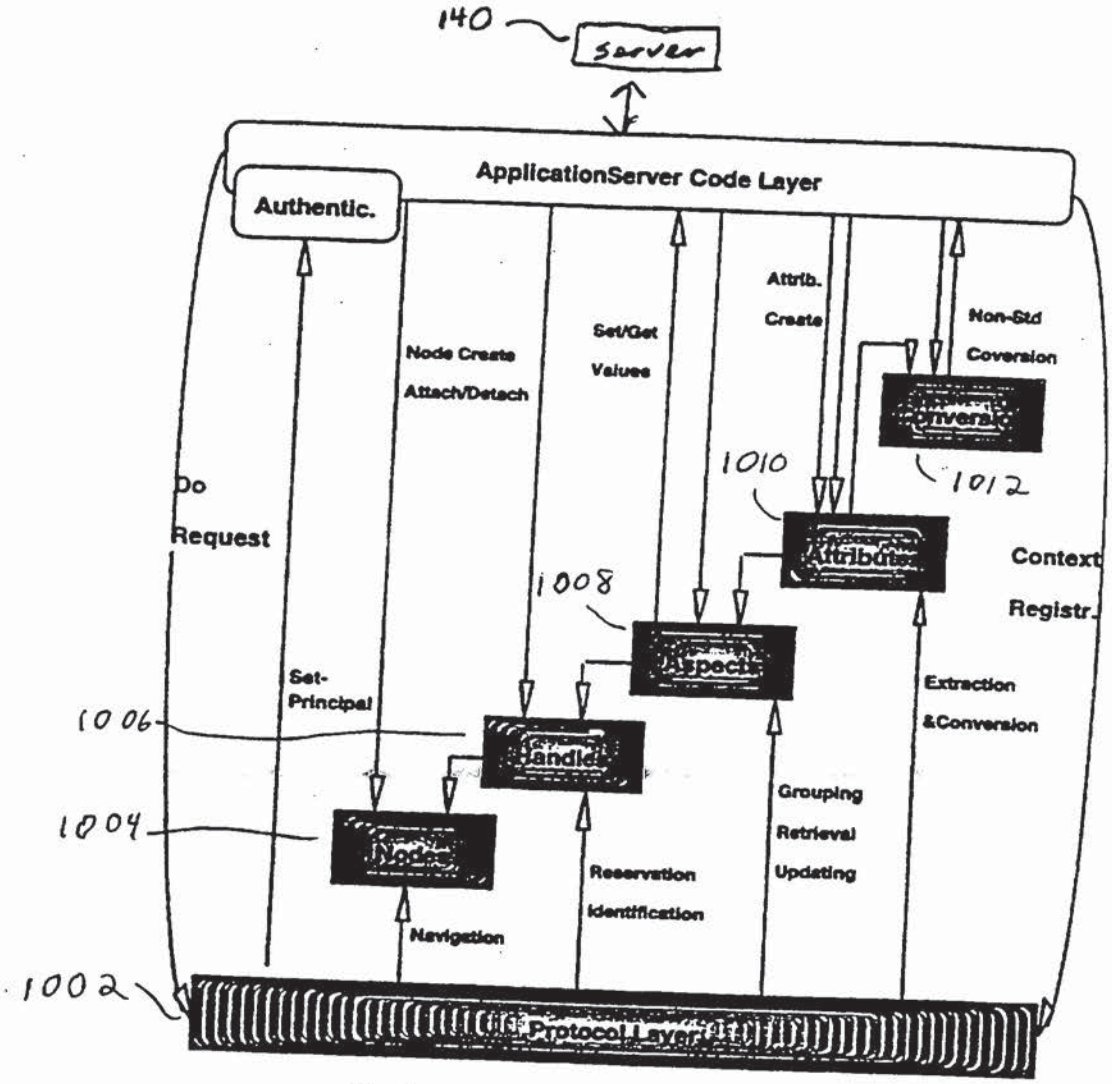


Fig. 9



The Layered Structure of the ORM Support Library

Fig. 10

INTERNATIONAL SEARCH REPORT

Inter ional Application No
PCT/US 97/11885

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 G06F17/30

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 6 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JAGANNATHAN V ET AL: "COLLABORATIVE INFRASTRUCTURES USING THE WWW AND CORBA-BASED ENVIRONMENTS" PROCEEDINGS - THE WORKSHOP ON ENABLING TECHNOLOGIES: INFRASTRUCTURE FOR COLLABORATIVE ENTERPRISES, 19 June 1996, pages 292-297, XP000645510 see page 293, column 1, line 39 - page 294, column 1, line 5 -----	1,14

☐ Further documents are listed in the continuation of box C.

☐ Patent family members are listed in annex.

° Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

1

Date of the actual completion of the international search	Date of mailing of the international search report
6 November 1997	12. 11. 97

Name and mailing address of the ISA	Authorized officer
European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Katerbau, R

Form PCT/ISA/210 (second sheet) (July 1992)

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WEST Search History

DATE: Friday, January 24, 2003

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L16	L14 not L15	55	L16
L15	L14 and (gateway or gate$way)	10	L15
L14	L13 not L10	65	L14
L13	L12 and ((707/$ or 709/$)!.ccls.)	71	L13
L12	L6 and L11	334	L12
L11	plural$3 near3 (database$ or template$ or container$ or folder$3)	28599	L11
L10	L8 and ((707/$ or 709/$).ccls.)	6	L10
L9	L8 and gate$way	4	L9
L8	L6 and L7	78	L8
L7	plural$3 near3 (container$ or folder$3)	22150	L7
L6	plural$3 near3 (register$3)	21112	L6
L5	L3 and ((container or folder$) with history)	7	L5
L4	L3 and gateway	20	L4
L3	L1 and L2	552	L3
L2	plural$3 with (register$3)	38977	L2
L1	plural$3 with (container$ or folder$3)	42649	L1

END OF SEARCH HISTORY

PATENT

IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

APPLICANT:	Michael De Angelo
APPLICATION NO.:	09/284,113
FILING DATE:	April 7, 1999
TITLE:	System And Method For Creating And Manipulating Information Containers With Dynamic Registers
EXAMINER:	Not yet assigned
GROUP ART UNIT:	2771
ATTY. DKT. NO.:	21114-03726

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, Washington, D.C. 20231 on the date shown below:

Dated: 1/16/03 By: _____
Greg T. Sueoka

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

REQUEST TO WITHDRAW AS ATTORNEYS

SIR:

Pursuant to 37 CFR §§ 1.36 and 10.40, the attorneys of record listed below, on whose behalf the undersigned attorney has signed this request and on whose behalf the undersigned attorney is authorized to sign, respectfully request permission to withdraw as attorneys of record in the above-referenced patent application, because the client has failed to pay one or more bills rendered by the undersigned's law firm for an unreasonable period of time.

☒ There is currently no outstanding official action known to the undersigned attorney dated prior to the date of this Request to Withdraw for which a response is now due to be filed.

☐ There is an outstanding term for response to an official action that is set to expire on _____. The above term is extendible under 37 CFR § 1.136(a), until _____.

☐ The client has been notified of the official action and of the deadline to respond to the official action.

☐ A response to the outstanding official action is attached.

☐ A petition for extension of time is requested and the fees for the extension are/are not being submitted.

The withdrawing attorneys of record are:

☒ associated with Customer Number 00758 →

OR

☐ as named below:

Name	Registration Number	Name	Registration Number
Greg T. Sueoka	33,800	James Okamoto	40,110

The attorneys of record listed above, on whose behalf the undersigned attorney has signed this request and on whose behalf the undersigned attorney is authorized to sign, respectfully request that all future correspondence regarding this application be addressed to the last know address:

Michael De Angelo
Information Equity Corporation
100 South Sunrise Boulevard, Suite 470
Palm Springs, CA 92262

A copy of this Request is being sent to Michael De Angelo, Information Equity Corporation, 100 South Sunrise Boulevard, Suite 470, Palm Springs, CA 92262 at the last-known address. This Request to Withdraw is being submitted in triplicate.

Respectfully submitted,
Michael De Angelo

Dated: 1/16/03

By: _____

Greg T. Sueoka, Reg. No.: 33,800
Fenwick & West LLP
Two Palo Alto Square
Palo Alto, CA 94306
Tel.: (650) 858-7194
Fax.: (650) 494-1417

WEST Search History

DATE: Tuesday, January 28, 2003

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=OR		
L18	L15 and ("OOP" or "object-oriented")	9	L18
L17	L16 not L13	37	L17
L16	L15 and (register$3 with (data or file or resource$ or information) with (container$ or folder$3))	39	L16
L15	L10 and L14	555	L15
L14	plural$ with (container$ or folder$3)	42715	L14
L13	L12 not L8	43	L13
L12	L11 and ("OOP" or "object-oriented")	45	L12
L11	L9 and L10	1065	L11
L10	plural$ with register$3	39067	L10
L9	plural$ with (container$ or folder$3 or database$)	50485	L9
L8	L3 and L7	17	L8
L7	L6 not L4	43	L7
L6	L1 and ("OOP" or "object-oriented")	44	L6
L5	L2 and ("OOP" or "object-oriented")	1	L5
L4	L2 and L3	35	L4
L3	(container$ or folder$3) with (Id or ids or identif$5)	6737	L3
L2	plural$ with (container$ or folder$3) with register$3	292	L2
L1	(container$ or folder$3) with register$3	2703	L1

END OF SEARCH HISTORY

UNITED STATES PATENT AND TRADEMARK OFFICE.

UNITED STATES DEPARTMENT OF COMMERCE
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/284,113	04/07/1999	MICHAEL DE ANGELO	3726-US	1910

7590 02/11/2003

GREG T SUEOKA
FENWICK & WEST
TWO PALO ALTO SQUARE
PALO ALTO, CA 94306

EXAMINER	
NGUYEN, CAM LINH T	
ART UNIT	PAPER NUMBER
2171	

DATE MAILED: 02/11/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/284,113	DE ANGELO, MICHAEL	
	Examiner	Art Unit	
	Cam-Linh T. Nguyen	2171	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *07 April 1999* .

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-36* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-36* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *07 April 1999* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)☐ The proposed drawing correction filed on _____ is: a)☐ approved b)☐ disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12)☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____ .

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14)☒ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) ☐ The translation of the foreign language provisional application has been received.

15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) *3 & 5* .
4) ☐ Interview Summary (PTO-413) Paper No(s). _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: .

DETAILED ACTION

Specification

1. This application does not contain an abstract of the disclosure as required by 37 CFR 1.72(b). An abstract on a separate sheet is required.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1 - 36 are rejected under 35 U.S.C. 102(e) as being anticipated by McKeehan et al (U.S. 6,016,495).

♦ As per claim 20,

McKeehan et al. discloses a method for creating an interactive information container comprising:

- "Forming a container" See Fig. 8, element 870, Fig. 10, col. 18 line 33 – 47.
- "Selecting an interactive register for the container" See Fig. 2B, col. 11 line 17 – 27, col. 20 line 59 – 67, Fig. 17A col. 24 line 65 – col. 25 line 9.
- "Identifying an item for inclusion in a container" corresponds to a particular resource or object that need to register and stored in the storage.

- "Creating a container element that includes the identified item" See Fig. 2B col. 11 line 17 – 27.when an item is registered and stored in the persistent storage, it is considered as an identified item, and the storage that includes the identified item is called "container" as in Fig. 10.

♦ As per claim 1,29,

With all limitations as in claim 20 further claim 1 includes:

- "Plurality of containers" See Fig. 1, 2A, 8, col. 15 line 47 – col. 16 line 40, col. 17 line 24 – 37.
- "Plurality of registers" See Fig. 2A.
- "A first register storing a unique container identification value" See fig. 11, col. 19 line 3 – 8, 59 - 61.
- "The second register stores information and evolves according to the relationship, use and interaction of the container with other containers" See Fig. 11; col. 19 line 15 – 62.
- "A gateway attached to and forming part of the container… controlling the interaction of the container with other containers" corresponds to the Transaction Manager (See col. 20 line 63 – 67).

♦ As per claim 2,

- "The information element is one from the group of text, graphic…a system" corresponds to the text of the objects that registered in the container (See Fig. 2A).

♦ As per claim 3 - 4,

- "One container history register for storing information regarding past interaction of the container with other container... modified" See col. 20 line 6 – 13, col. 23 line 21 – 42.

◆ As per claim 5 - 7,

- "Plurality of registers include at least one predefined register" corresponds to the methods that are defined by the system (See col. 23 line 21 – 42).
- "Plurality of registers include a user created register" corresponds to the extensible methods that defined by user (See col. 23 line 21 – 42).

◆ As per claim 8 – 9, 31 – 32,

- "Plurality of registers ... controlling the relationship of the container with other containers" corresponds to the Lock register because the lock class controls about the concurrent of objects stored in the container (See col. 19 line 24 – 39).
- Because the system support locking manager, it must include an "active time, passive time, neutral time", which shows the interactive of an object with other objects.

◆ As per claim 10, 33,

- "Plurality of registers include at least one acquire register" See col. 21 line 17 – 21, col. 19 line 55 – 61.

◆ As per claim 11 – 14,

- Because the system 800 is virtual addressing mechanisms that allow the programs to access to other storage, therefore, the system must have register that referring the space or location of a container.

♦ As per claim 15 - 19, 35 – 36,

Because the Transaction Manager carries out the transaction control by interacting with objects of the extensible Resource class that are registered to it (Col. 20 line 67 – col. 21 line 13), therefore, it must including the means of "allowing interaction, gather information, reporting information, and including the rules defining the interaction of the container".

♦ As per claim 21 - 22, 30,

- "Displaying a plurality of container levels" See Fig. 12, col. 22 line 1 – 22.
- "Receiving input from user selecting one of the displayed container level" corresponds to the query of user to retrieve a particular object in a container.
- "Displaying a container template corresponding to the container level input" corresponds to the result of the query.

♦ As per claim 23,

Because each container store different data structure, the system must providing a data structure as part of the container element (See col. 1 line 36 – 40, col. 2 line 28 – 38).

♦ As per claim 24 - 26,

- In order for a container work properly, the container must have a gateway to control the interaction of the container with other containers. In the instance reference, the gateway is corresponding to the Transaction Manager (See col. 20 line 63 – 67), because the transaction manager comprises a list of transaction ID that controls all transactions between containers (col. 24 line 62 – 64). In

addition, the container also has to determine the current gateway or register when a new container is created.

♦ As per claim 27 – 28, 34,

As noted above, the system creates some methods in order to create a new container; it must include the determination of available register to store items into the container (See col. 23 line 5 – 42).

Conclusion

1. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Itakura et al (U.S. 6,351,745) discloses a communication system for distributing such message as advertisement to user of terminal equipment.

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cam-Linh T. Nguyen whose telephone number is 703-305- 1951. The examiner can normally be reached on Monday - Friday from 8:00 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Safet Metjahic, can be reached on (703) 308- 1436. The fax phone number for the organization where this application or proceeding is assigned is 703- 746- 7239.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703- 305-3900.

Cam-Linh Nguyen
Art Unit 2171

LN

SAFET METJAHIC
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100

Notice of References Cited	Application/Control No. 09/284,113	Applicant(s)/Patent Under Reexamination DE ANGELO, MICHAEL	
	Examiner Cam-Linh T. Nguyen	Art Unit 2171	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,016,495	01-2000	McKeehan et al.	707/103R
	B	US-6,351,745	02-2002	Itakura et al.	707/10
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

FORM PTO-1449 (REV. 6-89)

U.S. DEPARTMENT OF COMMERCE
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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Attorney's Docket No.	Serial No.
3726	09/284,113
Applicant	
Michael De Angelo	
Filing Date	**Group Art Unit**
April 7, 1999	Unknown

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
LN	A	5 7 6 8 5 1 0	06/16/98	Gish	395	200.33	07/01/96
LN	B	5 8 4 8 2 4 6	12/08/98	Gish	395	200.58	07/01/96

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
Nguyen Cam Huch	1/30/03

EXAMINER: Initial if references considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

PTO-1449
REV: 12/96

FORM PTO-1449 (REV. 6-89) | U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Attorney's Docket No.	Serial No.
3726	09/284,113
Applicant	
Michael De Angelo	
Filing Date	**Group Art Unit**
April 7, 1999	2771

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
LN	C	5,664,208	09/02/97	Pavley et al.	395	777	May 16, 1995

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No
LN	D	WO 98 02831	01/22/98	PCT	G06F	17/30		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER [signature] DATE CONSIDERED 1/30/03

EXAMINER: Initial if references considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

PTO-1449
REV: 02/01

UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/284,113	04/07/1999	MICHAEL DE ANGELO	3726-US	1910

7590 10/30/2003

GREG T SUEOKA
FENWICK & WEST
TWO PALO ALTO SQUARE
PALO ALTO, CA 94306

EXAMINER	
NGUYEN, CAM LINH T	
ART UNIT	**PAPER NUMBER**
2171	7

DATE MAILED: 10/30/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Notice of Abandonment	Application No.	Applicant(s)	
	09/284,113	DE ANGELO, MICHAEL	
	Examiner	Art Unit	
	Cam-Linh T. Nguyen	2171	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

This application is abandoned in view of:

1. ☒ Applicant's failure to timely file a proper reply to the Office letter mailed on *11 February 2003*.
   (a) ☐ A reply was received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the period for reply (including a total extension of time of _____ month(s)) which expired on _____.
   (b) ☐ A proposed reply was received on _____, but it does not constitute a proper reply under 37 CFR 1.113 (a) to the final rejection.
   (A proper reply under 37 CFR 1.113 to a final rejection consists only of: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114).
   (c) ☐ A reply was received on _____ but it does not constitute a proper reply, or a bona fide attempt at a proper reply, to the non-final rejection. See 37 CFR 1.85(a) and 1.111. (See explanation in box 7 below).
   (d) ☒ No reply has been received.

2. ☐ Applicant's failure to timely pay the required issue fee and publication fee, if applicable, within the statutory period of three months from the mailing date of the Notice of Allowance (PTOL-85).
   (a) ☐ The issue fee and publication fee, if applicable, was received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the statutory period for payment of the issue fee (and publication fee) set in the Notice of Allowance (PTOL-85).
   (b) ☐ The submitted fee of $_____ is insufficient. A balance of $_____ is due.
   The issue fee required by 37 CFR 1.18 is $_____. The publication fee, if required by 37 CFR 1.18(d), is $_____.
   (c) ☐ The issue fee and publication fee, if applicable, has not been received.

3. ☐ Applicant's failure to timely file corrected drawings as required by, and within the three-month period set in, the Notice of Allowability (PTO-37).
   (a) ☐ Proposed corrected drawings were received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the period for reply.
   (b) ☐ No corrected drawings have been received.

4. ☐ The letter of express abandonment which is signed by the attorney or agent of record, the assignee of the entire interest, or all of the applicants.

5. ☐ The letter of express abandonment which is signed by an attorney or agent (acting in a representative capacity under 37 CFR 1.34(a)) upon the filing of a continuing application.

6. ☐ The decision by the Board of Patent Appeals and Interference rendered on _____ and because the period for seeking court review of the decision has expired and there are no allowed claims.

7. ☒ The reason(s) below:

A confirmation for abandoment was made on 10/22/2003 with Michael De Angelo. Phone number: 760 - 864 - 9500.

SAFET METJAHIC
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100

Petitions to revive under 37 CFR 1.137(a) or (b), or requests to withdraw the holding of abandonment under 37 CFR 1.181, should be promptly filed to minimize any negative effects on patent term.

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Paper No. 9

Greg T. Sueoka
FENWICK & WEST LLP
Two Palo Alto Square
Palo Alto, CA 94306

In re Application of:
Michael De Angelo
Application No. 09/284,113
Filed: April 7, 1999
For: SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS

DECISION ON REQUEST TO WITHDRAW AS ATTORNEY OR AGENT

This is a decision on the Request to Withdraw from Representation filed January 24, 2003.

A grantable request to withdraw as attorney of record should indicate thereon the present mailing addresses of the attorney(s) who is/are withdrawing from the record and of the applicant. The request for withdrawal must be signed by every attorney seeking to withdraw or contain a clear indication that one attorney is signing on behalf of another/others. A request to withdraw will not be approved unless at least 30 (thirty) days would remain between the date of approval and the later of the expiration date of a time to file a response or the expiration date of the maximum time period which can be extended under 37 C.F.R. § 1.136(a). The effective date of withdrawal being the date of decision and not the date of request. See M.P.E.P. § 402.06. 37 C.F.R. § 1.36 further requires that the applicant or patent owner be notified of the withdrawal of the attorney or agent.

The request is **GRANTED.**

All future communications from the Office will be directed to the below-listed address until otherwise notified by applicant. This correspondence address is provided by the withdrawn attorney(s). Applicant is reminded of the obligation to promptly notify the Patent and Trademark Office (Office) of any change in correspondence address to ensure receipt of all communications from the Office.

Vincent N. Trans
Special Program Examiner
Technology Center 2100
Computer Architecture, Software, and
Information Security
703-305-9750

cc: Michael De Angelo
Information Equity Corporation
100 South Sunrise Boulevard, Suite 470
Palm Springs, CA 92262

08-11-04 Attorney Docket No.: 17776-002US1

#10

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OFFICE OF PETITIONS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	DeAngelo, Michael	Art Unit	:	2171
Serial No.	:	09/284,113	Examiner	:	Cam N. Nguyen
Filed	:	April 7, 1999			
Title	:	SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS			

MAIL STOP PETITIONS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION TO REVIVE APPLICATION UNDER 37 C.F.R. § 1.137(b)

Applicant hereby petitions under 37 C.F.R. § 1.137(b) to revive the above application for failure to respond to the non-final office action mailed February 11, 2003.

Enclosed is 1) a declaration of Michael de Angelo in support of this petition and upon which this petition is based, 2) a response to the non-final office action mailed February 11, 2003, to continue prosecution of the application, and 3) a check for $665 in payment of the petition fee by a small entity as set forth in 37 C.F.R. § 1.17(m).

Applicant submits that the entire delay in filing the required reply from the due date for the reply until the filing of a grantable petition pursuant to 37 C.F.R. § 1.137(b) was unintentional.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: Aug. 9, 2004

Tamara Fraizer
Reg. No. 51,699

Fish & Richardson P.C.
500 Arguello Street, Suite 500
Redwood City, California 94063
Telephone: (650) 839-5070
Facsimile: (650) 839-5071

50227801.doc

08/12/2004 HALI11 00000003 09284113
01 FC:2453 665.00 OP

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EV 321 384 896 US

August 9, 2004
Date of Deposit

Attorney's Docket No.: 17776-002US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999
Title : SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS

Art Unit : 2171
Examiner : Cam N. Nguyen

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DECLARATION OF MICHAEL DE ANGELO IN SUPPORT OF PETITION TO REVIVE APPLICATION UNDER 37 C.F.R. § 1.137(b)

I, Michael De Angelo, declare and state as follows:

1. My citizenship, residence and address are as stated below by my name and signature.

2. I believe that I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: System And Method For Creating And Manipulating Information Containers With Dynamic Registers. My patent application is based upon over twenty-five years of committed work and development.

3. Prior to the Patent Office's issuance on February 11, 2003, of an Office Action pertaining to my application, I was informed by patent counsel that they were no longer representing me because I had not been able to pay their bills.

4. During the entire year of 2003 and until the present time, I have suffered severe hardship due to the criminal destruction of a corporation for which I was the Chairman and CEO. This matter was investigated by the FBI and the Department of Justice, and a Federal subpoena was issued to the perpetrators. The destruction of the corporation and multiple related lawsuits caused me extreme mental anguish and serious financial difficulties.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit

Signature

Typed or Printed Name of Person Signing Certificate

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999
Page : 2 of 3

Attorney's Docket No.: 17776-002US1

5. I had no understanding of the timeline for my response to the first office action issued with respect to my application and was unable to pursue the matter during this time.

6. The entire delay in filing of the required reply to the PTO Office Action of February 11, 2003, from the due date for the reply until the filing of the accompanying petition was unintentional.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Full name: MICHAEL DE ANGELO
Signature: _____
Date: August 5, 2004
Residence and Address: 3700 Andreas Hills Drive, Palm Springs, CA 92264
Citizen of: United States of America

Attorney Docket No.: 17776-002US1

#11/a

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999
Title : SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS

Art Unit : 2171
Examiner : Cam N. Nguyen

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF FEBRUARY 11, 2003

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EV 321 384 896 US

August 9, 2004
Date of Deposit

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

---

1. (Currently Amended) An apparatus for transmitting, receiving and manipulating information on a computer system, ~~,~~ the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

an information element having information;

a plurality of registers, the plurality of registers forming part of the container and including~~,~~

a first register ~~of the plurality of registers~~ for storing a unique container identification value and~~,~~

a second register having a representation designating time and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus event time~~of the plurality of registers that stores information and evolves according to the relationship, use and interaction of the container with other containers, processes and systems~~; and

a gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, systems ~~and~~ or processes.

2. (Currently Amended) The apparatus of claim 1 or 37, wherein the information element is one from the group of text, graphic images, video, audio, a digital pattern, a process, a nested container, bit, natural number and a system.

3. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the plurality of registers includes at least one container history register for storing information

regarding past interaction of the container with other containers, systems or processes, the container history register being ~~modified~~modifiable.

4. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the plurality of registers includes at least one system history register for storing information regarding past interaction of the container with different operating system and network processes.

5. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the plurality of registers includes at least one predetfined register, the predefined register being a register associated with an editor for user selection and ~~. the predefined register~~ being appendable to any container.

6. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the plurality of registers includes a user-created register, the user-created register being generated by the user, ~~one or more of which is~~and being appendable to any container.

7. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the plurality of registers includes a system-defined ~~-~~register, the system-defined register being set, controlled and used by the system, ~~one or more of which is~~ and being appendable to any container.

8. (Cancelled)

9. (Currently Amended) The ~~system~~ apparatus of claim ~~8~~1, wherein the plurality of registers includes:

an active time register for identifying times at which the container will act upon other containers, processes, systems or gateways;

an passive time register for identifying times at which the container can be acted upon by other containers, processes, systems, or gateways; and

a neutral tune time register for identifying times at which the container may interact with other containers, processes, systems or gateways.

a'

10. (Currently Amended) The system apparatus of claim 1 or 37, wherein the plurality of registers includes at least one acquire register for controlling whether the container adds a register or a container from other containers when interacting with them.

11-13. (Cancelled)

14. (Currently Amended) The system apparatus of claim 3711, wherein the plurality of registers includes:

an active space register for identifying space in which the container will act upon other containers, processes, systems or gateways;

an passive space register for identifying from space in which the container can be acted upon by other containers, processes, systems, or gateways; and

a neutral time space register for identifying space in which the container may interact with other containers, processes, systems or gateways.

15. (Currently Amended) The system apparatus of claim 1 or 37, wherein the gateway includes means for acting upon another container, the means for acting upon another container using the plurality of registers to determine whether and how the container acts upon other containers.

16. (Currently Amended) The system apparatus of claim 1 or 37, wherein the gateway includes means for allowing interaction, the means for allowing interaction using the plurality of registers to determine whether and how another container can act upon the container.

17. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the gateway includes means for gathering information, the means for gathering information recording register information from other containers, systems ~~and~~ or processes that interact with the container.

18. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the gateway includes means for reporting information, the means for reporting information providing register information to other containers, systems ~~and~~ or processes that interact with the container.

19. (Currently Amended) The ~~system~~ apparatus of claim 1 or 37, wherein the gateway includes an expert system including rules defining the interaction of the container with other containers, systems ~~and~~ or processes.

20-36. (Cancelled)

37. (New) An apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

an information element having information;

a plurality of registers, the plurality of registers forming part of the container and including

a first register for storing a unique container identification value and

a second register having a representation designating space and governing interactions ofthe container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus three-dimensional space; and

a gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, systems or processes.

REMARKS

Claims 1-36 were pending in the present application. Claims 1-7, 9-10, 14-19 have been amended. Claims 8, 11-13, and 20-36 have been cancelled. Claim 37 has been added.

No new matter has been added by way of this amendment. Support for newly added claim 37 can be found in the specification, for example, at page 5 lines 20-26, page 19 lines 6-7, and page 35 lines 25-30. Reconsideration and reexamination are respectfully requested in view of the amendments and following remarks.

Claim Rejections 35 USC § 102(e)

The Examiner rejected claims 1-36 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,016,495 to McKeehan et al. ("McKeehan"). The applicant respectfully disagrees.

The applicant's invention as defined in the amended claims provides an apparatus for transmitting, receiving, and manipulating information on a computer system, and includes an information element, a plurality of registers, and a gateway. Claim 1 requires a gateway that controls the interactions of the container, and a register that has a representation of time that governs the container's interactions according to the utility of the container's information relative to external-to-the-apparatus time.

In contrast, McKeehan describes an object-oriented framework mechanism that provides an infrastructure for persistent storage. McKeehan does not describe or suggest, as required by amended claim 1, "a second register for controlling the relationship of the container with other containers, systems or processes, the second register having a representation designating time and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus event time".

Rather, McKeehan describes a "LockManager class ... responsible for providing concurrency control of objects stored in persistent containers" (Column 19, lines 25-27). Applicant respectfully submits that McKeehan's LockManager class does not "govern[] interactions ... according to utility of information ... relative to an external-to-the-apparatus

event time", as required by claim 1, because the LockManager class controls simultaneous access to an object by multiple objects without regard to the utility of such access relative to "an external-to-the-apparatus event time."

Applicant's new claim 37 requires a gateway that controls the interactions of the container, and a register that has a representation of space that governs the container's interactions according to the utility of the container's information relative to external-to-the-apparatus space. McKeehan does not describe or suggest, as required by claim 37, "a second register for controlling the relationship of the container with other containers, systems and processes, the second register having a representation designating space and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus three-dimensional space".

As noted by the Examiner with respect to previously asserted claim 11, McKeehan describes a "[c]omputer system 800 [that] utilizes well known virtual addressing mechanisms that allow the programs of computer system 800 to behave as if they only have access to a large, single storage entity (referred to herein as computer system memory) instead of access to multiple, smaller storage entities such as main memory ..." (Column 15, line 66 to column 16, line 5). Applicant respectfully submits that McKeehan's virtual addressing mechanism is limited to the memory of the computer system, and as such, does not suggest "governing interactions ... according to utility of information ... relative to an external-to-the-apparatus three-dimensional space".

Accordingly, the applicant respectfully submits that claim 1, as amended, and new claim 37 are allowable. Claims 2-7, 9-10, and 14-19 depend from claim 1 or 37, and are allowable for at least the reasons discussed for claims 1 or 37. Allowance of claims 1-7, 9-10, 14-19 and 37 is thereby respectfully requested.

Submitted herewith is a petition for revival of the application and associated fees. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: Aug. 9, 2004

Tamara Fraizer
Reg. No. 51,699

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Telephone: (650) 839-5070
Facsimile: (650) 839-5071

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Attorney Docket No.: 17776-002US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999
Title : SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS

Art Unit : 2171
Examiner : Cam N. Nguyen

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	28	-	36	=	0	$0
Independent	2	-	3	=	0	$0
First Presentation of Multiple Dependent Claims						$145
Applicant hereby petitions under 37 C.F.R. §1.136 for a 0 month extension of time.						$0
TOTAL FEE DUE						$145

A check for $145 is attached.

Respectfully submitted,

Date: Aug. 9, 2004

Tamara Fraizer
Reg. No. 51,699

Fish & Richardson P.C.
500 Arguello Street, Suite 500
Redwood City, California 94063
Telephone: (650) 839-5070
Facsimile: (650) 839-5071

50231087.doc

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March 8, 2004
Date of Deposit

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DTW 09-04

Paper No. 12

MICHAEL DE ANGELO
INFORMATION EQUITY CORPORATION
100 SOUTH SUNRISE BOULEVARD, SUITE 470
PALM SPRINGS CA 92262

In re Application of :
Michael DeAngelo :
Application No. 09/284,113 : ON PETITION
Filed: 7 April, 1999 :
Att'y Docket No. 3726-US :

This is a decision on the petition under 1.137(b),[1] filed on 10 August, 2004, to revive the above-identified application.

The petition is **GRANTED**.

This application became abandoned on 12 May, 2003, for failure to timely submit a response to the non-final Office action mailed on 11 February, 2003, which set a three (3) month shortened statutory period for reply. No extensions of the time for reply

---

[1] Effective December 1, 1997, the provisions of 37 CFR 1.137(b) now provide that where the delay in reply was unintentional, a petition may be filed to revive an abandoned application or a lapsed patent pursuant to 37 CFR 1.137(b). A grantable petition filed under the provisions of 37 CFR 1.137(b) must be accompanied by:

(1) the required reply, unless previously filed. In a nonprovisional application abandoned for failure to prosecute, the required reply may be met by the filing of a continuing application. In a nonprovisional application filed on or after June 8, 1995, and abandoned for failure to prosecute, the required reply may also be met by the filing of a request for continued examination in compliance with § 1.114. In an application or patent, abandoned or lapsed for failure to pay the issue fee or any portion thereof, the required reply must be the payment of the issue fee or any outstanding balance thereof. In an application abandoned for failure to pay the publication fee, the required reply must include payment of the publication fee.

(2) the petition fee as set forth in 37 CFR 1.17(m);

(3) a statement that the entire delay in filing the required reply from the due date for the reply until the filing of a grantable petition pursuant to 37 CFR 1.137(b) was unintentional. The Commissioner may required additional information where there is a question whether the delay was unintentional; and

(4) any terminal disclaimer (and fee as set forth in 37 CFR 1.20(d)) required pursuant to 37 CFR 1.137(c)).

in accordance with 37 CFR 1.136(a) were obtained. Notice of Abandonment was mailed on 30 October, 2003.

There is no indication that petition herein was ever empowered to prosecute the instant application. If petitioner desires to receive future correspondence regarding this application, the appropriate power of attorney documentation must be mailed. A courtesy copy of this decision will be mailed to petitioner. However, all future correspondence will be directed to the address of record until such time as appropriate instructions are received to the contrary.

The application file is being forwarded to Technology Center 2100 for further processing.

Telephone inquiries concerning this matter may be directed to the undersigned at (703)308-6918.

Douglas I. Wood
Senior Petitions Attorney
Office of Petitions

cc: Fish & Richardson, P.C.
500 Arguello Street, Suite 500
Redwood City, CA 94063

L Number	Hits	Search Text	DB	Time stamp
1	6057	(plural43 or multiple) near2 register$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:46
2	112782	register$3 near5 (time or lock$3 or clock$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:47
3	50244	register$3 near3 (time)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:14
4	1681	((plural43 or multiple) near2 register$3) and (register$3 near3 (time))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:14
5	947	(((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:15
6	45	((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (707/$ or 709/$).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:37
7	414	interaction$3 near3 (databases or storages or containers)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:38
8	0	((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (interaction$3 near3 (databases or storages or containers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:37
9	1399	interaction$3 with (databases or storages or containers)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:48
10	0	((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (interaction$3 with (databases or storages or containers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:38
11	66	(register$3 near5 (time or lock$3 or clock$3)) and (interaction$3 with (databases or storages or containers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:39
12	66	((register$3 near5 (time or lock$3 or clock$3)) and (interaction$3 with (databases or storages or containers))) not (((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (707/$ or 709/$).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:39
13	23	(((register$3 near5 (time or lock$3 or clock$3)) and (interaction$3 with (databases or storages or containers))) not (((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (707/$ or 709/$).ccls.)) and @AD<19980130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:40

14	6991	(plural43 or multiple) near2 (register$3 or registration or registrative)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:18
15	81602	(register$3 or registration or registrative) near5 time	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:08
16	1389	((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:47
17	0	(interaction$3 with (databases or storages or containers)) and (((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:47
18	31	(register$3 or registration or registrative) with interaction$3 with (databases or storages or containers)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:48
19	6	((register$3 or registration or registrative) with interaction$3 with (databases or storages or containers)) and @AD<19980130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:49
20	98100	(register$3 or registration or registrative).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:51
21	262	(((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative).ti.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:51
22	12321	((register$3 or registration or registrative) with time).clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:51
23	47	((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative).ti.)) and (((register$3 or registration or registrative) with time).clm.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:52
24	47	(((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative).ti.)) and (((register$3 or registration or registrative) with time).clm.)) not (((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (707/$ or 709/$).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 08:52

25	57076	(register$3 or registration or registrative) near3 (time or time$stamp or timestamp)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:14
26	1050	(((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative) near3 (time or time$stamp or timestamp))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:10
27	203	((register$3 or registration or registrative).ti.) and ((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative) near3 (time or time$stamp or timestamp)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:12
29	0	((((register$3 or registration or registrative) with time).clm.) and (((register$3 or registration or registrative).ti.) and ((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative) near3 (time or time$stamp or timestamp))))) not ((((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative).ti.)) and (((register$3 or registration or registrative) with time).clm.)) not (((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (707/$ or 709/$).ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:12
28	43	(((register$3 or registration or registrative) with time).clm.) and (((register$3 or registration or registrative).ti.) and ((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative) near3 (time or time$stamp or timestamp))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:11
30	4	(((register$3 or registration or registrative).ti.) and ((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative) near3 (time or time$stamp or timestamp)))) and (707/$ or 709/$).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:12
31	222	(register$3 or registration or registrative) near3 (time$stamp or timestamp)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:18

32	0	((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near3 (time$stamp or timestamp)) and @AD<19980130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:15
33	68	((register$3 or registration or registrative) near3 (time$stamp or timestamp)) and @AD<19980130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:15
34	68	(((register$3 or registration or registrative) near3 (time$stamp or timestamp)) and @AD<19980130) not ((((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative).ti.)) and (((register$3 or registration or registrative) with time).clm.)) not (((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (707/$ or 709/$).ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:15
35	11	((((register$3 or registration or registrative) near3 (time$stamp or timestamp)) and @AD<19980130) not ((((((plural43 or multiple) near2 (register$3 or registration or registrative)) and ((register$3 or registration or registrative) near5 time) and @AD<19980130) and ((register$3 or registration or registrative).ti.)) and (((register$3 or registration or registrative) with time).clm.)) not (((((plural43 or multiple) near2 register$3) and (register$3 near3 (time))) and @AD<19980130) and (707/$ or 709/$).ccls.))) and interactions	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:15
36	8	((register$3 or registration or registrative) near3 (time$stamp or timestamp)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:18
37	8	((plural43 or multiple) near2 (register$3 or registration or registrative)) same (timestamp$ or time$stamp$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:19
38	1292	((plural43 or multiple) near2 (register$3 or registration or registrative)) same (time or timestamp$ or time$stamp$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:20
39	704	(((plural43 or multiple) near2 (register$3 or registration or registrative)) same (time or timestamp$ or time$stamp$3)) and @AD<19980130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:20
40	325	((((plural43 or multiple) near2 (register$3 or registration or registrative)) same (time or timestamp$ or time$stamp$3)) and @AD<19980130) and ((control or govern$3) with (interaction$3 or operation))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:22

41	16	(((((plural43 or multiple) near2 (register$3 or registration or registrative)) same (time or timestamp$ or time$stamp$3)) and @AD<19980130) and ((control or govern$3) with (interaction$3 or operation))) and (707/$ or 709/$ or 705/$).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/26 09:24

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/284,113	04/07/1999	MICHAEL DE ANGELO	3726-US	1910

7590 11/02/2004

Michael De Angelo
Information Equity Corporation
100 South Sunrise Boulevard, Suite 470
Palm Springs, CA 92262

EXAMINER	
NGUYEN, CAM LINH T	
ART UNIT	PAPER NUMBER
2161	

DATE MAILED: 11/02/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/284,113	DE ANGELO, MICHAEL	
	Examiner	Art Unit	
	CamLinh Nguyen	2161	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *10 August 2004*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-7,9,10,14-19 and 37* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-7,9,10,14-19 and 37* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Response to Amendment

1. Applicant's amendments to claims 1 – 37 are acknowledged. Consequently, claims 8, 11 – 13, 20 – 36 are cancelled. Claims 1 – 7, 9 – 10, 14 – 19, and 37 are currently pending.

Specification

1. This application does not contain an abstract of the disclosure as required by 37 CFR 1.72(b). An abstract on a separate sheet is required.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1 – 7, 10, 15 – 19, and 37 are rejected under 35 U.S.C. 102(e) as being anticipated by Chiussi et al (U.S. 6,075,791).

♦ As per claim 1, 37,

Chiussi discloses an apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

- " An information element having information" See Fig. 1, element 2, col.4, lines 10 - 13.
- " A plurality of registers (Fig. 1 - 3), the plurality of registers forming part of the container and including:

- "A first register for storing a unique container identification value" Fig. 3, element 30-1, col. 5, lines 1 – 2.
- " A second register" See Fig. 3, element 50 – 1, col. 5, lines 4 - 5.

- " A gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, system or processes" See Fig. 1, element 1, and Fig. 2, col. 4, lines 10 - 39.

♦ As per claim 2, Chiussi discloses:

- "The information element is one from the group of text, graphic…a system" col.4, lines 10 – 13 of Chiussi.

♦ As per claim 3 – 4, Chiussi discloses:

- "One container history register for storing information regarding past interaction of the container with other container… modified" See col. 6, lines 46 – 63, of Chiussi.

♦ As per claim 5 – 7, Chiussi discloses:

- "Plurality of registers include at least one predefined register" and "Plurality of registers include a user created register" See col. 17, lines 22.

♦ As per claims 9, 14,

- " An active time register for identifying times" See col. 5, lines 20 – 23 of Chiussi.

♦ As per claim 10, Chiussi discloses:

- "Plurality of registers include at least one acquire register" See col. 6, lines 34 – 41 of Chiussi

♦ As per claim 15 - 19, Chiussi discloses:

Because the server controls the operation of registers, it must including the means of "allowing interaction, gather information, reporting information, and including the rules defining the interaction of the container".

Conclusion

1. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Ramkumar et al (U.S. 6,173,280) discloses a method and apparatus for generating weighted association rules.
- Kawaguchi et al (U.S. 6,154,782) discloses a server switching between communication modes for clients coupled to the server.

- Chang et al (U.S. 6,198,738) discloses a communications between the public switched telephone network and packetized data networks.
- Teper et al (U.S. 5,815,665) discloses a system and method for providing trusted brokering services over a distributed network.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to CamLinh Nguyen whose telephone number is (571) 272-4024. The examiner can normally be reached on Monday - Friday from 8:00 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Safet Metjahic, can be reached on (571) 272- 4023. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 305-3900.

Cam-Linh Nguyen
Art Unit 2161

LN

SAFET METJAHIC
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100

Notice of References Cited	Application/Control No. 09/284,113	Applicant(s)/Patent Under Reexamination DE ANGELO, MICHAEL	
	Examiner CamLinh Nguyen	Art Unit 2161	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,173,280 B1	01-2001	Ramkumar et al.	707/6
	B	US-6,154,782 A	11-2000	Kawaguchi et al.	709/239
	C	US-6,198,738 B1	03-2001	Chang et al.	370/352
	D	US-5,815,665 A	09-1998	Teper et al.	709/229
	E	US-6,075,791 A	06-2000	Chiussi et al.	370/412
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 1910

SERIAL NUMBER	FILING DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
09/284,113	04/07/1999 RULE	707	2161	3726-US

APPLICANTS

MICHAEL DE ANGELO, SANTA BARBARA, CA;

** CONTINUING DATA ********** Yes LN

This application is a 371 of PCT/US99/01988 01/28/1999
which claims benefit of 60/073,209 01/30/1998

** FOREIGN APPLICATIONS ********** None LN

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 04/12/2000

Foreign Priority claimed ☐ yes ☑ no 35 USC 119 (a-d) conditions met ☐ yes ☑ no ☐ Met after Allowance Verified and Acknowledged Examiner's Signature LN Initials	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
	CA	30	~~36~~ 16	2

ADDRESS
Michael De Angelo
Information Equity Corporation
100 South Sunrise Boulevard, Suite 470
Palm Springs , CA
92262

TITLE
SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS

FILING FEE RECEIVED 669	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	☐ All Fees ☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of time) ☐ 1.18 Fees (Issue) ☐ Other _____ ☐ Credit

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Index of Claims

Application No.	Applicant(s)	
09/284,113	DE ANGELO, MICHAEL	
Examiner	**Art Unit**	
CamLinh Nguyen	2161	

√	Rejected	–	(Through numeral) Cancelled	N	Non-Elected	A	Appeal
=	Allowed	÷	Restricted	I	Interference	O	Objected

Claim		Date								
Final	Original	10/26/04								
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Search Notes

Application No.	Applicant(s)	
09/284,113	DE ANGELO, MICHAEL	
Examiner	**Art Unit**	
CamLinh Nguyen	2161	

SEARCHED			
Class	Subclass	Date	Examiner
707	6	10/25/2004	LINH

INTERFERENCE SEARCHED			
Class	Subclass	Date	Examiner

SEARCH NOTES (INCLUDING SEARCH STRATEGY)		
	DATE	EXMR
EAST search	10/25/2004	LINH

Attorney's Docket No.: 17776-002US1

OFFICIAL COMMUNICATION FACSIMILE:

OFFICIAL FAX NO: (703) 872-9306

Number of pages including this page 3 pages

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999

Art Unit : 2171
Examiner : Cam N. Nguyen

Title : System and Method for Creating and Manipulating Information Containers with Dynamic Registers

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is Power of Attorney by Assignee and Election of Assignee to Conduct Prosecution to Exclusion of Inventors, faxed this 3rd day of November, 2004, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: November 3, 2004

Tamara Fraizer
Reg. No. 51,699

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Fax: (650) 839-5071

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Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999
Page : 2 of 2

Attorney's Docket No.: 17776-002US1

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Typed name: Michael DeAngelo

Title:

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Attorney's Docket No.: 17776-002US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999
Title : SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS

Art Unit : 2171
Examiner : Cam N. Nguyen

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

POWER OF ATTORNEY BY ASSIGNEE AND ELECTION OF ASSIGNEE TO CONDUCT PROSECUTION TO EXCLUSION OF INVENTORS

The undersigned, as authorized representative of the assignee of the entire right, title and interest in the above-identified application, hereby appoints

Subroto Bose, Reg. No. 55,014
David J. Goren, Reg. No. 34,609
Brian J. Gustafson, Reg. No. Reg. No. 52,978
Tamara Fraizer, Reg. No. 51,699
Mark D. Kirkland, Reg. No. 40,048
Tim H. Pham, Reg. No. 48,589
Hans R. Troesch, Reg. No. 36,950
Kelvin Vivian, Reg. No. 53,727
Elissa Wang, Reg. No. 48,668
Jennifer Zanocco, Reg. No. 54,563

as its attorneys to prosecute the application and to transact all business in the Patent and Trademark Office connected therewith with full powers of substitution and revocation, said appointment to be to the exclusion of the inventors and their attorney(s) in accordance with the provisions of 37 CFR §3.71 *et seq.* of the Patent Office Rules of Practice.

Ownership is in the assignee by virtue of the assignment documents filed on April 7, 1999. The documents evidencing ownership have been reviewed and to the best of the assignee's knowledge and belief, title is in the assignee.

Please direct all communications regarding the application to the attorney at the address and telephone numbers indicated below.

I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office on the date indicated below.

_____
Date of Transmission

_____
Signature

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/284,113	04/07/1999	MICHAEL DE ANGELO	3726-US	1910

7590 11/02/2004

Michael De Angelo
Information Equity Corporation
100 South Sunrise Boulevard, Suite 470
Palm Springs, CA 92262

EXAMINER	
NGUYEN, CAM LINH T	
ART UNIT	PAPER NUMBER
2161	

DATE MAILED: 11/02/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/284,113	DE ANGELO, MICHAEL	
	Examiner	Art Unit	
	CamLinh Nguyen	2161	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *10 August 2004*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-7,9,10,14-19 and 37* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-7,9,10,14-19 and 37* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Response to Amendment

1. Applicant's amendments to claims 1 – 37 are acknowledged. Consequently, claims 8, 11 – 13, 20 – 36 are cancelled. Claims 1 – 7, 9 – 10, 14 – 19, and 37 are currently pending.

Specification

1. This application does not contain an abstract of the disclosure as required by 37 CFR 1.72(b). An abstract on a separate sheet is required.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1 – 7, 10, 15 – 19, and 37 are rejected under 35 U.S.C. 102(e) as being anticipated by Chiussi et al (U.S. 6,075,791).

◊ As per claim 1, 37,

Chiussi discloses an apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

- " An information element having information" See Fig. 1, element 2, col.4, lines 10 - 13.
- " A plurality of registers (Fig. 1 - 3), the plurality of registers forming part of the container and including:

- "A first register for storing a unique container identification value" Fig. 3, element 30-1, col. 5, lines 1 – 2.
- " A second register" See Fig. 3, element 50 – 1, col. 5, lines 4 - 5.

- " A gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, system or processes" See Fig. 1, element 1, and Fig. 2, col. 4, lines 10 - 39.

◆ As per claim 2, Chiussi discloses:

- "The information element is one from the group of text, graphic…a system" col.4, lines 10 – 13 of Chiussi.

◆ As per claim 3 – 4, Chiussi discloses:

- "One container history register for storing information regarding past interaction of the container with other container… modified" See col. 6, lines 46 – 63, of Chiussi.

◆ As per claim 5 – 7, Chiussi discloses:

- "Plurality of registers include at least one predefined register" and "Plurality of registers include a user created register" See col. 17, lines 22.

◆ As per claims 9, 14,

- " An active time register for identifying times" See col. 5, lines 20 – 23 of Chiussi.

◆ As per claim 10, Chiussi discloses:

- "Plurality of registers include at least one acquire register" See col. 6, lines 34 – 41 of Chiussi

◆ As per claim 15 - 19, Chiussi discloses:

Because the server controls the operation of registers, it must including the means of "allowing interaction, gather information, reporting information, and including the rules defining the interaction of the container".

Conclusion

1. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Ramkumar et al (U.S. 6,173,280) discloses a method and apparatus for generating weighted association rules.
- Kawaguchi et al (U.S. 6,154,782) discloses a server switching between communication modes for clients coupled to the server.

- Chang et al (U.S. 6,198,738) discloses a communications between the public switched telephone network and packetized data networks.
- Teper et al (U.S. 5,815,665) discloses a system and method for providing trusted brokering services over a distributed network.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to CamLinh Nguyen whose telephone number is (571) 272-4024. The examiner can normally be reached on Monday - Friday from 8:00 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Safet Metjahic, can be reached on (571) 272- 4023. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 305-3900.

Cam-Linh Nguyen
Art Unit 2161

LN

SAFET METJAHIC
SORY PATENT EXAMINER
LOGY CENTER 2100

Notice of References Cited	Application/Control No. 09/284,113	Applicant(s)/Patent Under Reexamination DE ANGELO, MICHAEL	
	Examiner CamLinh Nguyen	Art Unit 2161	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,173,280 B1	01-2001	Ramkumar et al.	707/6
	B	US-6,154,782 A	11-2000	Kawaguchi et al.	709/239
	C	US-6,198,738 B1	03-2001	Chang et al.	370/352
	D	US-5,815,665 A	09-1998	Teper et al.	709/229
	E	US-6,075,791 A	06-2000	Chiussi et al.	370/412
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	42605	(plural$3 or multiple) near3 register$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:18
L2	28564	1 and @AD<"19990407"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 09:50
L3	16264	active with register$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 09:50
L4	688	passive with register$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 09:51
L5	649	neutral with register$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 09:51
L6	0	2 and 3 and 4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 09:51

L7	2824	2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 09:51
L8	25	2 and 3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:16
L9	52525	register$3 with (ID or IDs or identif$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:17
L10	5583	2 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:17
L11	117	10 and "707"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:17
L12	6831	(register$3 with (ID or IDs or identif$6)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:17

L13	17321	((plural$3 or multiple) near3 register$3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:18
L14	1032	12 and 13 and @AD<"19980130"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:18
L15	19	14 and "707"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:22
L16	3	14 and 3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:23
L17	0	14 and 3 and 4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/30 10:23

Attorney's Docket No.: 17776-002US1

OFFICIAL COMMUNICATION FACSIMILE:

OFFICIAL FAX NO: (703) 872-9306

Number of pages including this page 15

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999

Art Unit : 2171
Examiner : Cam N. Nguyen

Title : System and Method for Creating and Manipulating Information Containers with Dynamic Registers

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is an AMENDMENT IN REPLY TO ACTION OF NOVEMBER 2, 2004, faxed this 3rd day of Janurary, 2005, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: Janurary 3, 2004

Dorian Cartwright
Reg. No. 53,853

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Attorney's Docket No.: 17776-002US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : DeAngelo, Michael
Serial No. : 09/284,113
Filed : April 7, 1999
Title : SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS

Art Unit : 2171
Examiner : Cam N. Nguyen

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INTERVIEW SUMMARY AND AMENDMENT
IN REPLY TO FINAL OFFICE ACTION OF NOVEMBER 2, 2004

Please amend the above-identified application as follows:

I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office on the date indicated below.

1/3/05
Date of Transmission

Dorian Cartwright
Signature

Dorian Cartwright

Amendments to the Specification:

Please delete previous abstract at page 50 and add the following new abstract, a clean version of which is also attached on a separate sheet:

--An apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising an information element, a plurality of registers, and a gateway. The plurality of registers, form part of the container, and include a first register for storing a unique container identification value; a second register having a representation designating time and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus event time; an active time register for identifying times at which the container will act upon other containers, processes, systems or gateways; a passive time register for identifying times at which the container can be acted upon by other containers, processes, systems or gateways; and a neutral time register for identifying times at which the container may interact with other containers, processes, systems or gateways. Additional registers designate space for container interactions.--

Amendments to the Claims:

Claims 1, 10 and 37 are amended. Claims 9 and 14 are cancelled. Claims 38 and 39 are added. This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (Currently amended) An apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

an information element having information;

a plurality of registers, the plurality of registers forming part of the container and including

a first register for storing a unique container identification value, ~~and~~

a second register having a representation designating time and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus event time,

an active time register for identifying times at which the container will act upon other containers, processes, systems or gateways,

a passive time register for identifying times at which the container can be acted upon by other containers, processes, systems or gateways, and

a neutral time register for identifying times at which the container may interact with other containers, processes, systems or gateways; and

a gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, systems or processes.

2. (Previously presented) The apparatus of claim 1 or 37, wherein the information element is one from the group of text, graphic images, video, audio, a digital pattern, a process, a nested container, bit, natural number and a system.

3. (Previously presented) The apparatus of claim 1 or 37, wherein the plurality of registers includes at least one container history register for storing information regarding past interaction of the container with other containers, systems or processes, the container history register being modifiable.

4. (Previously presented) The apparatus of claim 1 or 37, wherein the plurality of registers includes at least one system history register for storing information regarding past interaction of the container with different operating system and network processes.

5. (Previously presented) The apparatus of claim 1 or 37, wherein the plurality of registers includes at least one predefined register, the predefined register being a register associated with an editor for user selection and being appendable to any container.

6. (Previously presented) The apparatus of claim 1 or 37, wherein the plurality of registers includes a user-created register, the user-created register being generated by the user, and being appendable to any container.

7. (Previously presented) The apparatus of claim 1 or 37, wherein the plurality of registers includes a system-defined register, the system-defined register being set, controlled and used by the system, and being appendable to any container.

8. (Cancelled)

9. (Cancelled)

10. (Currently amended) The apparatus of claim 1 or 37, wherein the plurality of registers includes at least one acquire register for controlling whether the container adds a register ~~or a container~~ from other containers or adds a container from other containers when interacting with them.

11-13. (Cancelled)

14. (Cancelled)

15. (Previously presented) The apparatus of claim 1 or 37, wherein the gateway includes means for acting upon another container, the means for acting upon another container using the plurality of registers to determine whether and how the container acts upon other containers.

16. (Previously presented) The apparatus of claim 1 or 37, wherein the gateway includes means for allowing interaction, the means for allowing interaction using the plurality of registers to determine whether and how another container can act upon the container.

17. (Previously presented) The apparatus of claim 1 or 37, wherein the gateway includes means for gathering information, the means for gathering information recording register information from other containers, systems or processes that interact with the container.

18. (Previously presented) The apparatus of claim 1 or 37, wherein the gateway includes means for reporting information, the means for reporting information providing register information to other containers, systems or processes that interact with the container.

19. (Previously presented) The apparatus of claim 1 or 37, wherein the gateway includes an expert system including rules defining the interaction of the container with other containers, systems or processes.

20-36. (Cancelled)

37. (Currently amended) An apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

an information element having information;

a plurality of registers, the plurality of registers forming part of the container and including

a first register for storing a unique container identification value, ~~and~~

a second register having a representation designating space and governing interactions ~~ofthe~~ of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus three-dimensional space,

an active space register for identifying space in which the container will act upon other containers, processes, systems or gateways,

a passive space register for identifying space in which the container can be acted upon by other containers, processes, systems or gateways,

a neutral space register for identifying space in which the container may interact with other containers, processes, systems or gateways; and

a gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, systems or processes.

38. (New) An apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

an information element having information;

a plurality of registers, the plurality of registers forming part of the container and including

a first register for storing a unique container identification value,

a second register having a representation designating time and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus event time, and

at least one acquire register for controlling whether the container adds a register from other containers or adds a container from other containers when interacting with them; and

a gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, systems or processes.

39. (New) An apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising:

an information element having information;

a plurality of registers, the plurality of registers forming part of the container and including

a first register for storing a unique container identification value,

a second register having a representation designating time and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus event time, and

at least one acquire register for controlling whether the container adds a register from other containers or adds a container from other containers when interacting with them; and

a gateway attached to and forming part of the container, the gateway controlling the interaction of the container with other containers, systems or processes.

REMARKS

Claims 1-7, 9, 10, 14-19, and 37 were presented for examination. In a final office action mailed November 2, 2004, Examiner objected to the specification, and rejected claims 1-7, 9, 10, 14-19, and 37 under 35 U.S.C. § 102(e). On January 29, 2004, Examiner granted a telephone interview with Applicant's attorney.

In response, the specification is amended. Claims 1, 10 and 37 are amended. Claims 38 and 39 are added without introducing any new matter. Claims 9 and 14 are cancelled. Applicant thanks Examiner for examination and the subsequent interview, and now requests reconsideration of claims 1-7, 10, 15-19, and 37-39 in light of the following remarks.

I. Summary of the Interview

During the interview, Applicant argued that Chiussi fails to disclose containers configured for interactions with other containers as recited in independent claim 1 as an example. Furthermore, Applicant argued that Chiussi fails to disclose an active time register, a passive time register and/or a neutral time register as disclosed in dependent claim 9. While failing to reach agreement with respect to claim 1, Examiner agreed to reconsider claim 9 and other claims based on a formal communication.

II. Objection to the Specification

The specification was objected to because, according to the final action, the application did not contain an abstract of the disclosure. Applicant has amended the specification to include a new abstract. Applicant has also included the abstract a separate sheet. Therefore, Applicant respectfully submits that the specification should no longer be objected to.

III. Rejections under § 102(e)

Claims 1-7, 9, 10, 14-19, and 37 were rejected under § 102(e) as being anticipated by U.S. Patent No. 6,075,791 issued to Chiussi et al. ("Chiussi"). Applicant respectfully traverses the rejections as follows.

A. Claim 1

Claim 1 has been amended to include the limitations of claim 9, and is directed to an apparatus including a plurality of containers. Claim 1 further recites that each container comprises an active time register, a passive time register, and a neutral time register. Since the limitations are identical to a previously submitted claim, Applicant submits that no further searching is necessary.

Chiussi discloses a server 100 which services a plurality of queues having guaranteed data transfer rates and data transfer delays. (Abstract). A queue contains a connection identifier register 30-i, and a time stamp register 50-i. (col. 4, ln. 67-col. 5, ln. 5). The server 100 generates a new timestamp when a new packet reaches the head of a queue. (Col. 5, ll. 20-22)

However, Chiussi does not teach or suggest limitations recited in claim 1. Specifically, while the "active time register" of claim 1 identifies "times at which the container will act" (i.e., "upon other containers, processes, systems, or gateways"), the queue of Chiussi merely logs a time that a new packet reached the head of queue. Similarly, the "passive time register" of claim 1 identifies "times at which the container can be acted upon", and the "neutral time register" identifies "times at which the container may interact," neither of which are disclosed by Chiussi. Moreover, while claim 1 recites containers having a "second register...governing interactions with other containers," the queues of Chiussi are unable to interact with other queues because they have no awareness of other queues. Nor does Chiussi disclose any specific time parameters for such interaction. Thus, Chiussi fails to disclose the active time register, the passive time register, the neutral time register, or the second register as recited in claim 1.

Because Chiussi does not disclose every limitation of claim 1, Applicant respectfully submits that claim 1 is patentable over Chiussi.

B. Claim 37

Claim 37 has been amended to include the limitations of claim 14, and is directed to an apparatus including a plurality of containers. Claim 37 further recites that each container comprises an active space register, a passive space register, and a neutral space register. Since the limitations are identical to a previously submitted claim, Applicant submits that no further searching is necessary.

Examiner relies on the same disclosure for claim 37 as discussed above with respect to claim 1.

However, Chiussi does not teach or suggest limitations recited in claim 37. Specifically, the cited portions of Chiussi, in disclosing merely a connection identification register and a timestamp register, do not disclose a " second register designating space and governing interactions of the container with other containers" as recited in claim 37. Similarly, Chiussi does not specifically disclose an "active space register" to identify a "space in which the container will act upon, a "passive space register" to identify a "space in which the container can be acted upon," nor the "neutral space register" to identify a "space in which the container may interact." Also, while claim 37 recites a "second register...governing interactions of the container with other containers," the queues of Chiussi are unable to interact with other queues as discussed. Thus, Chiussi fails to disclose the active space register, the space time register, the neutral space register, or the second register as recited in claim 37.

Because Chiussi does not disclose every limitation of claim 37, , Applicant respectfully submits that claim 37 is patentable over Chiussi.

C. Claim 38

New claim 38 includes the limitations of previously presented claims 1 and 10, and is directed to an apparatus including a plurality of containers. Claim 38 further recites that each container comprises at least one acquire register. Because the limitations are identical to a previously submitted claim, Applicant submits that no further searching is necessary.

During the course of the interview, Examiner indicated that Chiussi discloses a server 100 that increments the content of register 115, that increments the content of queue length

register 60-i, that mathematically or logically adds the content of rate register 40-i to the content of register 110, and that adds the product of the contents of timestamp register 50-i and rate register 40-i to the content of register 123. (Col. 8, ll. 3-24).

However, Chiussi does not teach or suggest limitations recited in claim 38 Specifically, the containers of claim 38 include an "acquire register" that can "control[] whether the container adds a register from other containers or adds a container from other containers." Thus, the container of claim 38 can acquire the register itself from another container rather than merely mathematically adding (or logically adding) the contents of two registers together as disclosed in Chiussi. Moreover, whereas the container of claim 38 can condition "whether" a register or container is added to the container the queues of Chiussi present no conditions since the centrally-controlled queues are unaware that other queues exist . Nor does the server of Chiussi assist in the acquisition of a register of one queue by another queue. As such, the queues of Chiussi are not able add registers directly from other queues. Nor are the queues able to add another queue. Moreover, the purpose of Chiussi, for sharing communication access between queues as designated by the server, would be foreclosed by adding one queue to another queue. Thus, Chiussi fails to disclose the acquire register as recited in claim 38.

Because Chiussi does not disclose every limitation of claim 38, Applicant respectfully submits that claim 38 is patentable over Chiussi.

D. Claim 39

New claim 39 includes the limitations of previously presented claims 37 and 10, and is directed to an apparatus including a plurality of containers. Claim 39 further recites that each container comprises at least one acquire register. Since the limitations are identical to a previously submitted claim, Applicant submits that no further searching is necessary.

Examiner relies on the same disclosure for claim 39 as discussed above with respect to claim 1.

However, Chiussi does not teach or suggest limitations recited in claim 39 for at least the same reasons as discussed above with respect to claim 39. Also, as discussed with respect to

claim 37, Chiussi fails to disclose a register designating space. Thus, Chiussi fails to disclose the acquire register as recited in claim 38.

Because Chiussi does not disclose every limitation of claim 39, Applicant respectfully submits that claim 39 is patentable over Chiussi.

E. Dependent Claims

Because claims 2-7, 10, and 15-19 depend from patentable base claims, these claims are patentable for at least the same reasons.

Please apply $200 for excess claim fees, and any other charges or credits, to deposit account 06-1050.

Respectfully submitted,

Date: 1/3/05

Dorian Cartwright
Reg. No. 53,583

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50249246.doc

ABSTRACT OF THE DISCLOSURE

An apparatus for transmitting, receiving and manipulating information on a computer system, the apparatus including a plurality of containers, each container being a logically defined data enclosure and comprising an information element, a plurality of registers, and a gateway. The plurality of registers, form part of the container, and include a first register for storing a unique container identification value; a second register having a representation designating time and governing interactions of the container with other containers, systems or processes according to utility of information in the information element relative to an external-to-the-apparatus event time; an active time register for identifying times at which the container will act upon other containers, processes, systems or gateways; a passive time register for identifying times at which the container can be acted upon by other containers, processes, systems or gateways; and a neutral time register for identifying times at which the container may interact with other containers, processes, systems or gateways. Additional registers designate space for container interactions.

PATENT APPLICATION FEE DETERMINATION RECORD
Effective Nov mber 10, 1998

Application or Docket Number
09/284113

CLAIMS AS FILED - PART I

FOR	(Column 1) NUMBER FILED	(Column 2) NUMBER EXTRA
BASIC FEE		
TOTAL CLAIMS	36 minus 20=	* 16
INDEPENDENT CLAIMS	3 minus 3 =	*
MULTIPLE DEPENDENT CLAIM PRESENT		

SMALL ENTITY TYPE ☒ OR OTHER THAN SMALL ENTITY

RATE	FEE	OR	RATE	FEE
	380.00	OR		~~760.00~~
X$ 9=	$144	OR	X$18=	
X39=		OR	X78=	
+130=		OR	+260=	
TOTAL	$524	OR	TOTAL	

* If the difference in column 1 is less than zero, enter "0" in column 2

8/10/04 **CLAIMS AS AMENDED - PART II**

AMENDMENT A	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA
Total	* 16	Minus	** 36	= —
Independent	* 2	Minus	*** 3	= —
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM				

SMALL ENTITY OR OTHER THAN SMALL ENTITY

RATE	ADDITIONAL FEE	OR	RATE	ADDITIONAL FEE
X$ 9=		OR	X$18=	
X39=		OR	X78=	
+130=		OR	+260=	
TOTAL ADDIT. FEE	0	OR	TOTAL ADDIT. FEE	

1/3/05

AMENDMENT B	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA
Total	* 16	Minus	** 36	=
Independent	* 4	Minus	*** 3	= 1
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM				

RATE	ADDITIONAL FEE	OR	RATE	ADDITIONAL FEE
X$ 9=		OR	X$18=	
X39=		OR	~~X78=~~ 88	88
+130=		OR	+260=	
TOTAL ADDIT. FEE		OR	TOTAL ADDIT. FEE	88

AMENDMENT C	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA
Total	*	Minus	**	=
Independent	*	Minus	***	=
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM				

RATE	ADDITIONAL FEE	OR	RATE	ADDITIONAL FEE
X$ 9=		OR	X$18=	
X39=		OR	X78=	
+130=		OR	+260=	
TOTAL ADDIT. FEE		OR	TOTAL ADDIT. FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20."
***If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3."
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

FORM PTO-875
(Rev. 11/98)

Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
☆ U.S. GPO:1998-454-473/90301

STIC Search Report
EIC 2100

STIC Database Tracking Number: 141562

TO:	From:
Cam-Linh T Nguyen **Location: RND 3C21** **Art Unit : 2161** **Tuesday, January 04, 2005** **Case Serial Number: 09/284113**	**David Holloway** **Location: EIC 2100** **RND 4B19** **Phone: 2-3528** **david.holloway@uspto.gov**

Search Notes

Dear Examiner Nguyen,

Attached please find your search results for above-referenced case.
Please contact me if you have any questions or would like a re-focused search.

David

P fuse 24

D. N

SEARCH REQUEST FORM

Scientific and Technical Information Center

Access DB# ________

Requester's Full Name: Nguyen, Cam Linh Examiner #: 78921 Date: 12/30/04
Art Unit: 2161 Phone Number 30 2-4024 Serial Number: 09/284,43
Mail Box and Bldg/Room Location: RND 3C21 Results Format Preferred (circle): (PAPER) DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

****

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: System & method for Creating and Manipulating Information containers with Dynamic Registers

Inventors (please provide full names): DeAngelo, Michael

Earliest Priority Filing Date: 1/30/98

***For Sequence Searches Only* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.**

See claims

****

STAFF USE ONLY	**Type of Search**	**Vendors and cost where applicable**
Searcher: David Holloway	NA Sequence (#) ____	STN ____
Searcher Phone #: 2-3528	AA Sequence (#) ____	Dialog # 721
Searcher Location: RND 4B19	Structure (#) ____	Questel/Orbit ____
Date Searcher Picked Up: 1-3-05	Bibliographic ✓	Dr.Link ____
Date Completed: 1-4-04	Litigation ____	Lexis/Nexis ____
Searcher Prep & Review Time: 50	Fulltext ✓	Sequence Systems ____
Clerical Prep Time: ____	Patent Family ____	WWW/Internet ✓
Online Time: 150	Other ____	Other (specify) ____

PTO-1590 (8-01)

```
Set     Items   Description
S1     272090   CONTAINER? OR ENVELOPE? OR BUCKET? OR (DATA OR INFORMATION-
            )()(ENCLOSURE? OR RECEPTACLE? OR FOLDER?)
S2     220678   REGISTER? OR REGISTR? OR (STORAGE OR MEMORY)(N)(LOCATION? -
            OR AREA OR AREAS OR ADDRESS? OR SECTOR? OR REGION?)
S3      19487   S2(2N)(MULTIPLE OR MULTIPLICITY OR PLURAL OR PLURALITY OR -
            MULTIPLICITY OR SEVERAL OR DIFFERENT OR MANY OR VARIOUS OR VA-
            RIETY)
S4        330   S3(5N)(ALTERABLE OR DYNAMIC? OR CHANGE? OR MODIFY? OR REVI-
            S? OR EDIT? OR LIVE OR HOT)
S5    1245365   TIME? OR SCHEDUL? OR HOUR? OR CALENDAR? OR TIMING OR TIMING
             OR DURATION? OR INTERVAL?
S6         87   S4(S)S5
S7         12   S1(10N)S3(10N)S5
S8          4   S1(S)S4(S)S5
S9         11   S6 AND IC=(G06F-017? OR G06F-007?)
S10        25   S7 OR S8 OR S9
S11        48   S6 AND IC=G06F?
S12        59   S10 OR S11
S13        42   S12 NOT AD=19980130:20010130
S14        36   S13 NOT AD=20010130:20030130
S15        35   S14 NOT AD=20030130:20050103
S16        11   S15 AND S1
S17        11   IDPAT (sorted in duplicate/non-duplicate order)
S18        11   IDPAT (primary/non-duplicate records only)
File 348:EUROPEAN PATENTS 1978-2004/Dec W03
         (c) 2004 European Patent Office
File 349:PCT FULLTEXT 1979-2002/UB=20041230,UT=20041223
         (c) 2004 WIPO/Univentio
```

18/3,K/2 (Item 2 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00937647
System and method for parsing multiple sets of data
System und Verfahren zur Analyse mehrerer Datenmengen
Systeme et procede pour l'analyse de plusieurs ensembles de donnees
PATENT ASSIGNEE:
Hewlett-Packard Company, (206030), 3000 Hanover Street, Palo Alto, California 94304, (US), (Applicant designated States: all)
INVENTOR:
Pakenham, Gene, 5243 W 11th No. 1812, Greeley, Co 80634, (US)
DeVore, Darwin A., 1221 Eastlake Court, Loveland, CO 80537, (US)
LEGAL REPRESENTATIVE:
Carpmaels & Ransford (101821), 43 Bloomsbury Square, London WC1A 2RA, (GB)
PATENT (CC, No, Kind, Date): EP 853418 A2 980715 (Basic)
EP 853418 A3 000705
APPLICATION (CC, No, Date): EP 97309785 971204;
PRIORITY (CC, No, Date): US 782729 970113
DESIGNATED STATES: DE; FR; GB
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS: H04N-001/40; H04N-001/64
ABSTRACT WORD COUNT: 43
NOTE:
Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9829	351
SPEC A	(English)	9829	5723
Total word count - document A			6074
Total word count - document B			0
Total word count - documents A + B			6074

...SPECIFICATION from the CCD cells into a sequential or serial data stream.

A typical analog shift **register** comprises a **plurality** of "charge transfer **buckets** " each of which is connected to an individual cell. At the end of the exposure **time** , the charges collected by each of the CCD cells are simultaneously transferred to the charge transfer **buckets** , thus preparing the CCD cells for the next exposure sequence. The charge in each **bucket** is then transferred from **bucket** to **bucket** out of the shift register in a sequential or " **bucket** brigade" fashion during the time the CCD cells are being exposed to the next scan...

18/3,K/10 (Item 10 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2004 WIPO/Univentio. All rts. reserv.

00300850 **Image available**
UPDATE MECHANISM FOR COMPUTER STORAGE CONTAINER MANAGER
MOYEN DE MISE A JOUR POUR MODULE DE GESTION D'ELEMENTS DE STOCKAGE D'ORDINATEURS
Patent Applicant/Assignee:
APPLE COMPUTER INC,
Inventor(s):
HARRIS Jared M,
RUBEN Ira L,
Patent and Priority Information (Country, Number, Date):
Patent: WO 9519001 A1 19950713
Application: WO 95US196 19950104 (PCT/WO US9500196)
Priority Application: US 94177853 19940105
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AM AT AU BB BG BR BY CA CH CN CZ DE DK EE ES FI GB GE HU JP KE KG KP KR KZ LK LR LT LU LV MD MG MN MW MX NL NO NZ PL PT RO RU SD SE SI SK TJ TT UA UZ VN KE MW SD SZ AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG
Publication Language: English
Fulltext Word Count: 119635

Fulltext Availability:
Claims

Claim
... the TOC and global name table of
the target, Up to this point the updating **container**
has basically been opened "normally" like any other.
It has its own TOC and global...

...that will eventually be returned to the user
will be the one for the updating **container** , so the
targetFs TOC and global name table must be inherited
by the updating CCB,
The way the updating **container** gets to use the
...simply to copy
the target's TOC and global name table pointers into
the updating **container** CCB, But the pointers to the
tables already there can't simply be clobbered. They...

...global name table pointers used by
everyone, and the "private" pair mainly used by
close- **time** processing.
One other pointer is inherited. That is a pointer
referred to as the "target **container** pointer"
(targetContainer), It is a CCB pointer copied from
the target. It is always initialized...

...both "All and "B"s
targetContainer will point to "B", This pointer is
used for **container** refNum validity checks in the
various API routines, It is the opposite of the
updatingContainer pointer mentioned in step (1).
updatingContainer points to the top-most **container** ,
and targetContainer the bottom-most (final or ultimate
target).
(5). Load in updater's non-private TOC
If this is a previously existing updating
container opened for reading, then it is at this point
all the updates from the updating **container** are
applied to the target, The non-private portion of the
updating **container** 's TOC was loaded first in step (3),

Since the normal TOC is now the...

...updating" list properties for the objects they update will be encountered. As discussed for close- **time** processing, these will be value operations (set-infos, data edits, moves, etc.). The value data...

...represent all objects needing updating. The touched chain can now be walked much like close- **time** processing to process the updating instructions associated with the "updating" property of each object on the chain. Also like close- **time** processing, objects on the touched chain are removed from the chain after each updating list...

...size and offsets are generated for the value headers and segments. Applying updates at this **time** changes the logical sizes and offsets. Thus, after each value's updates are completed, if...

...the value must be "re-logicalized". It should also be pointed out that during this **time** , recording of updates is suppressed. It stays suppressed until the end of all open processing...

...and property updating instructions can be processed using the special TOC #1 property of the **container** 's private TOC. At this point the target And the updating **container** have been opened, The updater's CCB pointer is returned to the user as the **container** refNum, The diagram of Fig. 22 illustrates the pertinent data structures discussed above. In the...

...target point to the same tables. Since "All is opened first, then "B". the close- **time** processing reverses this by closing "B" then "A", In order to prevent the closing of...

...the TOC and global name tables. This prevents premature release of the data.

H. Open- **time** Processing for Multi-layered Updaters

The previous discussion was mainly limited to one **container** updating another, It is fairly simple extension to the algorithm to allow for multiple updaters. Multiple updaters arise if a new **container** is opened for updating a target in multiple sessions. For example, the above situation was other hand, there is nothing preventing another new updating **container** from being opened,, say "XI', and using "All as its target; IIX updating "All updating...

...The process is the same, except for one variation in step (3) of the open- **time** processing; opening of the target **container** . Basically, as part of standard open- **time** processing, a check is always made to see if TOC #1 has a "Pointing value". This only exists in updating **container** TOCs and allows access to the proper target, be it separate or appended. If the...

...TOC is present in memory that is indistinguishable from that of an ordinary, non-update **container** , except that some of the values refer to data actually present in other **containers** . Each of the **containers** in the update chain remain open

so that value operations can reach the data, I...

...Appendix D is a C-language header file for routines which read and write the **container** TOC. Appendix E sets forth the routines themselves, Appendix F is a C-language header file for a set of basic **container** handlers, and their metahandler, used by the **Container** Manager when doing update operations on a target **container** . Appendix G sets forth the handlers themselves.
The foregoing description of preferred embodiments of the...

...to
practitioners skilled in this art. As one example, an embodiment may permit an update **container** to be an update of two or more target **containers** concurrently, As another example, an implementation of the routines may construct only those aspects of the TOC in-memory which are needed for a particular operation after an update **container** is opened. The embodiments described herein were chosen and described in order to best explain...

```
Set     Items   Description
S1     905852   CONTAINER? OR ENVELOPE? OR BUCKET? OR (DATA OR INFORMATION-
            )()(ENCLOSURE? OR RECEPTACLE? OR FOLDER?)
S2    3635562   REGISTER? OR REGISTR? OR (STORAGE OR MEMORY)(N)(LOCATION? -
            OR AREA OR AREAS OR ADDRESS? OR SECTOR? OR REGION?)
S3      38232   S2(3N)(MULTIPLE OR MULTIPLICITY OR PLURAL OR PLURALITY OR -
            MULTIPLICITY OR SEVERAL OR DIFFERENT OR MANY OR VARIOUS OR VA-
            RIETY)
S4     142842   S2(5N)(ALTERABLE OR DYNAMIC? OR CHANGE? OR MODIFY? OR REVI-
            S? OR EDIT? OR LIVE OR HOT)
S5      50584   S1(12N)(TIME? OR SCHEDUL? OR HOUR? OR CALENDAR? OR TIMING -
            OR TIMING OR DURATION? OR INTERVAL?)
S6          0   S3(S)S4(S)S5
S7          6   S4(S)S5
S8          4   S3(S)S5
S9        570   S2(S)S5
S10       210   S2(10N)S5
S11         5   S10(10N)(ALTERABLE OR DYNAMIC? OR CHANGE? OR MODIFY? OR RE-
            VIS? OR EDIT? OR LIVE OR HOT)
S12        15   S11 OR S8 OR S7
S13         9   RD (unique items)
S14       146   S2(5N)S5
S15        57   S14(S)(DATA OR BIT OR BYTE? OR DATABLOCK? OR INFORMATION? -
            OR MEMOR? OR STORAGE? OR BUFFER? OR CACHE?)
S16        37   RD (unique items)
S17        45   S16 OR S13
S18        45   RD (unique items)
S19        23   S18 NOT PY>1998
S20        21   S19 NOT PD=19980130:20010130
S21        21   S20 NOT PD=20010130:20050110
File 275:Gale Group Computer DB(TM) 1983-2005/Jan 04
         (c) 2005 The Gale Group
File  47:Gale Group Magazine DB(TM) 1959-2005/Jan 04
         (c) 2005 The Gale group
File  75:TGG Management Contents(R) 86-2004/Dec W1
         (c) 2004 The Gale Group
File 636:Gale Group Newsletter DB(TM) 1987-2005/Jan 04
         (c) 2005 The Gale Group
File  16:Gale Group PROMT(R) 1990-2005/Jan 04
         (c) 2005 The Gale Group
File 624:McGraw-Hill Publications 1985-2004/Dec 28
         (c) 2004 McGraw-Hill Co. Inc
File 484:Periodical Abs Plustext 1986-2004/Dec W4
         (c) 2004 ProQuest
File 613:PR Newswire 1999-2005/Jan 03
         (c) 2005 PR Newswire Association Inc
File 813:PR Newswire 1987-1999/Apr 30
         (c) 1999 PR Newswire Association Inc
File 141:Readers Guide 1983-2004/Sep
         (c) 2004 The HW Wilson Co
File 370:Science 1996-1999/Jul W3
         (c) 1999 AAAS
File 696:DIALOG Telecom. Newsletters 1995-2005/Jan 03
         (c) 2005 The Dialog Corp.
File 553:Wilson Bus. Abs. FullText 1982-2004/Sep
         (c) 2004 The HW Wilson Co
File 621:Gale Group New Prod.Annou.(R) 1985-2005/Jan 04
         (c) 2005 The Gale Group
File 674:Computer News Fulltext 1989-2004/Dec W2
         (c) 2004 IDG Communications
File  88:Gale Group Business A.R.T.S. 1976-2005/Dec 30
         (c) 2005 The Gale Group
File 369:New Scientist 1994-2004/Dec W3
         (c) 2004 Reed Business Information Ltd.
File 160:Gale Group PROMT(R) 1972-1989
         (c) 1999 The Gale Group
File 635:Business Dateline(R) 1985-2005/Jan 01
         (c) 2005 ProQuest Info&Learning
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File 15:ABI/Inform(R) 1971-2005/Jan 01
(c) 2005 ProQuest Info&Learning
File 9:Business & Industry(R) Jul/1994-2005/Jan 03
(c) 2005 The Gale Group
File 13:BAMP 2005/Dec W4
(c) 2005 The Gale Group
File 810:Business Wire 1986-1999/Feb 28
(c) 1999 Business Wire
File 610:Business Wire 1999-2005/Jan 03
(c) 2005 Business Wire.
File 647:CMP Computer Fulltext 1988-2005/Dec W3
(c) 2005 CMP Media, LLC
File 98:General Sci Abs/Full-Text 1984-2004/Sep
(c) 2004 The HW Wilson Co.
File 148:Gale Group Trade & Industry DB 1976-2004/Jan 03
(c)2004 The Gale Group
File 634:San Jose Mercury Jun 1985-2004/Dec 31
(c) 2005 San Jose Mercury News

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Set     Items   Description
S1     325405   CONTAINER? OR ENVELOPE? OR BUCKET? OR (DATA OR INFORMATION-
            )()(ENCLOSURE? OR RECEPTACLE? OR FOLDER?)
S2     304185   REGISTER? OR REGISTR? OR (STORAGE OR MEMORY)(N)(LOCATION? -
            OR AREA OR AREAS OR ADDRESS? OR SECTOR? OR REGION?)
S3       5930   S2(2N)(MULTIPLE OR MULTIPLICITY OR PLURAL OR PLURALITY OR -
            MULTIPLICITY OR SEVERAL OR DIFFERENT OR MANY OR VARIOUS OR VA-
            RIETY)
S4         91   S3(5N)(ALTERABLE OR DYNAMIC? OR CHANGE? OR MODIFY? OR REVI-
            S? OR EDIT? OR LIVE OR HOT)
S5    8070002   TIME? OR SCHEDUL? OR HOUR? OR CALENDAR? OR TIMING OR TIMING
             OR DURATION? OR INTERVAL?
S6          1   S1 AND S4
S7      56519   S1 AND S5
S8         11   S3 AND S7
S9         36   S1 AND S3
S10        36   S6 OR S8 OR S9
S11        27   RD (unique items)
S12       445   S1 AND S2 AND S5
S13      8654   S1(3N)(DATA OR INFORMATION OR BYTE? OR BITS OR MEGABYTE? OR
             KILOBYTE? OR STORAGE? OR MEMORY?)
S14        49   S12 AND S13
S15        74   S14 OR S11
S16        65   RD (unique items)
S17        49   S16 NOT PY>1998
File   8:Ei Compendex(R) 1970-2005/Dec W4
         (c) 2005 Elsevier Eng.  Info. Inc.
File  35:Dissertation Abs Online 1861-2004/Dec
         (c) 2004 ProQuest Info&Learning
File  65:Inside Conferences 1993-2004/Dec W4
         (c) 2004 BLDSC all rts. reserv.
File   2:INSPEC 1969-2004/Dec W2
         (c) 2004 Institution of Electrical Engineers
File  94:JICST-EPlus 1985-2004/Nov W4
         (c)2004 Japan Science and Tech Corp(JST)
File 111:TGG Natl.Newspaper Index(SM) 1979-2004/Dec 29
         (c) 2004 The Gale Group
File   6:NTIS 1964-2004/Dec W4
         (c) 2004 NTIS, Intl Cpyrght All Rights Res
File 144:Pascal 1973-2004/Dec W1
         (c) 2004 INIST/CNRS
File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec
         (c) 1998 Inst for Sci Info
File  34:SciSearch(R) Cited Ref Sci 1990-2004/Dec W4
         (c) 2004 Inst for Sci Info
File  99:Wilson Appl. Sci & Tech Abs 1983-2004/Nov
         (c) 2004 The HW Wilson Co.
File  95:TEME-Technology & Management 1989-2004/Jun W1
         (c) 2004 FIZ TECHNIK
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17/5/13 (Item 13 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

00267759 E.I. Monthly No: EI7212010596
Title: SIMPLE CHARGE REGENERATOR FOR USE WITH CHARGE-TRANSFER DEVICES AND THE DESIGN OF FUNCTIONAL LOGIC ARRAYS.
Author: Tompsett, Michael F.
Corporate Source: Bell Telephone Lab, Inc, Murray Hill, NJ
Source: IEEE Journal of Solid-State Circuits v SC-7 n 3 Jun 1972 p 237-242
Publication Year: 1972
CODEN: IJSCBC ISSN: 0018-9200
Language: ENGLISH
Journal Announcement: 7212
Abstract: An inverting binary-charge regenerator for use with new charge-transfer devices (charge-coupled and integrated MOS **bucket** brigade) is described. This simple element requires an area approximately that of one bit in the register and is driven by the transfer pulses. Its uses with these shift **registers** in **various** configurations, which are described, make possible even larger functional devices. These uses include regeneration in serial memories, performing logic operations such as NAND and NOR involving the bit trains in **several** **registers** , and performing fixed counts and sequential addressing of other circuit elements.
Descriptors: *LOGIC CIRCUITS
Classification Codes:
721 (Computer Circuits & Logic Elements)
72 (COMPUTERS & DATA PROCESSING)

17/5/17 (Item 4 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online

01219256 ORDER NO: AAD92-14527

ANALYTICAL MODELS AND OPTIMAL STRATEGIES FOR AUTOMATED STORAGE/RETRIEVAL SYSTEM OPERATIONS (STORAGE-RETRIEVAL)

Author: PARK, BYUNG CHUN
Degree: PH.D.
Year: 1991
Corporate Source/Institution: GEORGIA INSTITUTE OF TECHNOLOGY (0078)
Director: EDWARD H. FRAZELLE
Source: VOLUME 52/12-B OF DISSERTATION ABSTRACTS INTERNATIONAL.
PAGE 6592. 184 PAGES
Descriptors: ENGINEERING, INDUSTRIAL
Descriptor Codes: 0546

The objective of this research is to provide exact reliable expressions for use in designing and operating automated storage/retrieval systems. We focus on the efficient operation of dual command cycles. The main performance criteria are travel **time** and system throughput.

We begin by developing a general analytical baseline for automated storage/retrieval system performance analysis. The baseline is a closed-form expression for the mean and variance of single and dual command cycle **time** . The model can be effectively used for evaluating any storage policy, i.e., random, dedicated or class-based storage. We give examples to illustrate how the formulas can be used for evaluating each storage policy.

With an evaluation baseline, our attention turns to specific improvement strategies. Contour line configurations for **storage location** assignment are developed first. We develop a general scheme to generate contour line configurations for dual command operations. To investigate the effects of alternative contour line configurations on system performance, a series of experiments are performed. The storage policies considered are random storage, priority-based open location (POL) storage, turnover-based storage, and 2-class storage. The performance of each contour line configuration is measured in terms of the expected dual command travel **time** .

Next, we develop optimal dwell point policies for automated storage/retrieval systems. Based on the fact that dwell point policies minimize the completion **time** of the first transaction after the storage/retrieval machine becomes idle, we show that there is a unique optimal dwell point policy, regardless of other system parameters. Then a variety of return paths to the dwell point are introduced and studied.

Finally, an end-of-aisle order picking system with inbound and outbound buffer positions is studied. This is usually referred to as a miniload system with a horse-shoe "front-end". The system is modeled as a two-stage cyclic queueing system consisting of one general and one exponential server with limited capacity. The cyclic queueing system is then analyzed by using the customer-hole duality concept. Closed-form expressions for the stationary probability and system throughput are developed. We also obtain the proportion of picker-idle and storage/retrieval machine-idle **time** by noting that the throughput of the picker is equal to that of the storage/retrieval machine. Then, a design problem to determine the optimal number of inbound and outbound buffer positions and a control problem to determine the optimal number of **storage containers** in the system are studied. The effect of buffer size on system throughput is also investigated.

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Set     Items   Description
S1     675611   CONTAINER? OR ENVELOPE? OR BUCKET? OR (DATA OR INFORMATION-
            )()(ENCLOSURE? OR RECEPTACLE? OR FOLDER?)
S2     397825   REGISTER? OR REGISTR? OR (STORAGE OR MEMORY)(N)(LOCATION? -
            OR AREA OR AREAS OR ADDRESS? OR SECTOR? OR REGION?)
S3      12040   S2(2N)(MULTIPLE OR MULTIPLICITY OR PLURAL OR PLURALITY OR -
            MULTIPLICITY OR SEVERAL OR DIFFERENT OR MANY OR VARIOUS OR VA-
            RIETY)
S4        140   S3(5N)(ALTERABLE OR DYNAMIC? OR CHANGE? OR MODIFY? OR REVI-
            S? OR EDIT? OR LIVE OR HOT)
S5    3326894   TIME? OR SCHEDUL? OR HOUR? OR CALENDAR? OR TIMING OR TIME(-
            )STAMP?
S6         34   S4 AND S5
S7          0   S1 AND S4
S8         70   S1 AND S3
S9          5   S8 AND IC=(G06F-017? OR G06F-007?)
S10        11   S8 AND IC=G06F?
S11        20   S4 AND IC=(G06F-017? OR G06F-007?)
S12        22   S6 AND IC=G06F?
S13     88865   MC=(T01-C04? OR T01-J05B?)
S14         5   S13 AND (S6 OR S8)
S15        40   S12 OR S11 OR S14
S16        40   IDPAT (sorted in duplicate/non-duplicate order)
S17        39   IDPAT (primary/non-duplicate records only)
File 347:JAPIO Nov 1976-2004/Aug(Updated 041203)
         (c) 2004 JPO & JAPIO
File 350:Derwent WPIX 1963-2004/UD,UM &UP=200482
         (c) 2004  Thomson Derwent
```

17/5/3 **(Item 3 from file: 350)**
DIALOG(R)File 350:Derwent WPIX

015947975 **Image available**
WPI Acc No: 2004-105816/200411
Method and system for vector scheduling **on object code level**
Patent Assignee: UNIV INHA (UYIN-N)
Inventor: KIM G C; KIM J S; KIM S D; LEE D H; LEE Y S
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
KR 2003078467	A	20031008	KR 200217526	A	20020329	200411	B

Priority Applications (No Type Date): KR 200217526 A 20020329
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
KR 2003078467	A		1	G06F-009/38	

Abstract (Basic): KR 2003078467 A
NOVELTY - A method and a system for vector **scheduling** on an object code level are provided to produce an excellent **scheduling** result in a **scheduling time** faster than a present software pipelining method.
DETAILED DESCRIPTION - An LCV(Loop Control Variable), and an initialization instruction, an initial value, a change instruction, a variance width, and an end instruction of the LCV are found out by using a CFG(Control Flow Graph) of a vector loop as input. The number of the concurrent executing instructions is calculated and the copies are generated by developing the vector loop with x. Registers are renamed in order to remove the data dependency of the registers used for each copy. The LCV of the copies is **changed** to the **register different** with each other. The LCV initialization instruction in the copies is changed based on the variance width. The LCV changing instruction in the copies is changed based on the x value. The instruction for copying the value of the LCV to the variable is inserted into a loop termination path of the copies. The vector **scheduled** CFG is generated by collecting the duplicated instruction for each instruction of the first copy.
pp; 1 DwgNo 1/10
Title Terms: METHOD; SYSTEM; VECTOR; **SCHEDULE** ; OBJECT; CODE; LEVEL
Derwent Class: T01
International Patent Class (Main): **G06F-009/38**
File Segment: EPI

17/5/5 **(Item 5 from file: 350)**
DIALOG(R)File 350:Derwent WPIX

015768651 **Image available**
WPI Acc No: 2003-830853/200377
XRPX Acc No: N03-663897

Dynamic random access memory controller in computer system, has configuration registers to store control information of memory banks, and column address strobe state machine to generate strobe signals for memory banks

Patent Assignee: INTEL CORP (ITLC)
Inventor: LANGENDORF B K; DODD J M; WADE N D
Number of Countries: 001 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030177303	A1	20030918	US 97814697	A	19970311	200377 B
			US 2003389092	A	20030313	
US 6725349	B2	20040420	US 94381091	A	19941223	200427
			US 97814697	A	19970311	
			US 2003389092	A	20030313	

Priority Applications (No Type Date): US 97814697 A 19970311; US 2003389092 A 20030313; US 94381091 A 19941223
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20030177303	A1		10	G06F-012/00	Cont of application US 97814697
US 6725349	B2			G06F-012/00	Cont of application US 94381091 Cont of application US 97814697

Abstract (Basic): US 20030177303 A1

NOVELTY - **Several** configuration **registers** (300) store control information for **dynamic** RAM (DRAM) memory banks of a main memory (103). A column address strobe (CAS) state machine (330) coupled to the registers, generates CAS signals (220) for the memory banks. A detection logic circuit coupled to the CAS state machine, determines type of DRAM device installed in each memory bank to store control information of the device in the registers.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for method for optimizing control of each memory bank.

USE - For automatically configuring and controlling memory banks installed with dynamic random access memory (DRAM) devices including standard page mode DRAM and extended data-out DRAM (EDO-DRAM) in computer system.

ADVANTAGE - The CAS state machine automatically controls **timing** requirements of the DRAM devices installed in the main memory to quickly and efficiently handle access requests. Thus, the performance of EDO-DRAM and standard page mode DRAM is preserved and controlled without increasing the hardware cost, while ensuring correct operation of the DRAMs.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of the DRAM controller.

main memory (103)
CAS signals (220)
configuration register (300)
address bank decoder (310)
CAS state machine (320)
pp; 10 DwgNo 3/7

Title Terms: DYNAMIC; RANDOM; ACCESS; MEMORY; CONTROL; COMPUTER; SYSTEM; CONFIGURATION; REGISTER; STORAGE; CONTROL; INFORMATION; MEMORY; BANK; COLUMN; ADDRESS; STROBE; STATE; MACHINE; GENERATE; STROBE; SIGNAL; MEMORY ; BANK
Derwent Class: T01; U14
International Patent Class (Main): **G06F-012/00**
File Segment: EPI

17/5/11 **(Item 11 from file: 350)**
DIALOG(R)File 350:Derwent WPIX

013483279 **Image available**
WPI Acc No: 2000-655222/200063
Related WPI Acc No: 1999-457716
XRPX Acc No: N00-485639

Data processing system used in graphical user interface, displays one of subset of stored object automatically only if at least one of stored object of subset has not been manually associated with its container

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)
Inventor: LISLE L A; MARTIN S L; MULLALY J M
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
US 6104394	A	20000815	US 97813717	A	19970307	200063	B
			US 99239405	A	19990128		

Priority Applications (No Type Date): US 97813717 A 19970307; US 99239405 A 19990128
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 6104394	A		19	G06F-017/30	Cont of application US 97813717 Cont of patent US 5936624

Abstract (Basic): US 6104394 A

NOVELTY - Two subsets constituting stored object of respective object types within respective **containers** , are displayed automatically in a display unit, only if at least one of stored objects of the two subsets has not been manually associated with their respective **containers** .

DETAILED DESCRIPTION - The display unit automatically updates the displayed subsets when the stored objects is charged, when either storing, editing, deleting, moving, archiving, copying, linking on undoing of stored object occurs. The object type is chosen from text type, audio type, graphic type, type corresponding to date and time. INDEPENDENT CLAIMS are also included for the following:

(a) operating data processing system;

(b) program product

USE - Used in graphical user interface, real world style interface with logical containment system.

ADVANTAGE - Enables user to organize representation of desired objects in **various storage locations** without requiring extra steps by a user access memory. Allows greater flexibility in obtaining desired graphical user interface, by the ability of user to modify the containment settings and to have modifications immediately rejected in logical **container** rendered on display device.

DESCRIPTION OF DRAWING(S) - The figure shows model diagram of data processing system explained with Booch notation.

pp; 19 DwgNo 8/8

Title Terms: DATA; PROCESS; SYSTEM; GRAPHICAL; USER; INTERFACE; DISPLAY; ONE; SUBSET; STORAGE; OBJECT; AUTOMATIC; ONE; STORAGE; OBJECT; SUBSET; MANUAL; ASSOCIATE; **CONTAINER**
Derwent Class: T01
International Patent Class (Main): G06F-017/30
File Segment: EPI

17/5/22 **(Item 22 from file: 350)**
DIALOG(R)File 350:Derwent WPIX

007659045 **Image available**
WPI Acc No: 1988-292977/198841
XRPX Acc No: N88-222362

Multiprocessor system with shared memory - has machine instruction sequence in shared memory for assigning register sets based on status information

Patent Assignee: STELLAR COMPUTER IN (STEL-N)
Inventor: DARNELL P A; MORTON M A

Number of Countries: 029 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
WO 8807720	A	19881006	WO 88US1032	A	19880325	198841	B
AU 8816821	A	19881102				198904	

Priority Applications (No Type Date): US 8734166 A 19870402
Cited Patents: 1.Jnl.Ref; EP 174446; US 3916383; US 3972029; US 3980922; US 4121286; US 4197579; US 4280176; US 4354227; US 4713757
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
WO 8807720	A	E	27		

Designated States (National): AU BB BG BR DK FI HU JP KP KR LK MC MG MW NO RO SD SU
Designated States (Regional): AT BE CH DE FR GB IT LU NL OA SE

Abstract (Basic): WO 8807720 A

The multiprocessor system has four processors which share machine instruction sequence (14) stored in parallel regions in the memory (16), which includes parallel regions of instructions. Each region has two blocks of instructions which are independent in that the same result is obtained if the blocks are executed by the same processor or by different processors.

The system provides a pool (22) of high speed register sets to regulate the work of the four processors. Each processor has a respective unshared register (40) which stores a program status word. The word provides information about the part of the process executing on a given processor at a given **time** .

ADVANTAGE - The assignment of shared **register** sets to **multiple** processors is effected **dynamically** without interrupting the execution of instructions in the sequence.

1/8

Title Terms: MULTIPROCESSOR; SYSTEM; SHARE; MEMORY; MACHINE; INSTRUCTION; SEQUENCE; SHARE; MEMORY; ASSIGN; REGISTER; SET; BASED; STATUS; INFORMATION
Derwent Class: T01
International Patent Class (Additional): **G06F-012/00**
File Segment: EPI

07054897 **Image available**

COMMUNICATION INSTRUCTION RESULT OF PROCESSOR AND COMPILING METHOD FOR PROCESSOR

PUB. NO.: 2001-282532 [JP 2001282532 A]
PUBLISHED: October 12, 2001 (20011012)
INVENTOR(s): TOPHAM NIGEL PETER
APPLICANT(s): SIROYAN LTD
APPL. NO.: 2001-032090 [JP 200132090]
FILED: February 08, 2001 (20010208)
PRIORITY: 00 200002848 [GB 20002848], GB (United Kingdom), February 08, 2000 (20000208)
INTL CLASS: **G06F-009/38** ; **G06F-009/30** ; **G06F-009/34** ; **G06F-009/45**

ABSTRACT

PROBLEM TO BE SOLVED: To simplify a task of a compiler for the allocation of a register and to set up an instruction in a more compact state.

SOLUTION: A processor 1 for executing a pipeline by software includes an instruction issuing device 10 for issuing plural instructions to be executed by a previously determined sequence. The sequence of instructions includes plural value generation instructions for generating respective values at the **time** of execution of the sequence. Each of instruction execution devices 14, 16, 18 executes an issued instruction. A register file 20 has plural registers and stores plural values generated by respective executed instructions. During the period of operation, the processor 1 allocates plural values generated by respective value generation instructions to respective sequence numbers in accordance with the issued order of respective value generation instructions. Each generated value is allocated to one of plural registers in order to store the generated value on the basis of the sequence number allocated to the value. The names of these **plural registers** can be **changed** in each issue of a value generation instruction.

03475828 **Image available**

DIGITAL PROCESSOR

PUB. NO.: 03-138728 [JP 3138728 A]
PUBLISHED: June 13, 1991 (19910613)
INVENTOR(s): SATOMURA RYUICHI
TOMOBE KATSUICHI
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 01-275821 [JP 89275821]
FILED: October 25, 1989 (19891025)
INTL CLASS: [5] **G06F-009/38**
JAPIO CLASS: 45.1 (INFORMATION PROCESSING -- Arithmetic Sequence Units)
JAPIO KEYWORD: R131 (INFORMATION PROCESSING -- Microcomputers & Microprocessers)
JOURNAL: Section: P, Section No. 1250, Vol. 15, No. 360, Pg. 105, September 11, 1991 (19910911)

ABSTRACT

PURPOSE: To shorten the instruction execution **time** of a microprocessor, etc., and to raise processing capacity by **dynamically** assigning **plural** work **registers** in accordance with its usage condition.

CONSTITUTION: n numbers of work registers WR1-WRn used when execution a micro-instruction, and a work register control part WRC which dynamically assigns these work registers WR1-WRn in accordance with their usage condition, are provided. Then, the work registers WR1-WRn can be dynamically assigned in accordance with their usage condition without specifying them with a micro-instruction, the score boarding of the work registers WR1-WRn can be executed simultaneously with the score boarding of a general purpose register GR by a machine language instruction, and the parallel processing of he following machine language instructions can be started early. Thus, the instruction execution **time** of the microprocessor, etc., can be shortened equally, and its processing capacity can be raised.

17/5/36 **(Item 36 from file: 347)**
DIALOG(R)File 347:JAPIO

01917037 **Image available**
CONTINUOUS GENERATING SYSTEM OF PLURAL ADDRESSES

PUB. NO.: 61-131137 [JP 61131137 A]
PUBLISHED: June 18, 1986 (19860618)
INVENTOR(s): AKIBA HIROSHI
AOYANAGI KEIZO
APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 59-253342 [JP 84253342]
FILED: November 30, 1984 (19841130)
INTL CLASS: [4] **G06F-012/02**
JAPIO CLASS: 45.2 (INFORMATION PROCESSING -- Memory Units)
JOURNAL: Section: P, Section No. 512, Vol. 10, No. 324, Pg. 91, November 05, 1986 (19861105)

ABSTRACT

PURPOSE: To attain count-up or count-down sequentially for **plural** address **registers** by using an adder to **modify** logically an address of an address register before one address and applying it sequentially to each register.

CONSTITUTION: An address modification data and a logical address from an adder 2 inputting an addend for modification and generating a logical address are inputted sequentially to plural address registers 3,4,5. Then address information of the address registers 3,4,5 is selected in the predetermined order by a selector 10 and outputs it as a memory access (f). Further, the address register selected precedingly by the selector 10 is selected by the other selector 9 at the same **time** and the address information (e) is fed back to a selector 6 as the address modification data. Then a required addition is executed by the adder 2 to modify the address of the register subject to feed back. The operation is executed sequentially to the registers 3,4,5 to generate continuously plural addresses thereby clearing them.

17/5/37 **(Item 37 from file: 347)**
DIALOG(R)File 347:JAPIO

01655844 **Image available**

GUIDANCE INFORMATION CONTROLLING SYSTEM

PUB. NO.: 60-134344 [JP 60134344 A]
PUBLISHED: July 17, 1985 (19850717)
INVENTOR(s): YOSHINO ISAO
SOMA MASATO
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 58-241921 [JP 83241921]
FILED: December 23, 1983 (19831223)
INTL CLASS: [4] **G06F-009/00 ; G06F-015/00**
JAPIO CLASS: 45.1 (INFORMATION PROCESSING -- Arithmetic Sequence Units); 45.4 (INFORMATION PROCESSING -- Computer Applications)
JOURNAL: Section: P, Section No. 408, Vol. 09, No. 299, Pg. 50, November 27, 1985 (19851127)

ABSTRACT

PURPOSE: To execute a change which requires no person's help by executing the change by sending guidance information to a terminal control device from a central processor by a guidance information change request from a change request mechanism of the central processor or a terminal equipment.

CONSTITUTION: A central processor 11, terminal control device 16 and a terminal equipment 20 are connected, a local guidance control mechanism 12 and a **timer** 13 are provided on the processor 11, and a storage mechanism 15 and a managing mechanism 14 are provided on the mechanism 12. Also, a local guidance change control mechanism 19 consisting of a store part 17 and a change identifying mechanism 18 is provided on the device 16, and the store part 17 is constituted of **plural storage area** 21 and on-demand **changeable** storage areas 22. In this state, the area 21 is changed by receiving a change request from the **timer** 13 by the mechanism 14 and sending retrieved information to the mechanism 19, a change request from the equipment 20 is registered in the mechanism 18 and also transferred to the processor 11, and the area 22 is changed by sending the retrieved information to the mechanism 19.

17/5/38 **(Item 38 from file: 347)**
DIALOG(R)File 347:JAPIO

01125342 **Image available**
OPERATION PROCESSING DEVICE

PUB. NO.: 58-062742 [JP 58062742 A]
PUBLISHED: April 14, 1983 (19830414)
INVENTOR(s): TAMURA NOBORU
APPLICANT(s): CANON INC [000100] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 56-162367 [JP 81162367]
FILED: October 12, 1981 (19811012)
INTL CLASS: [3] **G06F-007/00**
JAPIO CLASS: 45.1 (INFORMATION PROCESSING -- Arithmetic Sequence Units)
JOURNAL: Section: P, Section No. 208, Vol. 07, No. 152, Pg. 39, July 05, 1983 (19830705)

ABSTRACT

PURPOSE: To make the operation easy, by providing a means having a **plurality** of condition **registers** and storing the **change** in the condition codes in the past, and a means performing the operation instruction between condition registers.

CONSTITUTION: In executing an arithmetic logical operation instruction having the alteration of a CCR through the instruction of an instruction decoder 6, the conditions with the result of operation are set to the CCR. An arithmetic logical operation device 3 reads out the 1st CCR4-1 in one machine cycle and gives an output to the 2nd CCR4-2 in the next machine cycle. The conditions through the result of present operation are outputted to the 1st CCR4-1 in the next machine cycle. Through this operation, the previous condition is set to the 2nd CCTR4-2 and the present condition is set to the 1st CCR4-1. Further, with the operation instruction between the CCRs, the two CCRs 4-1, 4-2 are read and an output is given to the instructed CCR

```
Set     Items   Description
S1          9   AU=(DEANGELO, M? OR DEANGELO M? OR DE ANGELO M? OR DE ANGE-
             LO, M?)
S2          3   S1 AND IC=G06F?
File 347:JAPIO Nov 1976-2004/Aug(Updated 041203)
         (c) 2004 JPO & JAPIO
File 348:EUROPEAN PATENTS 1978-2004/Dec W03
         (c) 2004 European Patent Office
File 349:PCT FULLTEXT 1979-2002/UB=20041230,UT=20041223
         (c) 2004 WIPO/Univentio
File 350:Derwent WPIX 1963-2004/UD,UM &UP=200482
         (c) 2004  Thomson Derwent
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2/5/1 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

01077982
SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS
SYSTEM UND VERFAHREN ZUR ERZEUGUNG UND BEARBEITUNG VON INFORMATIONSBEHALTERN MITDYNAMISCHEN REGISTERN.
SYSTEME ET PROCEDE POUR LA CREATION ET LA MANIPULATION DE CONTENEURS D'INFORMATIONS A REGISTRES DYNAMIQUES
PATENT ASSIGNEE:
Ematrix Corporation, (2819080), 104 West Anapamu, Santa Barbara, CA 93101, (US), (Applicant designated States: all)
INVENTOR:
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LEGAL REPRESENTATIVE:
McLeish, Nicholas Alistair Maxwell et al (74621), Boult Wade Tennant Verulam Gardens 70 Gray's Inn Road, London WC1X 8BT, (GB)
PATENT (CC, No, Kind, Date): EP 1049996 A1 001108 (Basic)
WO 9939285 990805
APPLICATION (CC, No, Date): EP 99905548 990128; WO 99US1988 990128
PRIORITY (CC, No, Date): US 73209 980130
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE
INTERNATIONAL PATENT CLASS: **G06F-017/30** ; **G06F-003/14**
NOTE:
No A-document published by EPO
LEGAL STATUS (Type, Pub Date, Kind, Text):

Application:	001108 A1	Published application with search report
Application:	991006 A1	International application. (Art. 158(1))
Withdrawal:	040204 A1	Date application deemed withdrawn: 20030801
Examination:	001108 A1	Date of request for examination: 20000713
Search Report:	010425 A1	Date of drawing up and dispatch of supplementary:search report 20010308
Application:	991006 A1	International application entering European phase

LANGUAGE (Publication,Procedural,Application): English; English; English

2/5/2 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

00507933 **Image available**
SYSTEM AND METHOD FOR CREATING AND MANIPULATING INFORMATION CONTAINERS WITH DYNAMIC REGISTERS
SYSTEME ET PROCEDE POUR LA CREATION ET LA MANIPULATION DE CONTENEURS D'INFORMATIONS A REGISTRES DYNAMIQUES
Patent Applicant/Assignee:
EMATRIX CORPORATION,
DE ANGELO Michael,
Inventor(s):
DE ANGELO Michael
Patent and Priority Information (Country, Number, Date):
Patent: WO 9939285 A1 19990805
Application: WO 99US1988 19990128 (PCT/WO US9901988)
Priority Application: US 9873209 19980130
Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU ZW GH GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

Main International Patent Class: **G06F-017/30**
International Patent Class: **G06F-003/14**
Publication Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 18390

English Abstract
A system for creating and manipulating information containers with dynamic registers on a multi-user computer system, or computer network comprises an interactive information container, a container editor, a search interface, a user profile, system-wide hierarchical container gateways (site 7), interactive and evolving container registers, a data collection means, a data reporting means, an analysis engine with editor, an executing engine with editor, and a means of communicating with other computers, computer networks, or digital-based public or published media. The container editor provides an authoring user with the capacity to encapsulate any information component such as a file, set, database, network, event or process, and a set of parameters of multiple container registers to govern the interaction of that container with other containers or processes. The container registers include system-defined, system-alterable, user-defined and user-alterable registers.

French Abstract
L'invention concerne un systeme pour la creation et la manipulation de conteneurs d'informations a registres dynamiques, sur un systeme informatique multi-utilisateur, ou sur un reseau informatique. Ce systeme comprend un conteneur d'informations interactif, un editeur de conteneur, une interface de recherche, un profil d'utilisateur, des passerelles (site 7) de conteneurs hierarchiques a l'echelle du systeme, des registres interactifs et evolutifs, un dispositif de rassemblement de donnees, un dispositif d'edition de donnees, un moteur d'analyse avec editeur, un moteur d'execution avec editeur, et un dispositif permettant de communiquer avec d'autres ordinateurs, avec des reseaux informatiques, ou avec des supports numeriques publics ou publies. L'editeur de conteneur permet a un utilisateur-auteur d'encapsuler n'importe quel composant d'information tel qu'un dossier, un ensemble, une base de donnees, un reseau, un evenement ou un procede, et fournit a cet utilisateur une serie de parametres pour plusieurs registres de conteneurs pour commander l'interaction de ce conteneur avec d'autres conteneurs ou procedes. Les registres de conteneurs comprennent des registres definis par le systeme, modifiables par le systeme, definis par l'utilisateur et modifiables par l'utilisateur.

2/5/3 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX

012673114 **Image available**
WPI Acc No: 1999-479221/199940
XRPX Acc No: N99-356757