

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY, LTD.
and TSMC NORTH AMERICA CORPORATION,
Petitioners,

v.

ZOND, LLC,
Patent Owner.

Case IPR2014-00803
Patent 7,808,184 B2

Before KEVIN F. TURNER, DEBRA K. STEPHENS, JONI Y. CHANG,
SUSAN L. C. MITCHELL, and JENNIFER M. MEYER,
Administrative Patent Judges.

MITCHELL, *Administrative Patent Judge.*

DECISION
Institution of *Inter Partes* Review
37 C.F.R. § 42.108

I. INTRODUCTION

Taiwan Semiconductor Manufacturing Company, Ltd. and TSMC North America Corporation (collectively, “TSMC”) filed a Petition requesting *inter partes* review of claims 6–10 and 16–20 of U.S. Patent No. 7,808,184 B2 (“the ’184 patent”). Paper 2 (“Pet.”). Zond, LLC (“Zond”) filed a Preliminary Response. Paper 7 (“Prelim. Resp.”). We have jurisdiction under 35 U.S.C. § 314.

The standard for instituting an *inter partes* review is set forth in 35 U.S.C. § 314(a), which provides:

THRESHOLD.—The Director may not authorize an *inter partes* review to be instituted unless the Director determines that the information presented in the petition filed under section 311 and any response filed under section 313 shows that there is a reasonable likelihood that the petitioner would prevail with respect to at least 1 of the claims challenged in the petition.

Upon consideration of TSMC’s Petition and Zond’s Preliminary Response, we conclude that the information presented in the Petition demonstrates that there is a reasonable likelihood that TSMC would prevail in challenging claims 6–10 and 16–20 (“the challenged claims”) as unpatentable under 35 U.S.C. § 103(a). Pursuant to 35 U.S.C. § 314, we hereby authorize an *inter partes* review to be instituted as to claims 6–10 and 16–20 of the ’184 patent based on the specific grounds discussed below.

A. *Related District Court Proceedings*

TSMC indicates that the ’184 patent was asserted in *Zond, LLC v. Intel Corp.*, No.1:13-cv-11570-RGS (D. Mass.). Pet. 1. TSMC also identifies other cases where Zond asserted the claims of the ’184 patent. *Id.*

B. Related Inter Partes Reviews

Intel Corporation (“Intel”) filed a Revised Petition to institute an *inter partes* review in IPR2014-00456, challenging the same claims based on the same grounds of unpatentability as those in the instant proceeding.

Compare IPR2014-00456, Paper 4 (“’456 Pet.”), 3–58, with Pet. 4–59; Pet. 1 (stating challenged claims of the ’184 patent “are presently the subject of a substantially identical petition for *inter partes* review” in *Intel Corp. v. Zond*, IPR2014-00456 (PTAB)). On September 3, 2014, we instituted an *inter partes* review of claims 6–10 and 16–20 of the ’184 patent in IPR2014-00456 (Paper 12, “’456 Dec.”), based on the grounds that claims 6, 7, 9, 10, 16, 17, 19, and 20 are unpatentable as obvious over the combination of Wang and Kudryavtsev, and that claims 8 and 18 are unpatentable as obvious over the combination of Wang, Kudryavtsev, and Mozgrin. *Id.* at 30–31. The trial, however, was terminated in light of the Written Settlement Agreement, made in connection with the termination of the proceeding in accordance with 35 U.S.C. § 317(b) and 37 C.F.R. § 42.74(b), between Intel and Zond. IPR2014-00456, Papers 14, 15; IPR2014-00455, Ex. 1025.

TSMC has filed a Motion for Joinder, seeking to join the instant proceeding with *Intel Corp. v. Zond, LLC.*, Case IPR2014-00456 (PTAB). Paper 6 (“Mot.”). In view of the termination of the Intel Proceeding, however, TSMC’s Motion for Joinder is dismissed as moot in a separate decision.

The following Petitions for *inter partes* review also challenge the same claims based on the same grounds of unpatentability as those in IPR2014-00456 and in the instant proceeding: *Fujitsu Semiconductor Ltd. v. Zond, LLC*, Case IPR2014-00858, Paper 1; *The Gillette Co. v Zond, LLC*,

Case IPR2014-00996, Paper 3; and *Advanced Micro Devices, Inc. v. Zond, LLC*, Case IPR2014-01061, Paper 2.

C. The '184 patent

The '184 patent relates to methods for generating strongly-ionized plasmas in a plasma generator. Ex. 1101, Abs. When creating a plasma in a chamber, a direct current (“DC”) electrical discharge, which is generated between two electrodes with a feed gas, generates electrons in the feed gas that ionize atoms to create the plasma. *Id.* at 1:16–20. For an application, such as magnetron plasma sputtering, a relatively high level of energy must be supplied, which may result in overheating the electrodes or the work piece. *Id.* at 1:21–26. Such overheating may be addressed by complex cooling mechanisms, but such cooling can cause temperature gradients in the chamber causing a non-uniform plasma process. *Id.* at 1:26–30. These temperature gradients may be reduced by pulsing the DC power, but high-power pulses may result in arcing at plasma ignition and termination. *Id.* at 1:31–36. Arcing is problematic because it can cause the release of undesirable particles in the chamber thereby contaminating the work piece. *Id.* at 1:36–37, 4:8–11.

According to the '184 patent, a pulsed power supply may include circuitry that minimizes or eliminates the probability of arcing in the chamber by limiting the plasma discharge current to a certain level and dropping the generated voltage for a certain period of time if the limit is exceeded. *Id.* at 4:6–15. Figure 2, reproduced below, shows measured data of discharge voltage as a function of discharge current for admitted prior-art,

low-current plasma 152, and high-current plasma 154 created by the claimed methods using the pulsed power supply. *Id.* at 1:58–60.

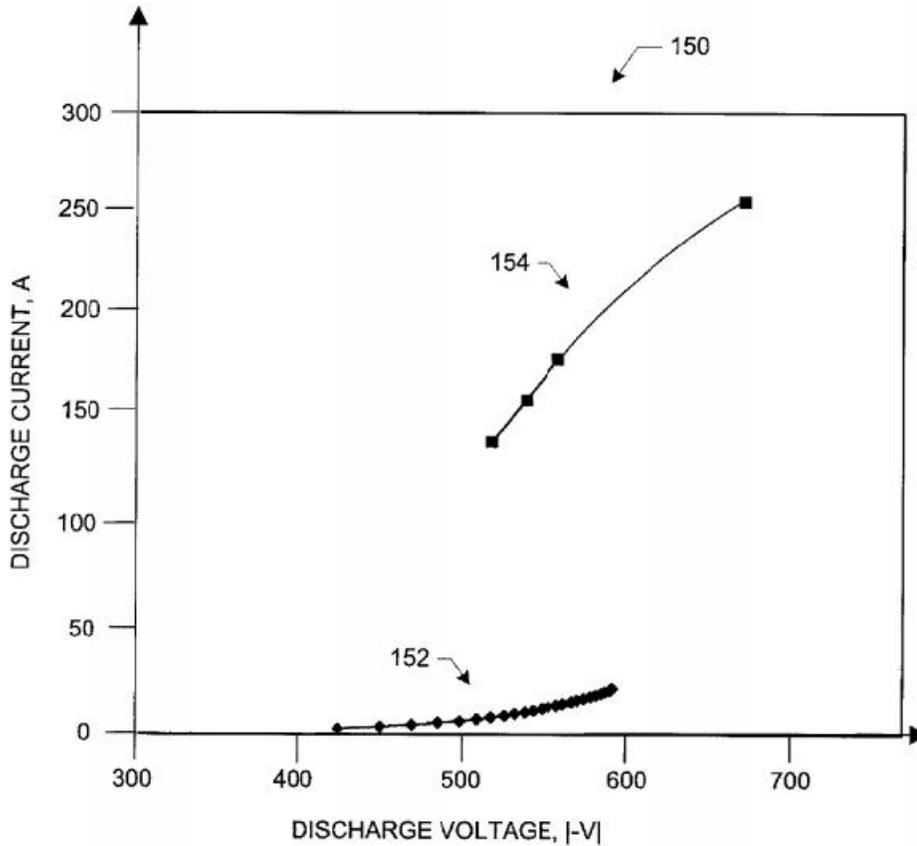


FIG. 2

Figure 2 shows current-voltage characteristic 154 that represents actual data for plasma generated by the pulsed power supply in the plasma sputtering system depicted in Figure 1 (not reproduced here). *Id.* at 5:28–30. The current-voltage characteristic 154 is in a high-current regime that generates a relatively high plasma density (greater than 10^{12} – 10^{13} cm^{-3}). *Id.* at 5:40–43. The pulsed power supply generates waveforms that create and sustain the high-density plasma with current-voltage characteristics in the high-current regime. *Id.* at 5:55–59. The '184 patent explicitly defines the

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