PNY Technologies, Inc.

EXHIBIT 1002

PNY Exhibit 1002 Inter Partes Review of

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE BEFORE THE PATENT TRIALS & APPEALS BOARD

In re <i>Inter Partes</i> Review of: U.S. Patent No. 7,518,879	
Patent Issued: April 14, 2009	
Named Inventors: Chung et al.	
Patentee: Phison Electronics Corp.	
App. No.: 11/384,371	: Petition Filed: To be Filed
App. Filed: March 21, 2006	:
Title: UNIVERSAL SERIAL BUS (USB) MEMORY PLUG	: Filed Electronically : Per 37 CFR § 42.6(b)(1) :

Mail Stop PATENT BOARD

Patent Trial and Appeal Board United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

POWER OF ATTORNEY UNDER 37 C.F.R. § 42.10

I hereby appoint Mark E. Nikolsky (Reg. No. 48,319) and Sanjiv M. Chokshi (Reg.

No. 44,080), both of McCarter & English, LLP, as the lead and back-up attorneys,

respectively, to represent Petitioner PNY Technologies, Inc. in the Inter Partes

Review for the patent identified above, and to transact all business connected

therewith Signature

-01,10-

11/13/13 Date

Printed Name and Position - PNY Technologies, Inc.

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