

EXHIBIT 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RELOADED GAMES, INC.
(Petitioner)

v.

PARALLEL NETWORKS LLC
(Patent Owner)

Case No. IPR2014-00136

Patent No. 7,188,145

Inventors:

Keith A. Lowery

Bryan S. Chin

David A. Consolver

Gregg A. DeMasters

Filed: January 12, 2001

For: Method and System for
Dynamic Distributed Data Caching

Mail Stop: PATENT BOARD
Patent Trial and Appeal Board
US Patent and Trademark Office
PO Box 1450
Alexandria, VA 22313-1450

DECLARATION OF DR. MITCHELL A. THORNTON

I, Mitchell A. Thornton, declare the following:

1. I submit this Declaration in connection with my role as a consulting expert in the matter of the Inter Partes review, *Reloaded Games, Inc. v. Parallel Networks, LLC*, IPR2014-00136 (the “IPR”), on behalf of Parallel Networks, LLC.

I have personal knowledge of the facts set forth below.

INTRODUCTION AND BACKGROUND

2. I have been retained as an expert by McGuire-Woods LLP (“MGW”) in the above-captioned matter to provide expert analysis, opinions, and testimony regarding U.S. Patent No. 7,118,145 (“the ’145 patent”).

3. I am being compensated for my work and travel time associated with this case at my ordinary rate of \$350.00 per hour, plus reimbursement of reasonable direct expenses. I have no other interest in this action or the parties thereto and my compensation does not depend on the outcome of this matter.

4. I was asked by MGW to review and analyze the ‘145 patent, the ‘145 prosecution history, IPR pleadings including the petition and response, and various prior art identified in the following sections of this declaration.

5. In forming my opinions set forth in this Declaration, I relied on the ‘145 patent and the file history of the ‘145 patent, U.S. Patent No. 6,341,311 issued to Smith (“Smith”), and U.S. Patent No. 6,256,747 issued to Shigekazu Inohara, et al. (“Inohara”).

6. In this Declaration, I have set forth my opinions concerning the teachings and disclosure of Inohara.

7. In forming my opinions, I also relied upon my knowledge as person of ordinary skill in the art obtained through more than 29 years of work in computer science and engineering.

QUALIFICATIONS

8. My curriculum vitae is attached as Appendix 1 to this Declaration.

9. I hold a Ph.D. degree from Southern Methodist University in Computer Engineering; a Master's degree from Southern Methodist University in Computer Science; a Master's degree in Electrical Engineering from the University of Texas-Arlington; and a Bachelor's degree in Electrical Engineering from Oklahoma State University.

10. I am a Full Professor of Computer Science and Engineering and also of Electrical Engineering at Southern Methodist University. I additionally hold the position of Technical Director of the Darwin Deason Institute for Cyber Security and Principal of the Hardware and Network Security Engineering Program within the Institute. I have been engaged in a variety of computer architecture and digital systems research projects funded by both governmental and industrial sponsors. Furthermore, I have served as an independent professional engineering consultant offering design and analysis services related to aspects of computer systems

architecture, digital systems design, wireless/wired networked systems and interfaces, and integrated circuit design. My independent consulting work began in 1993.

11. Before entering academia, I was employed fulltime by E-Systems, Inc. from 1986 through 1991 in Greenville, Texas (now L3-Communications, Inc.). I resigned as a full-time employee with the title Senior Electronic Systems Engineer in 1991 to pursue full-time graduate study. At E-Systems, I was involved in the design, implementation, test, and evaluation of a number of both ground-based and airborne computer systems and customized peripherals.

12. During my PhD studies, I was employed both part-time and full-time at various points in time with the Cyrix Corporation as a Design Engineer. In that capacity I was a member of the M1 microprocessor design group where my duties included the design and test of various portions of a new microprocessor including the bus controller, branch prediction circuitry, cache and memory interfaces, and others. The M1 microprocessor is compatible with the Intel Pentium microprocessor but is a completely new and independent design and architecture as compared to the Intel device.

13. I am a licensed professional engineer in Texas, Mississippi, and Arkansas. I am also the proprietor of a registered engineering firm in Texas and have worked as a professional engineer either independently or as an employee of

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