

being prepared as the newest video coding standard of the ITU-T Video Coding Experts Group and the ISO/IEC Moving Picture Experts Group. The main goal of the HEVC standardization effort is to enable significantly improved compression performance relative to existing standards—in the range of 50% bit-rate reduction for equal perceptual video quality. This paper provides an overview of the technical features and characteristics of the HEVC standard.

Index Terms—Advanced video coding (AVC), H.264, High Efficiency Video Coding (HEVC), Joint Collaborative Team on Video Coding (JCT-VC), Moving Picture Experts Group (MPEG), MPEG-4, standards, Video Coding Experts Group (VCEG), video compression.

I. INTRODUCTION

THE High Efficiency Video Coding (HEVC) standard is the most recent joint video project of the ITU-T Video Coding Experts Group (VCEG) and the ISO/IEC Moving Picture Experts Group (MPEG) standardization organizations, working together in a partnership known as the Joint Collaborative Team on Video Coding (JCT-VC) [1]. The first edition of the HEVC standard is expected to be finalized in January 2013, resulting in an aligned text that will be published by both ITU-T and ISO/IEC. Additional work is planned to extend the standard to support several additional application scenarios, including extended-range uses with enhanced precision and color format support, scalable video coding, and 3-D/stereo/multiview video coding. In ISO/IEC, the HEVC standard will become MPEG-H Part 2 (ISO/IEC 23008-2) and in ITU-T it is likely to become ITU-T Recommendation H.265.

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development of the well-known ITU-T and ISO/IEC standards. The ITU-T produced H.261 [2] and H.263 [3], and ISO/IEC produced MPEG-1 [4] and MPEG-4 Visual [5]. Other standardization organizations jointly produced the H.262/MPEG-2 Visual [6] and H.264/MPEG-4 Advanced Video Coding (AVC) [7] standards. The two standards that were jointly produced have had a particularly strong impact and have found their way into a wide variety of products that are increasingly prevalent in our daily lives. Throughout this evolution, continued effort has been made to maximize compression capability and efficiency while maintaining characteristics such as data loss robustness, while minimizing the computational resources that were practical for implementation in products at the time of anticipated deployment of each standard.

The major video coding standard directly derived from the HEVC project was H.264/MPEG-4 AVC, which was developed in the period between 1999 and 2003. H.264/MPEG-4 AVC was extended in several important ways from H.264/MPEG-4 AVC. H.264/MPEG-4 AVC has been an enabling technology for high definition video in almost every area that was not previously covered by H.262/MPEG-2 Video and has substantially replaced the older standard within its existing application areas. H.264/MPEG-4 AVC is widely used for many applications, including broadcast television, high definition (HD) TV signals over satellite, cable, and terrestrial transmission systems, video content acquisition and distribution systems, camcorders, security applications, Internet-based mobile network video, Blu-ray Discs, and real-time applications such as video chat, video conferencing, and telepresence systems.

However, an increasing diversity of services, the growing popularity of HD video, and the emergence of new HD formats (e.g., 4k×2k or 8k×4k resolution) have created even stronger needs for coding efficiency superior to what H.264/MPEG-4 AVC's capabilities. The need is even stronger for higher resolution is accompanied by stereo video capture and display. Moreover, the traffic caused by these applications targeting mobile devices and tablets is increasing as the transmission needs for video-on-demand services are imposing severe challenges on today's networks. The growing desire for higher quality and resolutions is a major driver for mobile applications.

HEVC has been designed to address essential requirements for applications of H.264/MPEG-4 AVC and to parallelize on two key issues: increased video resolution and efficient use of parallel processing architectures. The system architecture is designed to support a wide range of applications.

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quality, implementation cost, time to market, and other considerations). However, it provides no guarantees of end-to-end reproduction quality, as it allows even crude encoding techniques to be considered conforming.

To assist the industry community in learning how to use the standard, the standardization effort not only includes the development of a text specification document, but also reference software source code as an example of how HEVC video can be encoded and decoded. The draft reference software has been used as a research tool for the internal work of the committee during the design of the standard, and can also be used as a general research tool and as the basis of products. A standard test data suite is also being developed for testing conformance to the standard.

This paper is organized as follows. Section II highlights some key features of the HEVC coding design. Section III explains the high-level syntax and the overall structure of HEVC coded data. The HEVC coding technology is then described in greater detail in Section IV. Section V explains the profile, tier, and level design of HEVC. Since writing an overview of a technology as substantial as HEVC involves a significant amount of summarization, the reader is referred to [1] for any omitted details. The history of the HEVC standardization effort is discussed in Section VI.

II. HEVC CODING DESIGN AND FEATURE HIGHLIGHTS

The HEVC standard is designed to achieve multiple goals, including coding efficiency, ease of transport system integration and data loss resilience, as well as implementability using parallel processing architectures. The following subsections briefly describe the key elements of the design by which these goals are achieved, and the typical encoder operation that would generate a valid bitstream. More details about the associated syntax and the decoding process of the different elements are provided in Sections III and IV.

A. Video Coding Layer

The video coding layer of HEVC employs the same hybrid approach (inter-/intrapicture prediction and 2-D transform coding) used in all video compression standards since H.261. Fig. 1 depicts the block diagram of a hybrid video encoder, which could create a bitstream conforming to the HEVC standard.

reference picture and motion vector (MV) to predicting the samples of each block. The encoder generates identical interpicture prediction signals for motion compensation (MC) using the MV and reference data, which are transmitted as side information.

The residual signal of the intra- or interpicture prediction, which is the difference between the original block and the prediction, is transformed by a linear spatial transform. The transform coefficients are then scaled, quantized, and transmitted together with the prediction information.

The encoder duplicates the decoder processing (shown in gray-shaded boxes in Fig. 1) such that both encoder and decoder produce identical predictions for subsequent data. Thereby, the quantized transform coefficients are constructed by the encoder and are then inverse transformed to duplicate the residual signal. The reconstructed residual is added to the prediction, and the result of that addition is then fed into one or two loop filters to smooth the block. The block is then induced by block-wise processing and quantization to produce a picture representation (that is a duplicate of the original picture). The decoder (shown in gray-shaded boxes) is stored in a decoded picture buffer and used for the prediction of subsequent pictures. In general, the order of encoding or decoding processing of pictures differs from the order in which they arrive from the source; the distinction between the decoding order (i.e., bitstream order) and the output order (i.e., display order) for a picture is defined by the picture order count (POC).

Video material to be encoded by HEVC is expected to be input as progressive scan imagery (i.e., the source video originating in that format or format after deinterlacing prior to encoding). No explicit coding modes are present in the HEVC design to support the use of interlaced scanning, as interlaced scanning is no longer used and is becoming substantially less common for video. However, a metadata syntax has been provided to allow an encoder to indicate that interlace-scan video has been sent by coding each field (i.e., the even or odd lines of each video frame) of interlaced video as an HEVC coded picture. This provides an efficient way of coding interlaced video without burdening the decoder with the need to support a special decoding process for interlaced video.

In the following, the various features involved in video coding using HEVC are highlighted as follows:

1) Coding tree units and coding tree block (CTB)

The core of the coding layer in previous

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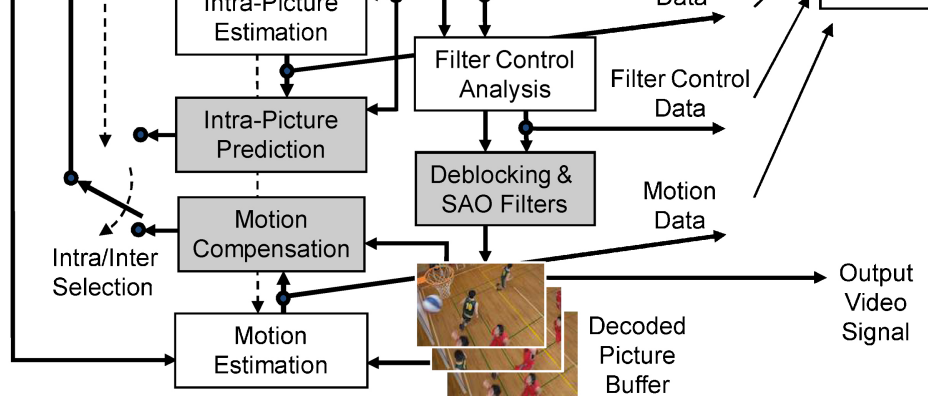


Fig. 1. Typical HEVC video encoder (with decoder modeling elements shaded in light gray).

the macroblock, containing a 16×16 block of luma samples and, in the usual case of 4:2:0 color sampling, two corresponding 8×8 blocks of chroma samples; whereas the analogous structure in HEVC is the coding tree unit (CTU), which has a size selected by the encoder and can be larger than a traditional macroblock. The CTU consists of a luma CTB and the corresponding chroma CTBs and syntax elements. The size $L \times L$ of a luma CTB can be chosen as $L = 16, 32, \text{ or } 64$ samples, with the larger sizes typically enabling better compression. HEVC then supports a partitioning of the CTBs into smaller blocks using a tree structure and quadtree-like signaling [8].

- 2) *Coding units (CUs) and coding blocks (CBs)*: The quadtree syntax of the CTU specifies the size and positions of its luma and chroma CBs. The root of the quadtree is associated with the CTU. Hence, the size of the luma CTB is the largest supported size for a luma CB. The splitting of a CTU into luma and chroma CBs is signaled jointly. One luma CB and ordinarily two chroma CBs, together with associated syntax, form a coding unit (CU). A CTB may contain only one CU or may be split to form multiple CUs, and each CU has an associated partitioning into prediction units (PUs) and a tree of transform units (TUs).
- 3) *Prediction units and prediction blocks (PBs)*: The decision whether to code a picture area using interpicture or intrapicture prediction is made at the CU level. A PU partitioning structure has its root at the CU level.

Depending on the basic prediction-type, luma and chroma CBs can then be further partitioned and predicted from luma and chroma prediction blocks (PBs). HEVC supports variable PB sizes, which can be as small as 4×4 samples.

- 4) *TUs and transform blocks*: The prediction units are further partitioned into transform units (TUs) and coded using block transforms. A TU tree has its root at the CU level. The luma CB root is identical to the luma transform block (TB). The TB can be further split into smaller luma TBs. The same applies to the chroma TBs. Integer basis functions such as those of a discrete cosine transform (DCT) are used for square TB sizes $4 \times 4, 8 \times 8, 16 \times 16, \text{ and } 32 \times 32$. For 4×4 transform of luma intrapicture prediction, an integer transform derived from a form of discrete sine transform (DST) is alternatively specified.
- 5) *Motion vector signaling*: Advanced motion vector prediction (AMVP) is used, including derivation of most probable candidates based on data from neighboring PUs and the reference picture. A merge mode of coding can also be used, allowing the derivation of MVs from temporally or spatially neighboring PUs. Moreover, compared to H.264/MPEG-4 AVC, skip and direct motion inference are also supported.
- 6) *Motion compensation*: Quarter-sample precision is used for the MVs, and 7-tap or 8-tap filters are used for interpolation of fractional-sample positions. Additionally, six-tap filtering of half-sample positions is used, and linear interpolation for quarter-sample positions is used.

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DC (flat) prediction modes. The selected intrapicture prediction modes are encoded by deriving most probable modes (e.g., prediction directions) based on those of previously decoded neighboring PBs.

- 8) *Quantization control*: As in H.264/MPEG-4 AVC, uniform reconstruction quantization (URQ) is used in HEVC, with quantization scaling matrices supported for the various transform block sizes.
- 9) *Entropy coding*: Context adaptive binary arithmetic coding (CABAC) is used for entropy coding. This is similar to the CABAC scheme in H.264/MPEG-4 AVC, but has undergone several improvements to improve its throughput speed (especially for parallel-processing architectures) and its compression performance, and to reduce its context memory requirements.
- 10) *In-loop deblocking filtering*: A deblocking filter similar to the one used in H.264/MPEG-4 AVC is operated within the interpicture prediction loop. However, the design is simplified in regard to its decision-making and filtering processes, and is made more friendly to parallel processing.
- 11) *Sample adaptive offset (SAO)*: A nonlinear amplitude mapping is introduced within the interpicture prediction loop after the deblocking filter. Its goal is to better reconstruct the original signal amplitudes by using a look-up table that is described by a few additional parameters that can be determined by histogram analysis at the encoder side.

B. High-Level Syntax Architecture

A number of design aspects new to the HEVC standard improve flexibility for operation over a variety of applications and network environments and improve robustness to data losses. However, the high-level syntax architecture used in the H.264/MPEG-4 AVC standard has generally been retained, including the following features.

- 1) *Parameter set structure*: Parameter sets contain information that can be shared for the decoding of several regions of the decoded video. The parameter set structure provides a robust mechanism for conveying data that are essential to the decoding process. The concepts of sequence and picture parameter sets from H.264/MPEG-4 AVC are augmented by a new video parameter set (VPS) structure.

typically restricted, and the number of CTUs in a slice is often varied to minimize the packetization overhead while keeping the size of each packet within a certain limit.

- 4) *Supplemental enhancement information (SEI) and usability information (VUI) metadata*: The SEI and VUI metadata includes support for various types of metadata, such as SEI and VUI. Such data provide information about the timing of the video pictures, the proper color space used in the video signal, 3-frame packing information, other display information, and so on.

C. Parallel Decoding Syntax and Modified Slice Structure

Finally, four new features are introduced in the HEVC standard to enhance the parallel processing capabilities. The structuring of slice data for packetization purposes of them may have benefits in particular applications, and it is generally up to the implementer of the decoder to determine whether and how to take advantage of these features.

- 1) *Tiles*: The option to partition a picture into rectangular regions called tiles has been specified. The purpose of tiles is to increase the capabilities for parallel processing rather than provide error resilience. Each tile is independently decodable regions of a picture. A picture encoded with some shared header information can be accessed additionally be used for the purpose of random access to local regions of video pictures. The tile configuration of a picture consists of dividing the picture into rectangular regions with equal numbers of CTUs in each tile. This allows for parallelism at a more coarse level of granularity (picture/subpicture), and no sophisticated synchronization between threads is necessary for their use.
- 2) *Wavefront parallel processing*: When wavefront parallel processing (WPP) is enabled, a slice is processed in rows of CTUs. The first row is processed first. In this way, the second row can begin to be processed as soon as only two CTUs have been processed in the first row, the third row can begin to be processed as soon as two CTUs have been processed in the second row, and so on. The context models of the CTUs in each row are inferred from those in the previous row with a two-CTU processing lag. This is a form of processing parallelism at a rather fine level.

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in low-delay encoding, where other parallel tools might penalize compression performance.

In the following two sections, a more detailed description of the key features is given.

III. HIGH-LEVEL SYNTAX

The high-level syntax of HEVC contains numerous elements that have been inherited from the NAL of H.264/MPEG-4 AVC. The NAL provides the ability to map the video coding layer (VCL) data that represent the content of the pictures onto various transport layers, including RTP/IP, ISO MP4, and H.222.0/MPEG-2 Systems, and provides a framework for packet loss resilience. For general concepts of the NAL design such as NAL units, parameter sets, access units, the byte stream format, and packetized formatting, please refer to [9]–[11].

NAL units are classified into VCL and non-VCL NAL units according to whether they contain coded pictures or other associated data, respectively. In the HEVC standard, several VCL NAL unit types identifying categories of pictures for decoder initialization and random-access purposes are included. Table I lists the NAL unit types and their associated meanings and type classes in the HEVC standard.

The following subsections present a description of the new capabilities supported by the high-level syntax.

A. Random Access and Bitstream Splicing Features

The new design supports special features to enable random access and bitstream splicing. In H.264/MPEG-4 AVC, a bitstream must always start with an IDR access unit. An IDR access unit contains an independently coded picture—i.e., a coded picture that can be decoded without decoding any previous pictures in the NAL unit stream. The presence of an IDR access unit indicates that no subsequent picture in the bitstream will require reference to pictures prior to the picture that it contains in order to be decoded. The IDR picture is used within a coding structure known as a closed GOP (in which GOP stands for group of pictures).

The new clean random access (CRA) picture syntax specifies the use of an independently coded picture at the location of a random access point (RAP), i.e., a location in a bitstream at which a decoder can begin successfully decoding pictures without needing to decode any pictures that appeared earlier in the bitstream, which supports an efficient temporal coding

33	Sequence parameter set (SPS)
34	Picture parameter set (PPS)
35	Access unit delimiter
36	End of sequence
37	End of bitstream
38	Filler data
39, 40	SEI messages
41–47	Reserved for future use
48–63	Unspecified (available for system use)

order known as open GOP operation. Good support for random access is critical for enabling channel switching, time-shift operations, and dynamic streaming services. Some pictures may follow a CRA picture in decoding order and precede it in the bitstream. This order may contain interpicture prediction references to pictures that are not available at the decoder. These pictures must therefore be discarded by a decoder during its decoding process at a CRA point. For this reason, pictures that are nondecodable are identified as random access leading (RASL) pictures. The location of splicing points in different original coded bitstreams can be indicated by bitstream link access (BLA) pictures. A bitstream splice operation can be performed by simply changing the NAL unit type of a CRA picture in one bitstream to the value of a BLA picture and concatenating the new bitstream to the position of a RAP picture in the other bitstream. A BLA picture may be an IDR, CRA, or BLA picture. A BLA picture and concatenating the new bitstream to the position of a RAP picture may be followed by BLA pictures in the bitstream (depending on the particular NAL unit type used for a BLA picture). Any pictures associated with a BLA picture must always be decodable at the decoder, as they may contain references to pictures that are not actually present in the bitstream due to the splice operation. The other type of picture that can be decoded in decoding order and precede it in the bitstream is the random access decodable leading (RADL) picture. A RADL picture cannot contain references to any pictures that appear before the RAP picture in decoding order. RASL and RADL pictures are collectively referred to as leading pictures (LPs). Pictures that follow a RAP picture in both decoding order and bitstream order, which are known as trailing pictures, cannot contain references to LPs for interpicture prediction.

B. Temporal Sublayering Support

Similar to the temporal scalability feature supported in H.264/MPEG-4 AVC scalable video coding (SVC) [12],

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