**PNY Technologies, Inc.** 

# **EXHIBIT 1004**

PNY Exhibit 1004 Inter Partes Review of

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#### **1** (10) Pub. No.: US 2006/0002096 A1 (43) Pub. Date: Jan. 5, 2006

#### (54) SYSTEM AND METHOD FOR PROVIDING A FLASH MEMORY ASSEMBLY

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Wang et al.

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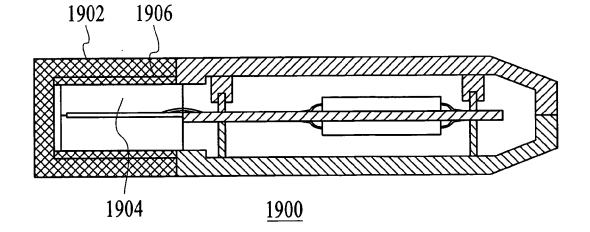
**Publication Classification** 

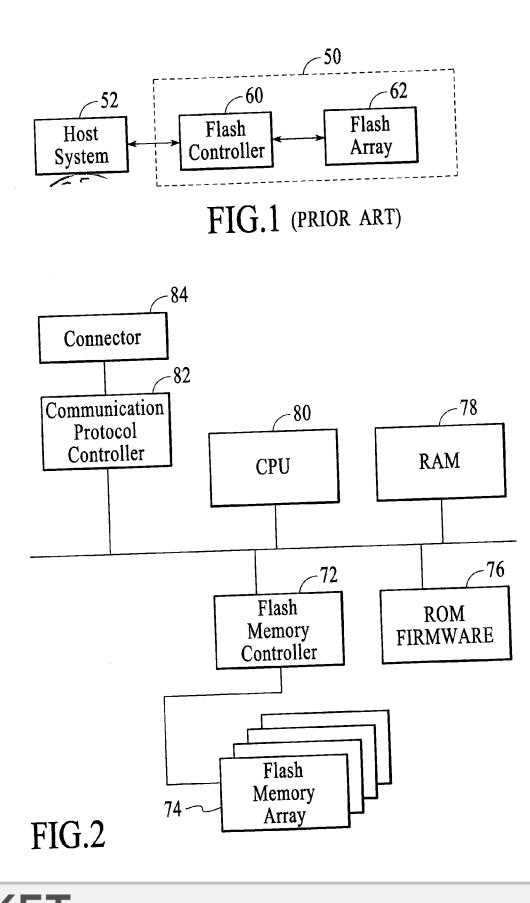
(2006.01)

(51) Int. Cl. *H05K 5/03* 

#### (57) **ABSTRACT**

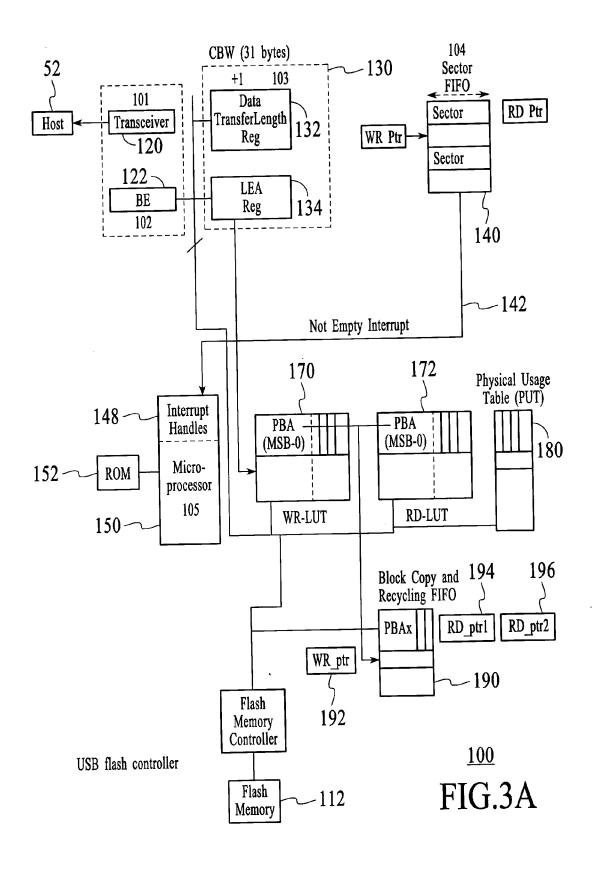
The present invention relates to a method and system for providing a flash memory assembly. The flash memory assembly includes a connector and a printed circuit board (PCB) coupled to the connector. The center of the PCB is positioned substantially at the center of the connector. An electronic component is coupled to one side of the PCB. In another aspect of the present invention, a second electronic component is coupled to a second side of the PCB. In another aspect of the present invention, the electronic components and the PCB are protected by covers joined using interference fitting or ultrasonic joining. In another aspect of the present invention, a cap protects the connector. The cap can be removably coupled to the connector.





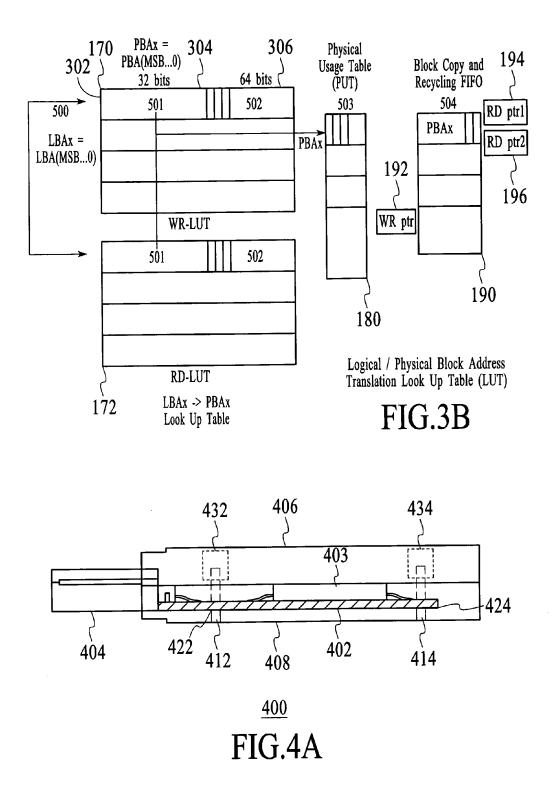
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