

[54] **METHOD AND APPARATUS FOR CONTROLLING A MULTI-MODE SEGMENTED DISPLAY**

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[51] Int. Cl. .... **G06f 3/14**

[58] Field of Search ..... **340/324 AD, 154**

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[57] **ABSTRACT**

A method and apparatus for controlling a segmented display on a processor read-out device. An addressable memory is provided for storing segments of information which may be displayed. The addresses of the segments presently being displayed are stored and these addresses are utilized to retrieve the information to be displayed in each of the segments.

The segmented display may be presented in a plurality of different modes. A capability is provided for changing the mode being displayed at any given time and for altering the information which is displayed in each of the modes. When there is a change in the mode of display, various functions must be performed. Among these functions may be the positioning of an entry marker, the saving or clearing of a memory segment which may be used for different types of information in different modes, and the chaining together of related segments for the selected mode.

53 Claims, 26 Drawing Figures

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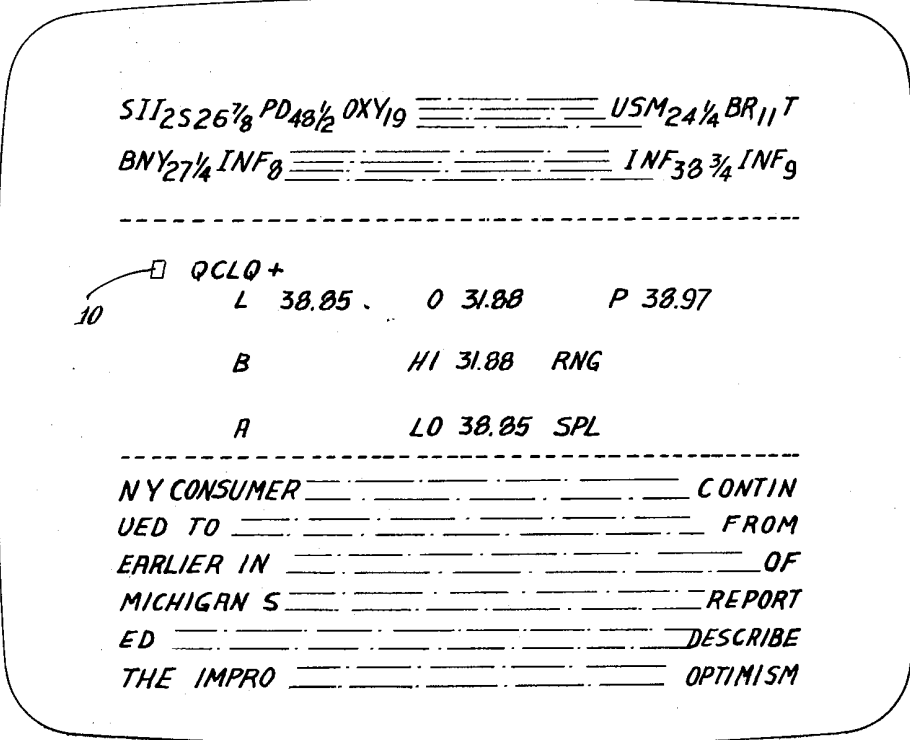


Fig. 1.

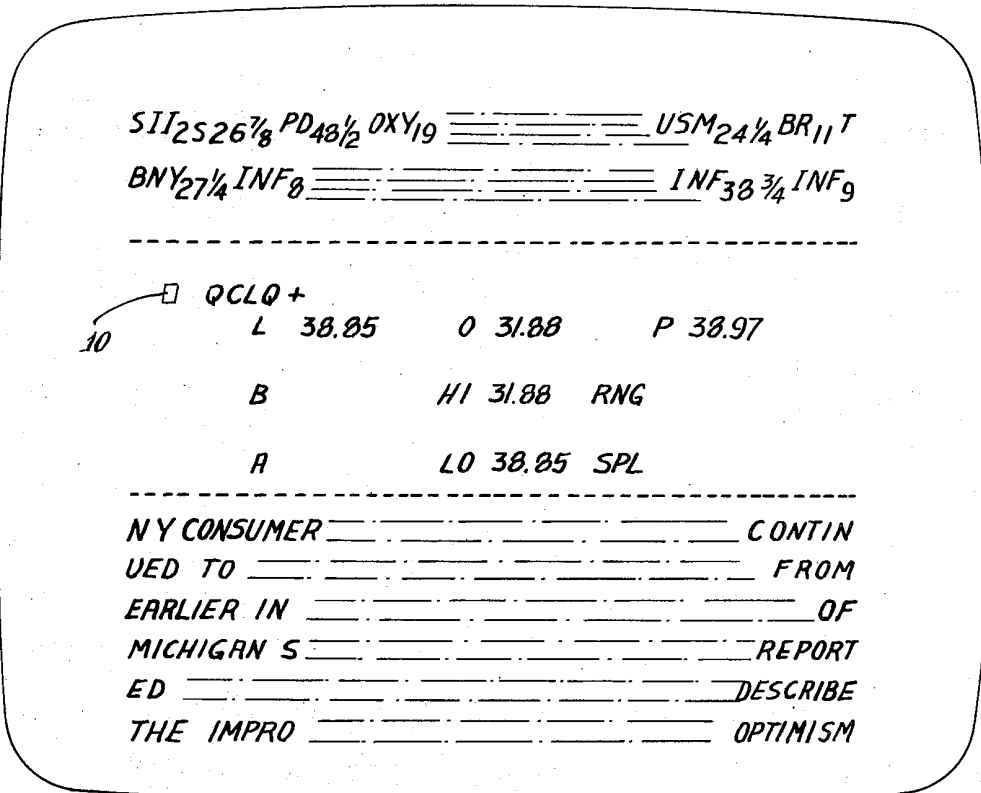


Fig. 2.

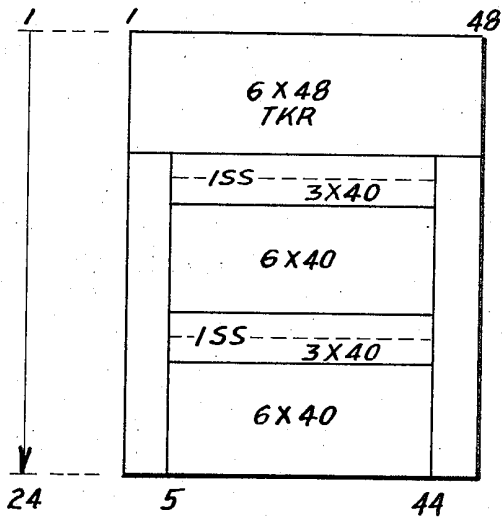


Fig. 3.

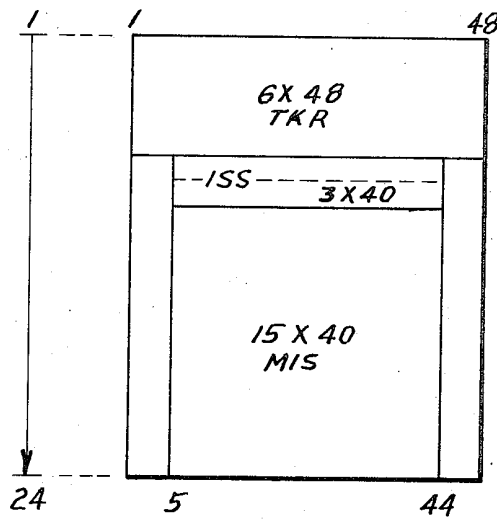


Fig. 4.

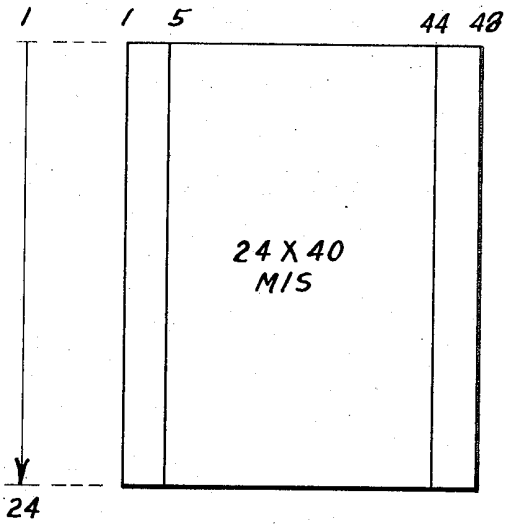


Fig. 5.

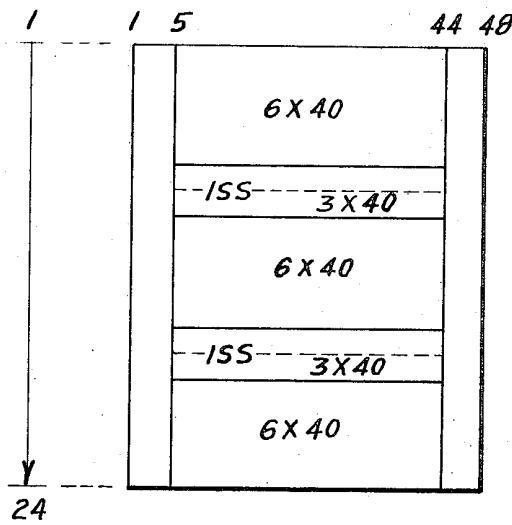


Fig. 6A.

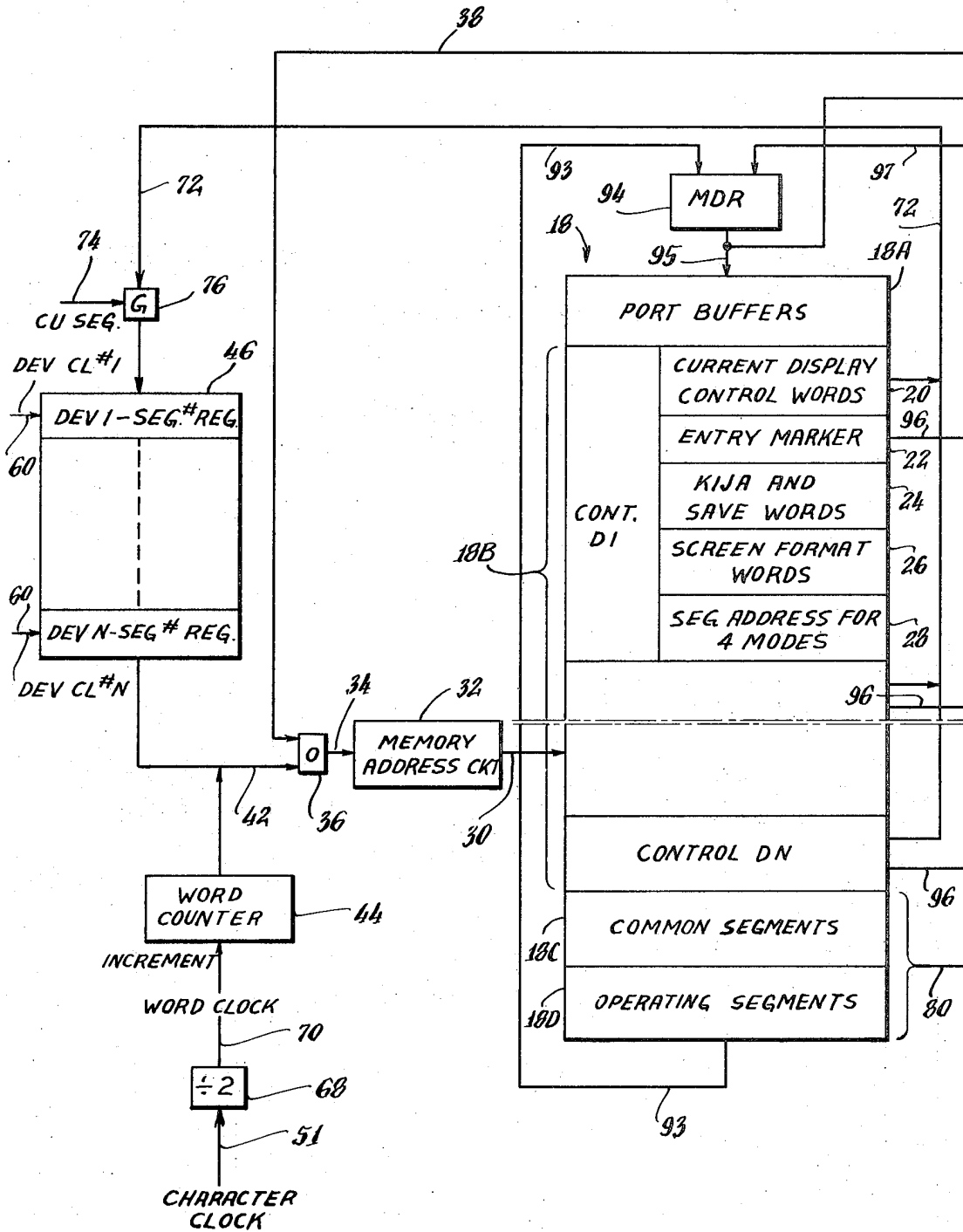
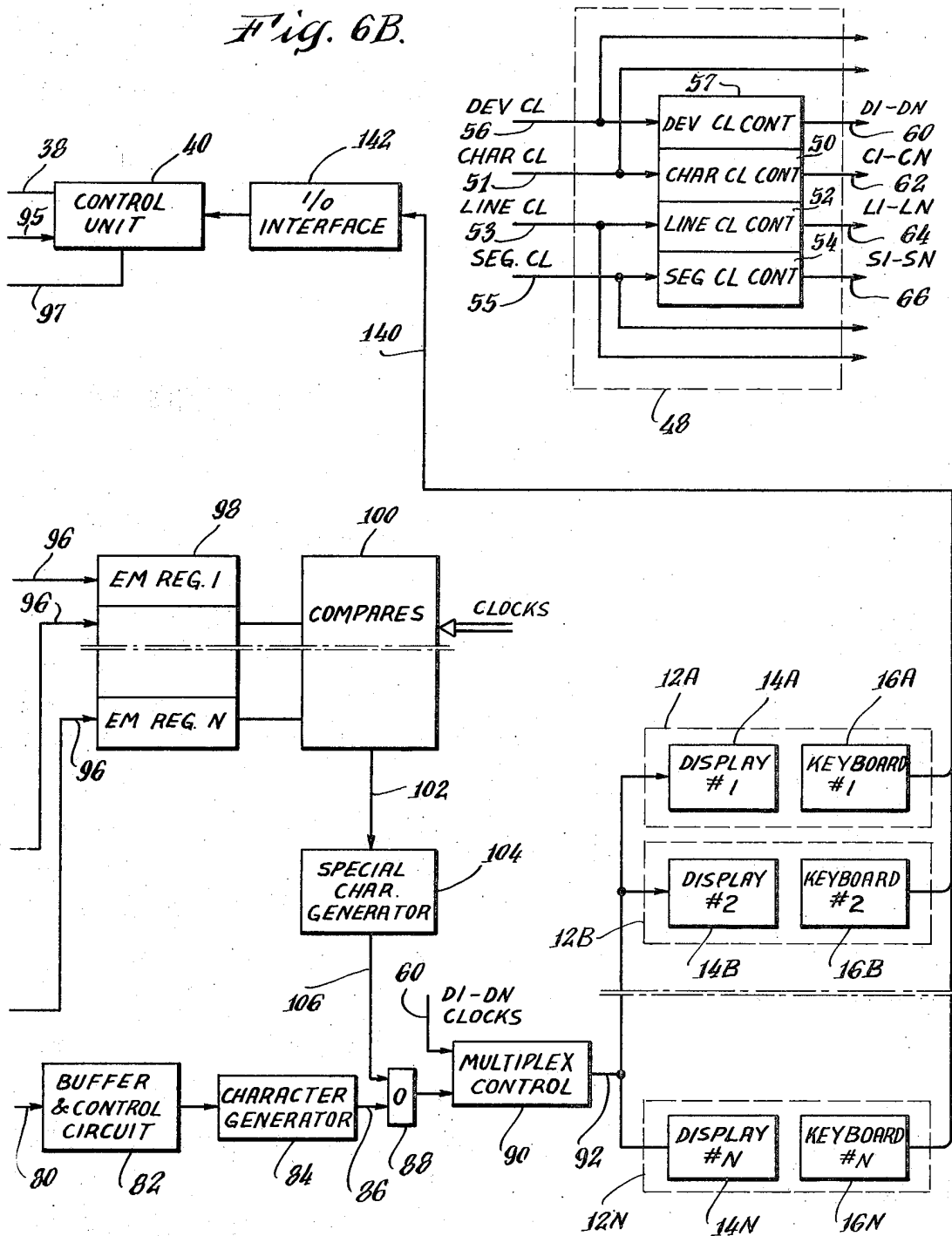


Fig. 6B.



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