

SEL EXHIBIT NO. 2026

INNOLUX CORP. v. PATENT OF SEMICONDUCTOR ENERGY
LABORATORY CO., LTD.

IPR2013-00066

1 UNITED STATES PATENT AND TRADEMARK OFFICE
2 BEFORE THE PATENT TRIAL AND APPEAL BOARD

3 INNOLUX CORPORATION,)
4)
5 Petitioner,)
6)
7 vs.) IPR2013-00068
8) U.S. Pat. No.
9 SEMICONDUCTOR ENERGY) 8,066,204
10 LABORATORY CO., LTD.,)
11)
12 Patent Owner.)

13 The videotaped deposition of MICHAEL J.
14 ESCUTI, Ph.D., called by the Petitioner for
15 examination, pursuant to Notice, and pursuant to
16 the applicable rules, taken before Sandra L.
17 Rocca, CSR, CRR, at 115 South LaSalle Street,
18 Chicago, Illinois, on the 6th day of September,
19 2013, at the hour of 9:49 a.m.
20
21
22
23
24
25

Page 2

1 APPEARANCES:
2
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9 appeared on behalf of the
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11
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appeared on behalf of the
Patent Owner.

Also Present:
Ms. Mary Ann Naas, Videographer

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1 VIDEOGRAPHER: Okay. We're on record.
2 My name is Mary Ann Naas of Veritext. Today's
3 date is September 6th, 2013. The time is
4 approximately 9:49.
5 This deposition is being held in the
6 office of Steptoe & Johnson located at 115 South
7 LaSalle Street, Chicago, Illinois.
8 The caption of the case is Innolux Corp.
9 versus Patent of Semiconductor Energy Lab, case
10 number IPR 2013-00068, Patent No. 8,066,204, in
11 the United States Patent and Trademark Office
12 before the Patent Trial and Appeal Board. The
13 name of the witness is Dr. Michael Escuti.
14 At this time will the attorneys please
15 identify themselves and the parties they
16 represent, after which our court reporter, Sandra
17 Rocca of Veritext, will swear in the witness and
18 we can proceed.
19 MR. GIBSON: Stan Gibson on behalf of
20 the Petitioner.
21 MR. SCHLITTER: Stan Schlitter of
22 Steptoe & Johnson and Edward Manzo from Husch
23 Blackwell on behalf of the patent owner.
24
25

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3 MICHAEL J. ESCUTI, Ph.D.
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8 EXHIBITS
9 NUMBER PRESENTED
10 Deposition Exhibit
11 No. 1004 U.S. Pat. No. 5,504,601 144
12 No. 1005 U.S. Pat. No. 5,636,329 106
13 No. 1008 Late-News Paper: Polarization
14 Independent Liquid Crystal
15 Microdisplays 53
16 No. 1009 Schematic of Fig. A, pg. 40
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22 No. 1012 U.S. Pat. No. 8,068,204 9
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24 No. 2011 Escuti declaration re
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1 MICHAEL J. ESCUTI, Ph.D.,
2 having been first duly sworn, was examined and
3 testified as follows:
4 EXAMINATION
5 BY MR. GIBSON:
6 Q. Good morning. If you could once again
7 spell your last name and state your name for the
8 record.
9 A. Good morning. My last name is spelled
10 E-s-c-u-t-i and my full name is Michael James
11 Escuti.
12 Q. And I went over the background rules for
13 the deposition yesterday. I'm not going to repeat
14 those unless you would like me to do so.
15 Would you like me to repeat those for
16 you?
17 A. There's no need to do so.
18 Q. And there's no reason your deposition
19 can't proceed today?
20 A. There's no reason.
21 Q. All right. Let's -- we're now here
22 dealing with the '204 patent and you also
23 submitted a declaration in that matter as well?
24 A. I did.
25

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1 (Document marked previously as Exhibit
2 Number 2011 was presented.)
3 BY MR. GIBSON:
4 Q. And I'm going to hand you a copy of that
5 and ask you to take a look at it and confirm that
6 it's your declaration.
7 A. It does appear to be my declaration and
8 exhibit -- I'm sorry, declaration and appendices
9 but not the exhibits.
10 Q. And if you look at Appendix B to your
11 declaration --
12 A. I see it.
13 Q. -- are those the materials that you
14 reviewed to prepare your declaration?
15 A. Yes.
16 Q. And did you review anything else in
17 preparing your declaration?
18 A. In forming the opinions that are
19 expressed here and in preparing the declaration
20 itself, I didn't review anything else in addition
21 to this list.
22 Q. So you didn't look at any other patents,
23 for example?
24 A. Not for the purpose of forming the
25 opinions and preparing the declaration, no. As I

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1 did mention yesterday, there were other things I
2 looked at but decided not to spend any more time
3 on, other than recognizing that I had seen them.
4 Q. And do you recall any patents you looked
5 at and decided not to consider?
6 A. No. I certainly don't recall any -- any
7 of those.
8 Q. How much time did you spend looking at
9 the things you decided not to consider?
10 A. A small -- small number of hours,
11 one hour, not very much time at all.
12 Q. Were -- those things that you didn't
13 consider, were those provided to you by counsel or
14 were those just things you looked at on your own?
15 A. Those were things that I looked at on my
16 own.
17 Q. Were there any things that were provided
18 by counsel that you did not consider?
19 A. Not that I can recall. This list seems
20 to be complete in that regard.
21 Q. And the CV that's attached to your
22 declaration, is it the same CV as yesterday?
23 A. It appears to be, but there does appear
24 to be two copies of it. I'm not sure if that's
25 our error or an error that happened in your

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1 printing, but it does seem to be that there's two
2 copies of the same thing.
3 Q. Okay. And I'm not sure if that's
4 attached to the original that way or if that was
5 something that was done in the copying either, but
6 putting that aside, is it the same CV, albeit with
7 two copies of the one we went through yesterday?
8 A. It does appear to be the same and that
9 certainly was my intention.
10 Q. If you'd look at paragraph 52 of your
11 declaration --
12 A. I see it.
13 Q. -- and if you have a moment, just to
14 read that to yourself. Just let me know when
15 you're done.
16 A. I've read it.
17 Q. And what are you trying to articulate
18 there?
19 A. The statement says what it says and I
20 stand by it, that an ordinarily skilled artisan
21 understands that this terminal in the '204 patent
22 is fabricated generally from the bottom up,
23 beginning with the foundation and substrate and
24 then the other layers. And that's required
25 because of the processing that's needed during the

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1 fabrication.
2 Q. And this figure's coming from the '204
3 patent, is that correct?
4 A. Yes, I believe it's Fig. 4A.
5 (Document marked previously as Exhibit
6 Number 1012 was presented.)
7 BY MR. GIBSON:
8 Q. I'm going to hand you the '204 patent.
9 And --
10 A. To be clear, it's been colorized in my
11 declaration. So it's a modification of Fig. 4A,
12 but that's where it's from.
13 Q. Right. Now, when you look at say
14 Claim 1 of the '204 patent -- have you had a
15 chance to look at that?
16 A. While I have reviewed Claim 1 of the
17 '204 patent, I certainly haven't spent a lot of
18 time reading it. That certainly was not my focus.
19 Q. Okay, fair enough.
20 What would you consider to be a
21 representative claim that would embody what's in
22 4A in the '204 patent?
23 A. Can you tell me what you mean by
24 "representative claim"?
25 Q. What's a claim that would claim the

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1 features that you see in Fig. 4A of the '204
2 patent?
3 A. Well, Fig. 4A is an embodiment of
4 Claim 31 and maybe others. So is that what you're
5 asking, what my opinion is?
6 Q. Yeah, and what specific claim do you
7 think would cover that embodiment?
8 MR. SCHLITTER: Objection, form.
9 THE WITNESS: Well, I'm quite certain
10 that Fig. 4A covers multiple claims in this
11 patent. The one that comes to mind first is
12 Claim 31, but by no means is it limited to that.
13 BY MR. GIBSON:
14 Q. Okay. And I'm not asking for a limit.
15 I'm asking for something that would be
16 representative.
17 And Claim 31 uses the language of
18 "first" and "second," for example? There's those
19 words in that claim?
20 A. The words "first" and "second" are
21 indeed in Claim 31, but of course applied to lines
22 -- oh, I'm sorry. Well, there's the insulating
23 films in Claim 31.
24 In Claim 54, which also I think applies,
25 it's used in a different way, those words. But

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1 the words "first" and "second" do appear in
2 Claim 31.
3 Q. All right. And when you -- or when an
4 ordinary -- when a person of -- an ordinary person
5 of skill in the art reads a claim that states
6 "first" and "second," would you understand that
7 that's referring to the order that the layers are
8 deposited --
9 MR. SCHLITTER: Objection, form.
10 BY MR. GIBSON:
11 Q. -- on the substrate?
12 MR. SCHLITTER: And foundation.
13 THE WITNESS: It would depend on the
14 claim that the person of ordinary skill is
15 reading.
16 BY MR. GIBSON:
17 Q. Well, the Claim 31, for example, how
18 would you interpret the order of the deposition
19 steps as being set forth in that claim?
20 A. Are you asking generally or do you want
21 me to give you a comprehensive --
22 Q. Specifically in Claim 31, how would a
23 person of ordinary skill in the art understand the
24 words "first" and "second" in terms of a direction
25 into -- or the depositing of the layers?

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1 A. Well, in Claim 31, first of all, the
2 terms "first insulating film" and "second
3 insulating film" are part of the claim and they
4 are part of the claim in a sequence of
5 limitations, which I'd like to go through to lay
6 the ground work for my answer, right.
7 So it's clearly a liquid crystal display
8 device. There must be a substrate with thin film
9 transistors, pixel electrodes each electrically
10 connected to one of the thin film transistors, a
11 counter substrate facing the substrate, a liquid
12 crystal material and a sealant provided between
13 the substrate and the counter substrate.
14 And then we get into the claim
15 limitations that are really particularly at issue.
16 There's an auxiliary line, an external connection
17 line overlapping the auxiliary line with a first
18 insulating film interposed there between. So
19 that's the first instance of the word "first"
20 applied to the insulating film.
21 The word "first" in this use doesn't
22 require a sequence, but its relationship of this
23 element to the other elements identified here does
24 imply a sequence. It's not so much the word
25 "first," but rather that whole limitation that

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1 describes the relationship between those three
2 elements.
3 Q. All right. And so it's obviously not --
4 what you're saying is it's not the word "first" by
5 itself, but you understand in the context of that
6 claim when you see the words "first" and "second,"
7 there is an order that's being directed in how
8 you're going to deposit these layers?
9 MR. SCHLITTER: Objection, form.
10 THE WITNESS: In Claim 31, the order
11 that is and sequence of the layers that are
12 present is not principally linked to the use of
13 the word "first" or the use of the word "second."
14 It's the other descriptions that are provided that
15 describe that.
16 The function in this claim of the word
17 "first" and "second" principally goes to identify
18 that there's two separate insulating films.
19 BY MR. GIBSON:
20 Q. Would that -- would you understand if
21 you didn't have the words "first" and "second,"
22 that you could order these in a -- well, strike
23 that.
24 Let's look at Claim 54. In Claim 54,
25 you also have the words "first" and "second"?

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1 A. Yes, they're used and applied on first
2 conductive line, second conductive line as well as
3 first insulating film and second insulating film.
4 Q. And the statement where you say, "a
5 first conductive line over the substrate," you
6 would understand that element to be telling you a
7 sequence of how you're going to deposit that
8 layer, correct?
9 A. Well, not strictly speaking. If there's
10 a substrate, there should be a -- in this claim
11 limitation, there should be a first conductive
12 line over that substrate. I think a person of
13 ordinary skill would normally expect that the
14 substrate is first somehow manufactured and then
15 prepared for the deposition of that conductive
16 line, but the claim certainly doesn't require
17 that. It could be the inverse.
18 Q. What do you mean it could be the
19 inverse?
20 A. It's entirely possible to have a metal
21 layer formed and a material deposited onto that
22 that will later serve the function of the
23 substrate. You know, for example, in flexible
24 displays, that's a possibility. I'm not saying
25 it's common or -- or preferred, but it's certainly

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1 an aspect that's explored in that context.
2 Q. Okay. But looking at Claim 54, you
3 would not understand Claim 54 to be directing that
4 kind of step, right?
5 A. It doesn't direct either way. It simply
6 says there must be a first conductive line over
7 the substrate and that word "over" then places a
8 direction above a substrate that's going to be
9 built upon in the rest of the claim limitations.
10 Q. All right. So you're saying the
11 substrate could come after the first conductive
12 line?
13 A. Claim 54 could certainly apply to
14 terminals where a first conductive line is first
15 somehow prepared and then a substrate material is
16 applied onto that.
17 It sounded to me like you were asking me
18 if this limitation required a sequence between a
19 substrate first and then a first conductive line
20 and I'm simply commenting that that's not the what
21 claim limitation requires. It could be the
22 inverse as well.
23 Q. When you look at a first insulating film
24 over the first conductive line, could that be the
25 inverse as well?

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1 A. Well, in that case, I don't think a
2 person of ordinary skill would -- would be able to
3 see that as the inverse. So in that case, it's
4 building on top of that first conductive line
5 because of the word "over."
6 Q. No, the word "over" is also used in the
7 previous element.
8 A. It is.
9 Q. First conductive line over the
10 substrate.
11 A. It is.
12 Q. So how is the word "over" being used
13 differently in the element of first insulating
14 film over the first conductive line?
15 A. Well, I'd like to be clear. I think a
16 person of ordinary skill would normally read this,
17 a first conductive line over the substrate
18 limitation and understand that the substrate would
19 be prepared first and then the first wiring line
20 would be deposited and patterned on top of it, but
21 the claim limitation does not require that.
22 That's simply what I'm -- I'm pointing out.
23 And the use of the word "over," the
24 first time it's used refers to the arrangement of
25 just two things. Whereas its second use refers to

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1 its use with respect to more than two things.
2 Q. So would you then read a limitation into
3 a first conductive line over the substrate as
4 saying the substrate's going to have to come
5 before the first conductive line because the next
6 element of first insulating film over the first
7 conductive line requires the insulating film to be
8 over the conductive line?
9 MR. SCHLITTER: Objection, form.
10 THE WITNESS: I -- I don't think that a
11 person of ordinary skill would read the phrase "a
12 first conductive line over the substrate" as
13 requiring that the substrate come first. That is
14 a preferable way to do it certainly, but it's not
15 required.
16 However, when it's used in the next
17 limitation, the first insulating film over the
18 first conductive line, there is an order that's
19 required there because it's -- it's describing the
20 relationship of the first insulating film which
21 now must be over the first conductive line, which
22 of course is already over the substrate.
23 BY MR. GIBSON:
24 Q. Now, when you look at the next element,
25 a second conductive line -- actually let me just

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1 follow-up on what you just said.
2 When you look at the two elements
3 together, a first conductive line over the
4 substrate, a first insulating film over the first
5 conductive line, are you saying that that does
6 tell you that the substrate is going to come first
7 and then you're going to have a conductive line
8 and then you're going to have an insulating film
9 over the conductive line?
10 A. Perhaps I misunderstand what you mean by
11 "first." What sequence are you referring to more
12 specifically, the sequence that is used during the
13 actual fabrication or in --
14 Q. No, I'm looking at the claim language
15 and I want to make sure it's not what I
16 understand, it's what you understand about the
17 words "first" that matter.
18 And I'm just trying to understand, if
19 you look at those two claim elements, a first
20 conductive line over the substrate, a first
21 insulating film over the first conductive line,
22 does that tell you that there's going to be a
23 substrate and then a conductive line over the
24 substrate and then a first insulating film over
25 the first conductive line?

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1 A. At the end of whatever process is used
2 to implement these claim limitations, there should
3 be a substrate, which I'd prefer to talk about as
4 being on the lower side of the element. The next
5 element should be a first conductive line, and
6 then the next element should be a first insulating
7 film. I think that structure is required by those
8 claim limitations. I think the difference that
9 I'm trying to express is that that structure can
10 be reached even if the substrate isn't the first
11 thing that's actually fabricated.
12 Q. Okay. When we get to the next element,
13 a second conductive line over the first insulation
14 film, would you understand that to require that
15 the second conductive line is coming after the
16 first conductive line has been deposited?
17 A. I think a person of ordinary skill
18 would -- would read a second conductive line over
19 the first insulating film as requiring that the
20 deposition and patterning of the second conductive
21 line should happen after the first insulating film
22 is already deposited.
23 Q. All right. So, I mean, you wouldn't
24 expect someone to build this backwards. In other
25 words, we wouldn't start with the second

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1 conductive line and then put down -- put an
2 insulating film and put a first conductive line
3 next and then put a substrate on top of that?
4 A. Well, the claim refers to the final
5 relative relationships between these elements.
6 The claim doesn't have forming language. It's not
7 a process claim. So no, I don't think I can agree
8 that it requires what I think I heard you
9 describe. This whole thing could have been
10 fabricated in an inverted way.
11 Q. As one of ordinary skill in the art,
12 would you be able to fabricate this? If you look
13 at all the claims, would you be able to fabricate
14 this in an inverted way, opening up the particular
15 layers and --
16 A. Well, what I'm -- what I'm I guess most
17 clearly trying to say is that the path of
18 fabrication, the process of creating these layers
19 can take many different paths and I don't think
20 the claim limits how the structure is gotten to.
21 It does limit the relative relationships of those
22 -- of those layers.
23 So there's a normal way that I think one
24 of ordinary skill would anticipate building this
25 and that's certainly corresponding with what

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1 you're asking me to agree to, but I don't think
2 the claim limits it to that.
3 Q. So you wouldn't agree that in Claim 54,
4 the sequence of the disclosed layers necessarily
5 follows that you're going to have the first
6 conductive line deposited, then the insulating
7 film deposited and patterned to enable the
8 required electrical connection?
9 A. Well, it depends on what you mean by
10 "sequence."
11 Q. What do you mean?
12 A. Well, again, if we're talking about the
13 sequence used during fabrication, that's one
14 thing. If we talk about the sequence looking at
15 the structure itself at the end, it's a different
16 matter. So the claim does require the relative
17 sequence in the final structure.
18 Q. As one of ordinary skill in the art,
19 isn't that how you're going to have to deposit
20 them in order to achieve that structure?
21 A. Again, I gave you the example in the
22 first claim limitation that we were talking about,
23 a first conductive line over the substrate. There
24 are at least two ways to achieve that claim
25 limitation. One is where the substrate is somehow

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1 formed and then the first conductive line is
2 patterned -- deposited and patterned on top of it.
3 I think that would be a very usual
4 example, but the opposite could have also been the
5 case, where the first conductive line is somehow
6 prepared and the substrate material applied on top
7 of that. In either way, you still wind up with
8 that relative relationship of the two and you can
9 then process the rest of these on top of that.
10 So in the end, the relative sequence in
11 looking at the final structure, going from the
12 substrate and to the next layer that's over the
13 substrate in the language of the claim, the
14 sequence is prescribed.
15 Q. But you think that the deposition steps
16 could be reversed from what the sequence is when
17 you're looking at the finished product?
18 A. What --
19 MR. SCHLITTER: Objection, form.
20 THE WITNESS: What are you asking me to
21 reverse?
22 BY MR. GIBSON:
23 Q. Well, what I'm trying to understand is
24 the order of the deposition steps that you would
25 understand would happen from Claim 54. And I

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1 would assume that -- well, I don't want to assume
2 anything. Let me just ask you a question.
3 Don't you think that based on what
4 Claim 54 says, that a person of ordinary skill in
5 the art is going to understand the sequence of the
6 disclosed layers to be that the first conductive
7 line is deposited, then the insulating film is
8 deposited and patterned, then the -- then the
9 second conductive line is deposited, followed by
10 the second insulating film and then followed by
11 the transparent conductive film? Isn't that what
12 Claim 54 is directing the sequence of deposition
13 steps to be?
14 A. Claim 54 doesn't direct the sequence of
15 the deposition steps. It directs the sequence of
16 the layers in the final structure, the final
17 terminal. The sequence that you just described is
18 certainly one example that I think a person of
19 ordinary skill would -- would follow, but it's not
20 limited to that. The claim is not limited to
21 that. It's limited in its relative sequence at
22 the end.
23 Q. But wouldn't you agree that the
24 deposition steps necessarily follow from the
25 language of Claim 54?

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1 A. Do you mean the sequence of the
2 deposition steps?
3 Q. Yes.
4 A. I -- I can't agree exactly with that.
5 Again, the claim doesn't describe a -- it's not a
6 process claim. It's a claim that limits the
7 structure of the final terminal.
8 Q. If you look at your declaration,
9 paragraph 54, and in paragraph 54, you state "In
10 Claim 54, and Claim 61, 68 and 76 reciting similar
11 limitations, the sequence of the disclosed layers
12 necessarily follows:
13 "First, the first conductive line metal,
14 401 in Fig. 4A is deposited. Second, the first
15 insulating film 112 in Fig. 4A is deposited and
16 patterned to enable the required electrical
17 connection between the first and second conductive
18 lines. Third, the second conductive line metal
19 403 in Fig. 4A is deposited. Fourth, the second
20 insulating film 113 in Fig. 4A is deposited and
21 patterned to have an opening. Fifth, the
22 transparent conductive film 114 in Fig. 4A is
23 deposited and patterned."
24 Those were your words, correct?
25 A. Yes, and I stand by paragraph 54 fully.

Page 25

1 Q. And you stand by the sentence that says
2 "in Claims 54, the sequence of the disclosed
3 layers necessarily follows"?"
4 A. I certainly do. That's what I'm
5 expressing right now. The sequence of the
6 disclosed layers in the final terminal assembly
7 necessarily follows from the claim. How to get
8 there is not specified in the claim. What I list
9 is the most likely way, but it's not the only way.
10 Q. It doesn't say the most likely way in
11 your declaration. It says "necessarily follows,"
12 correct?
13 A. It says that "the sequence of the
14 disclosed layers necessarily follows from
15 Claim 54." I still stand by that.
16 Q. Now, if you look at Fig. 4A -- and I
17 just want to check to see if I've got a blowup of
18 that. If not, we'll just use the one that's in
19 '204. I'll just use the one that's in the '204
20 patent.
21 So you've got that in front of you?
22 A. Yes, I do.
23 Q. And could you -- Claim 54 refers to a
24 first conductive line?
25 A. Yes, it does.

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1 Q. And if you could write down for me next
 2 to Fig. 4A what you would assume to be the first
 3 conductive line.
 4 MR. SCHLITTER: Objection, form.
 5 THE WITNESS: You want me to label on my
 6 copy of Fig. 4A --
 7 BY MR. GIBSON:
 8 Q. Yes, please.
 9 A. -- where is the first conductive line?
 10 Q. Yes.
 11 A. Would you like me to just add the words
 12 to the label or actually color through the
 13 element?
 14 Q. Oh, just add the words, please. That
 15 will be sufficient.
 16 A. Okay. (Indicating.)
 17 Okay, I've done so.
 18 Q. And then if you could -- Claim 54 also
 19 refers to a first insulating film?
 20 A. It does.
 21 Q. If you could write where that first
 22 insulating film is.
 23 A. (Indicating.)
 24 I've done that.
 25 Q. Thank you.

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1 And Claim 54 also refers to a second
 2 conductive line?
 3 A. Yes.
 4 Q. And if you could write that on Fig. 4A
 5 as well.
 6 A. (Indicating.)
 7 I've done it.
 8 Q. And Claim 54 refers to a second
 9 insulating film?
 10 A. Yes.
 11 Q. And if you could write that down as well
 12 on Fig. 4A.
 13 A. (Indicating.) Yes.
 14 Q. All right. If I could just take a look
 15 at that. Thank you.
 16 So as you've indicated there, the first
 17 conductive line is equivalent to 401 auxiliary
 18 lines?
 19 A. I can't agree that it's equivalent, but
 20 I'm pointing to that label in Fig. 4A. The
 21 auxiliary lines correspond to the first conductive
 22 line of Claim 54.
 23 Q. You understand that to satisfy that
 24 claim limitation?
 25 A. I understand that the 401 auxiliary

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1 lines satisfy the first conductive line claim
 2 limitation in Claim 54, yes.
 3 Q. And the first insulating film, you
 4 understand that is the -- corresponds to the 112?
 5 A. The first insulating film of Claim 54
 6 does correspond or is -- that claim limitation is
 7 met by element 112 in Fig. 4A.
 8 Q. And then the second conductive line is
 9 the 403 external connection lines?
 10 A. 403 meets that claim limitation, the
 11 second conductive line.
 12 Q. And the second insulating film is met by
 13 113, the resin inter-layer film?
 14 A. Element 113 corresponds to the second
 15 insulating film in Claim 54.
 16 Q. Okay. Let's look at Shiba again. I
 17 think I've got the patent from the '204 matter.
 18 MR. SCHLITTER: Thank you.
 19 (Document marked previously as Exhibit
 20 Number 1013 was presented.)
 21 BY MR. GIBSON:
 22 Q. And this is one of the patents that you
 23 reviewed in preparing your declaration for this
 24 matter?
 25 A. Yes, it is.

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1 Q. And if you'd look at Fig. 1 of Shiba,
 2 what's your understanding of what's being
 3 disclosed there?
 4 A. Well, Shiba describes Fig. 1 in
 5 Column 3, line 32 as a plan view of an active
 6 matrix LCD panel according to an embodiment of the
 7 present invention.
 8 Q. And there's a wiring 127, is that
 9 correct?
 10 A. I see it. It has at least two labels in
 11 Fig. 1. It's a wiring that begins on the left
 12 side, extends up the left side across the top of
 13 the display and down the right side.
 14 Q. That's the overall length of the
 15 wiring 127?
 16 MR. SCHLITTER: Objection, form.
 17 THE WITNESS: I'm not sure I can
 18 identify a length of wiring 127, but that's where
 19 it is located and illustrated.
 20 BY MR. GIBSON:
 21 Q. And I'm going to ask for a specific
 22 dimension. I mean, if you knew the hypotenuse, I
 23 suppose you could give us precise dimensions for
 24 wire 127 based on that, correct, so it's a right
 25 angle triangle?

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1 MR. SCHLITTER: Objection, form.
2 THE WITNESS: I'm -- can you rephrase
3 your question? I'm not sure what you mean by a
4 "precise dimension."
5 BY MR. GIBSON:
6 Q. If you knew the length of the hypotenuse
7 that runs -- there's a line that runs through the
8 middle diagonally?
9 A. It's a structure that has three sides of
10 a rectangle. So we don't normally think of
11 hypotenuse applying to such a structure, right.
12 It's normally triangles that have hypotenuse. I'm
13 not sure what you're asking.
14 Q. Well, if you knew the dimension of one
15 side of the rectangle, you could figure out the
16 length of the overall wire 127, correct?
17 MR. SCHLITTER: Objection, form.
18 THE WITNESS: You or someone else would
19 need to define what they mean by the length of
20 that wiring. That's a very complicated pattern
21 that's disclosed in other figures of Shiba. And I
22 don't think without more information I can define
23 a length of such a pattern.
24 BY MR. GIBSON:
25 Q. All right. But you do agree the wiring

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1 runs from the bottom left corner up to the top and
2 then across and then down to the bottom right
3 corner?
4 A. That's correct. It's also -- I
5 illustrate that in page 47 of my declaration.
6 Q. You put it in green, I believe. We have
7 a black and white copy right now, but you put that
8 in green?
9 A. That's correct. I'm trying to highlight
10 what Shiba indicates as Fig. 127 (sic) in Fig. 1
11 there as well as in Fig. 3.
12 Q. And what is the purpose of wiring 127?
13 A. Well, Shiba discloses that the purpose
14 of wiring 127 in Column 5 and Column 6 is to
15 connect the power supply pads and the common pads
16 that are around the seal region. They have
17 numbers, for example, 731 to 734 and even 731 to
18 738, and these are all being connected together so
19 that a steady reference voltage is -- would be
20 supplied to those pads so that the counter
21 electrode can have a steady reference voltage. I
22 don't think he uses the word "steady," but that's
23 the idea.
24 Q. And would you also understand that
25 wiring 127 is supplying voltage to power supply

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1 pad 735 and 738?
2 A. Yes, 735 and 738 are included in the
3 list of pads in Column 5 and 6 that wiring 127
4 connects to.
5 Q. Now, the wiring 127, would you agree
6 that the distance it's traveling along the three
7 sides of the rectangle is longer than the diagonal
8 of the display?
9 MR. SCHLITTER: Objection, form.
10 THE WITNESS: Can you define what you
11 mean by the distance of the wiring 127?
12 BY MR. GIBSON:
13 Q. Well, if we just take a -- the distance
14 from the three sides of the triangle, if we
15 traverse those three sides, it would be longer to
16 take that traverse than it would be to traverse
17 the diagonal of the display?
18 A. I still have trouble applying your
19 question to element 127 because it's a complicated
20 pattern. But if you're asking me would the three
21 sides of this rectangle be longer than the
22 diagonal dimension, then yes, it would. I think
23 that would always be true.
24 Q. What's the impact of the resistance of
25 the 127 wiring on the ability to support frame

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1 inversion?
2 MR. SCHLITTER: Objection, form.
3 THE WITNESS: What do you mean by --
4 MR. SCHLITTER: Foundation.
5 THE WITNESS: What do you mean by "frame
6 inversion"?
7 BY MR. GIBSON:
8 Q. Is that a term that you've heard?
9 A. It certainly is.
10 Q. Okay. How would one of ordinary skill
11 in the art interpret frame inversion?
12 A. Well, frame inversion is what generally
13 is designed into displays because the DC bias, the
14 average voltage that appears in liquid crystal
15 layer, needs to be zero. In other words, it needs
16 to be unbiased on average.
17 And what that means is that then the
18 voltages on either side of the pixel need to flip
19 or invert periodically and quite often, that's
20 every frame or every other frame kind of a thing.
21 So that's typically what is -- what is done in
22 frame inversion.
23 Q. And does the wiring 127 have any impact
24 on frame inversion in Shiba?
25 A. It's provided to support not only frame

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1 inversion itself, but the connection to the
2 counter electrode.
3 Q. And how does it -- how does it -- how
4 does it perform that function?
5 A. Well, Shiba discloses that this wiring
6 spread out along the sides of the display and
7 connected to those eight pads, as well as the
8 multiple terminal connections to that conductive
9 pattern, all go to support an even voltage that
10 can be supplied to that. So that in general, the
11 -- the time it takes to reach the desired voltage
12 on that counter electrode would be small. I mean,
13 I don't think he uses that language, but that's
14 certainly my -- my recognition of what he's
15 talking about.
16 Q. Now, if 127 has high resistance, can it
17 support frame inversion?
18 A. Well, it doesn't appear to me that
19 line 127 has high resistance. It seems to me that
20 that's the express purpose of why he split it up
21 into multiple lines and he didn't use just one
22 small line, for example, among other reasons as
23 far as why he split it up. So I don't think Shiba
24 recognizes that that's a problem for him.
25 Q. Right. But the wiring of -- having low

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1 resistance in wiring 127 is important so that you
2 can support frame inversion, correct?
3 A. Having a resistance that's low enough to
4 support the design is important.
5 Q. And wiring 127 is critical to that?
6 A. It's important to that, but that doesn't
7 necessarily mean that it needs to be as low as one
8 can imagine. It needs to be good enough.
9 Q. Right. It needs to be low enough so
10 that you don't have a signal distortion because if
11 you have a signal distortion, your image quality
12 will be low?
13 MR. SCHLITTER: Objection, form,
14 foundation.
15 THE WITNESS: I think that's a fair
16 description of the disclosure in Shiba, yeah.
17 BY MR. GIBSON:
18 Q. Now, in Shiba, would you agree that the
19 first wiring line 27 can be formed in the same
20 step of forming the data lines Xi?
21 A. I notice that Shiba in Column 6 and in
22 the figures of Shiba does disclose exactly that,
23 that the wiring lines 127 are formed in the same
24 step of deposition and patterning as the data
25 lines X, Xi.

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1 Q. And would you also understand they can
2 be formed in the same step of forming the scanning
3 lines Yj?
4 A. While Shiba doesn't illustrate that, it
5 is in Column 6, around lines 33 where it says
6 depending on the kind of TFTs, the aforementioned
7 wiring lines can be formed in the same step of
8 forming the scanning lines Yj. So that's an
9 alternate embodiment that he identifies.
10 Q. And then it goes on to explain that
11 the wiring lines 127 may also be formed in the
12 step of forming the scanning lines Yj and the data
13 lines Xi respectively, thereby constituting a
14 two-layer structure.
15 Do you see that?
16 A. Yes, I certainly do. After all, this
17 comes after his paragraph where he says they can
18 be used -- it could be formed from the data line,
19 alternatively it can be formed in the scan lines,
20 and here he says it can be formed from both to
21 make a two-layered structure.
22 Q. So would you agree that Shiba discloses
23 a single layer wiring, that is, a wiring that's
24 formed from the same material as data lines Xi as
25 one possible option?

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1 MR. SCHLITTER: Objection, form.
2 THE WITNESS: Can you tell me what you
3 mean by a "single layer wiring."
4 BY MR. GIBSON:
5 Q. I think we talked a little bit about --
6 yesterday that these wiring lines can be put down
7 as a layer. That's what I was referring to.
8 A. I think then that Shiba does illustrate
9 and talk about how wiring 127 is formed from a
10 single deposition step and a single layer of a
11 conductor that is the same as the data lines.
12 Q. And it also can be the same as the
13 scanning lines Yj, that's also disclosed by Shiba?
14 A. It could also be a single layer wire
15 using your definition formed instead by the data
16 lines -- I'm sorry, the scanning lines Y.
17 Q. The scanning lines?
18 A. Yes.
19 Q. And then it also goes on to disclose a
20 double layer wiring where one layer is made from
21 the material the data lines, Xi and one layer is
22 made from the material the scanning lines, Yj?
23 A. Is that a question?
24 Q. Yes.
25 A. I missed the question, I'm sorry. I'm

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1 not trying to be difficult.
2 Q. I appreciate that.
3 Would you agree that Shiba also
4 discloses that you can have a double layer wiring
5 where one layer is made from the material of the
6 data line, Xi and one layer is made from the
7 material the scanning lines, Yj?
8 A. Well, the only disclosure in Shiba is in
9 Column 6 where he mentions in one sentence that
10 wiring line 127 may be formed from the scanning
11 lines and the data lines and he refers to it as a
12 two-layer structure. Clearly that's the sentence
13 that's disclosed in Shiba.
14 And to me, it's very unclear what a
15 person of ordinary skill -- well, I -- I think it
16 would be very unclear to a person of ordinary
17 skill what Shiba is disclosing there. Whatever it
18 is, it should be a two-layer structure. That's
19 maybe the only thing that's clear.
20 Q. Well, it's saying more than it's a
21 two-layer structure. It's also saying it may be
22 formed in the step of forming the scanning lines
23 and the data lines?
24 A. He's saying that a two-layer wiring line
25 may be formed from those two metals, to accomplish

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1 the purpose of wiring line 127 in two layers.
2 Q. And then it goes on to say that the
3 layers may then be partially connected to each
4 other, correct?
5 A. That's what the next sentence mentions,
6 yes.
7 Q. And so we have a -- we have two options
8 here. We have a single layer option or a double
9 layer option that's being discussed in Shiba in
10 this column?
11 A. Well, in this column there's at least
12 three options. There's a single layer formed from
13 the data lines, a single layer formed from the
14 scanning lines and then he mentions that some
15 combination of the two is a third option, but he
16 certainly doesn't describe what that third option
17 involves. And there's I think likely many
18 implementations that would meet the two sentences
19 that he's described here.
20 Q. The two-layered structure, there would
21 be a number of ways to implement that?
22 A. Yes, there certainly would be. I can
23 think of at least three.
24 Q. And of a -- between a -- well, why don't
25 you tell me, what are those three that you can

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1 think of?
2 A. Well, I think one of ordinary skill
3 would not know what Shiba is talking about in
4 these two sentences and it would be -- it would be
5 hard to implement anything in this direction. But
6 I'll speculate on some things that would meet what
7 he's describing if you'd like.
8 Q. Go ahead. Let me know what your best
9 thoughts are on this.
10 A. So I think one of ordinary skill would
11 read two-layered structure to implement a wiring
12 line as, first of all, meaning that it would be
13 two conductors on top of each other forming a
14 two-layered structure accomplishing a wiring line.
15 And that's -- that's -- there are
16 examples in our prior art in this case where
17 that's exactly the kind of wiring that's either
18 talked about in the specifications or illustrated
19 in the figures in the other patents here. So I
20 think that's, first of all, what a person of
21 ordinary skill would turn to.
22 But nevertheless, there's still many
23 different ways to implement that wiring line 127
24 where you still have only partial connection. One
25 example is if we turn to Fig. 1, one could imagine

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1 keeping the wiring 127 as disclosed, but then in
2 addition, extending from the right side the scan
3 lines, for example, element 724, a single terminal
4 or perhaps more that would extend from the
5 terminal region under the sealant and over into
6 the portion that is 127, so that it could then
7 thereby connect to it and provide additional
8 support for that reference voltage. So that's one
9 example.
10 Q. And what were the other two that you had
11 thought of?
12 A. Well, another way to imagine it would
13 be -- well, another -- another implementation
14 would be for some part of the wiring line inside
15 the existing 127 without an additional terminal
16 connection to the scanning lines, as I mentioned
17 in my first example, but for some parts of that to
18 have the additional metal deposited right on top.
19 So for example, one could have -- all
20 along the upper line, have both the material from
21 the data lines and the scanning lines deposited on
22 top of one another to form that connection.
23 A third example, of course, is that
24 wiring line 127 has six sublimes in it and
25 certainly one can imagine that you could divide

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1 those up between those patterning steps and
2 connect them at the start, at the end or perhaps
3 even somewhere in the middle and then form a
4 structure that meets what he seems to be
5 suggesting may be possible in those two sentences.
6 Q. Now, in terms of a single layer
7 structure that's mentioned earlier and then the
8 double-layer structures, some examples of which
9 you provided, which would have a lower resistance?
10 A. Compared to what?
11 Q. The single layer that's mentioned
12 compared to the double-layer structure.
13 A. You'd have to define from where to where
14 you want me to comment on where the resistance
15 would be.
16 Q. In line 127 where line 127 traverses the
17 rectangle or the three sides of the rectangle?
18 A. Well, in all my cases I've -- I've
19 identified only local regions where the two wires
20 are on top of one another. If you'd like me to
21 limit my answer to that I, of course, can.
22 Q. Why don't we start with that?
23 A. Maybe the easiest thing to do is take my
24 second example and just use that. And in that
25 second example, I'm commenting that wiring

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1 line 7 could have --
2 Q. 127?
3 A. I'm sorry, yes, 127 could have the data
4 line layer as disclosed and then have the scanning
5 line metal applied, for example, across the top
6 border of the wiring line 127 only. And so if you
7 want me to comment would the resistance be lowered
8 from the top left of wiring 127 to the right, yes,
9 it would be.
10 Q. And why would it be lowered?
11 A. Well, it would be lowered because there
12 is more conductor. It would be a thicker wiring
13 than it would be if it was a single wiring. But
14 in no case that I've offered here is it a
15 three-layered structure with insulating material
16 in between. I think that's the one thing that's
17 excluded from his description.
18 Q. Where does he exclude that?
19 A. Well, he calls it a two-layered
20 structure. A person of ordinary skill would never
21 look at a conductor and then an insulator and then
22 another conductor and understand that to be a
23 two-layered structure.
24 Q. Doesn't he talk about it being partially
25 connected?

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1 A. He does. But he calls it a two-layered
2 structure.
3 Q. Wouldn't one of ordinary skill in the
4 art understand that if it's going to be partially
5 connected, that it's not going to be lying on top
6 of each other?
7 A. What do you mean by "lying on top of
8 each other"?
9 Q. In direct contact.
10 A. Certainly not. A person of ordinary
11 skill would -- would -- cannot come to that
12 conclusion based on both sentences that he's
13 provided here. It's a two-layered structure.
14 It's not a three-layered structure.
15 Q. If it's -- if it's only a two-layered
16 structure, how would it be -- how would that
17 structure not be in contact all the time?
18 A. Well, first, let me note that Shiba
19 doesn't require that it's only partially
20 connected. It says if the layers are partially
21 connected, right. That's just one possible
22 implementation of his sentence about the
23 two-layered structure.
24 But nevertheless, in the case that I've
25 offered in my first example where there's a

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1 connection that comes from the terminal on the
2 right side of the display in Fig. 1, for example,
3 coming from the connections in element 724, then
4 there would be a wiring that starts at the
5 terminal that is not overlapping 127. It goes
6 into the sealant. It's still not overlapping
7 until it reaches 127. So it would be partially
8 overlapping.
9 My third example also would apply to
10 that where it's only connected at the ends, the
11 beginning and the end of the conductors where
12 those six lines are divided up, let's say three to
13 the data lines and three to the scanning lines.
14 Q. All right. But you're reading
15 two-layered structure to exclude an insulating
16 film, correct?
17 A. I'm commenting that Shiba calls it a
18 two-layered structure and it's my opinion that a
19 person of ordinary skill would listen to him and
20 understand it must have two layers and only two
21 layers and thereby, would not have an insulating
22 film in between the two conductive films.
23 Q. You would agree at the time that Shiba
24 is doing this, it was well-known in the art to
25 have two wiring layers that were separated by an

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1 insulating film?
2 A. I think it was well-known in the art by
3 1997 to have a multi-layer wiring that was very
4 standard. Almost all the patents here talk about
5 that. And certainly it's well-known that
6 connecting through an opening in an insulator was
7 well-known in 1997 to connect two insulators.
8 But nowhere except in the '204 or '413
9 patent is the structure of two conductors with an
10 insulating film in between where their electrical
11 contact is because of -- or through the openings
12 in that insulating layer is that being used to
13 provide some benefit in the -- in the direction of
14 the wiring. It is in Sukegawa, but that's --
15 that's just for the terminal region.
16 Q. Now, you would agree that when -- a
17 person of ordinary skill in the art, they bring
18 the knowledge that they have to these patents,
19 correct?
20 A. Certainly they do.
21 Q. And they're presumed to know not just
22 about the one patent, but about the other patents
23 as well in the field, correct?
24 A. Yes.
25 Q. So you can't look at the patent just by

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1 itself. You are, as one of ordinary skill in the
2 art, permitted to look at the patents together in
3 the field?
4 A. Certainly a person of ordinary skill is
5 permitted to do that.
6 Q. Let's look at Fig. 6 of Shiba.
7 A. I've got it. Oh, I notice that the
8 sealant is in direct contact with a transparent
9 conductive layer in Fig. 6. I couldn't -- I
10 couldn't find the example yesterday in our
11 discussion, but here it is.
12 Q. I'll move to strike as nonresponsive. I
13 hadn't asked a question.
14 Now, if we look at -- if we look at this
15 figure, you see wiring 127?
16 A. I do see wiring 127 on the bottom right
17 of the figure.
18 Q. And is it a single layer or a double
19 layer in that figure?
20 A. In that figure, it is -- it's a
21 cross-section, of course, of something that Shiba
22 calls a first wiring 127 and it's formed from a
23 single layer of deposition and patterning. So I
24 think it's appropriate to call it a single layer
25 wiring. But let's keep in mind it has multiple

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1 parallel lines to it.
2 Q. And is this the data line Xi or the
3 scanning line Yj?
4 A. What's illustrated in Fig. 6 is that the
5 metal in 127 was deposited with -- from the data
6 line Xj or in that step. That's all he discloses
7 in any of his figures.
8 Q. Is the Yj?
9 A. It's the Xj.
10 Q. I'm sorry, it's the Xi.
11 A. Xi.
12 Q. That's what confused me.
13 A. I'm sorry.
14 Q. So it's the Xi. Okay. And that is the
15 data line layer, correct?
16 A. Xi would be the data line metal, yes, or
17 the metal that's deposited along with the data
18 lines is probably the most precise way to say it.
19 Q. All right. And what is the layer below
20 127?
21 A. The layer below 127 is element 211 and
22 that is called the gate dielectric.
23 Q. And is that an insulating layer?
24 A. That is an insulating material.
25 Q. Is there any other layer shown below

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1 211?
2 A. The next layer that's illustrated is
3 element 200 which is the substrate. It's called
4 the array substrate.
5 Q. You wouldn't consider that to be a layer
6 in the sense that we've been using the word
7 "layer" to talk about wiring layers and insulating
8 layers?
9 A. It depends.
10 Q. In terms of Shiba, is the substrate
11 something you would understand to be a layer?
12 A. It depends.
13 Q. Well, in Fig. 6, would you consider the
14 substrate to be a layer?
15 A. I know I'm repeating myself, but it
16 depends.
17 Q. So it might be; it might not?
18 A. I can't say either way at the moment.
19 It depends on the context.
20 Q. Looking at Fig. 6, would you consider
21 that to be the glass substrate 200?
22 A. Shiba calls it the array substrate and I
23 think it's -- it would be commonly some kind of
24 glass. I'm not sure Shiba requires it to be so.
25 I could study to find out.

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1 Q. That's fine. Would you consider the
 2 gate dielectric layer 211 to be a first insulating
 3 film?
 4 A. What do you mean by "first insulating
 5 film"?
 6 Q. Is it the first insulating film that's
 7 on the substrate?
 8 A. Well, it is the first layer that's
 9 illustrated in Fig. 6 in Shiba, but it is
 10 certainly not the first insulating film of the
 11 claims in the '204 patent, specifically Claim 54.
 12 It cannot be because it doesn't have a first
 13 conductor underneath it.
 14 Q. Putting that aside, is it -- in Fig. 6,
 15 is there any insulating film that comes before the
 16 gate dielectric?
 17 A. When you say "before," what do you mean?
 18 Q. There's nothing in between 211 and 200,
 19 right?
 20 A. Elements 211 and 200 are illustrated in
 21 Fig. 6, as least in the region of 127, as being in
 22 direct contact, yes.
 23 Q. What's the layer that's above 127?
 24 A. There are many layers above 127. The
 25 first --

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1 Q. I meant the first layer. Let's talk
 2 about that.
 3 A. Well, for -- of course 127 has six
 4 sublines and the -- all of them have element 241
 5 on top of it and then beyond that, it varies. But
 6 let me see what 241 is called.
 7 Q. And what is 241?
 8 A. It's the protective overcoat which is
 9 identified as being, in one example, formed as
 10 silicon nitride. So that's an insulating
 11 material.
 12 Q. So you would agree that 241 is an
 13 insulating layer?
 14 A. I can agree that it's an insulating
 15 layer, yeah.
 16 Q. And it's coming after the gate
 17 dielectric insulating layer?
 18 A. By "after," do you mean it's been
 19 deposited in time after it?
 20 Q. Yes.
 21 A. I think that's -- that's the disclosure
 22 in Shiba that it would be formed in sequence after
 23 the other insulating layer. Of course there would
 24 be other steps in between.
 25 Q. What is the element 113?

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1 A. Element 113 is called the sealing agent.
 2 Q. And layer 241 runs under element 113?
 3 A. Are you asking me if that's the case in
 4 Fig. 6?
 5 Q. Yes.
 6 A. In Fig. 6, it is illustrated that layer
 7 241 is -- is at least partially below the element
 8 113. Of course, element 241 extends far beyond
 9 that.
 10 Q. Now, if the wiring 127 is formed in the
 11 same step of forming the scan lines Yj, how will
 12 they appear in Fig. 6?
 13 A. I don't think there's one answer to
 14 that.
 15 Q. Can you give me the different answers to
 16 that then?
 17 A. Well, I guess the most -- because of
 18 course Shiba doesn't say, right. Shiba has one
 19 sentence that says it can be done and doesn't
 20 describe how. So it's not really fully disclosed
 21 to one of ordinary skill.
 22 One example I can think of is that it
 23 would be the same structure, those six lines, at
 24 least in Fig. 6, and those would be -- those would
 25 appear between element 200 and element 211 instead

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1 of being as illustrated being between 211 and 241.
 2 Q. Any other examples that you can think
 3 of?
 4 A. Well, in this cross-section, I mean, of
 5 course this is not all of the wiring line 127.
 6 There's also the rest of the display. And so
 7 there certainly are many variations on -- on how
 8 to do that. I can't -- I don't think I could
 9 enumerate all the other possibilities. It's just
 10 not -- not clear what else could be done for a
 11 person of ordinary skill to implement this idea.
 12 MR. GIBSON: If we could marked this as
 13 1008.
 14 (Document marked as Exhibit Number 1008
 15 for identification.)
 16 BY MR. GIBSON:
 17 Q. And Exhibit 1008 is titled "Late-News
 18 Paper: Polarization Independent Liquid Crystal
 19 Microdisplays" and it lists you among others as
 20 one of the authors.
 21 Do you see that?
 22 A. Yes, I do.
 23 Q. And I think yesterday when we were
 24 talking, you mentioned a particular paper and I
 25 was curious if this is that paper?

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1 A. We talked about many papers. I agree
2 that I did identify this paper in answer to one of
3 your questions. I don't remember specifically
4 which one.
5 Q. Okay. I think it had to do with work on
6 TFTs. Do you recall that?
7 A. There was an extensive discussion on my
8 work with TFTs and again, I don't remember the
9 specific question for which I identified this, but
10 this is -- this is one of the papers I identified
11 yesterday, yes.
12 Q. I think you identified this as the
13 single paper where you dealt with active matrix
14 displays and peripheral driving circuits on the
15 same substrate.
16 Do you recall that?
17 A. Well, I don't recall exactly what I
18 said. What I do -- what is true is that this is
19 the -- maybe the only publication that came out of
20 my work involving TFTs and active matrix
21 substrates, but my work, as I said, was largely
22 unpublished. This is the only thing that did come
23 out that I can point to and share with you.
24 Q. And would you agree that this paper
25 deals with a 256-by-256 pixel silicon backplane?

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1 A. Yes, exactly. This was one of the many
2 implementations that we were working on.
3 Q. And what is the substrate for the
4 silicon backplane?
5 A. What do you mean? What is the material?
6 Q. Yes.
7 A. Well, you said it, it's crystalline
8 silicon.
9 Q. And the -- what's the substrate itself?
10 A. There are two substrates. The upper one
11 is glass and ITO. The lower one is predominantly
12 silicon but, of course, it has other layers on it,
13 aluminum and other wirings and various dopings to
14 achieve the transistors that are in the
15 microdisplay.
16 Q. Would you -- would you call that a
17 silicon wafer?
18 A. Well, it's a piece that came from a
19 silicon wafer, yes.
20 Q. Does the silicon backplane use a single
21 crystal silicon wafer?
22 MR. SCHLITTER: Objection, form.
23 THE WITNESS: I think I'm not sure what
24 you mean by a "silicon" -- I'm sorry, what you
25 mean by a "single crystal silicon wafer"? It is

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1 crystalline silicon.
2 BY MR. GIBSON:
3 Q. Would you agree that the backplane is
4 not made on a glass substrate?
5 A. In this example, it is -- it is not a
6 glass substrate, that's right.
7 Q. And would you agree that the transistors
8 used in this silicon backplane are conventional
9 single crystal silicon transistors?
10 A. In this example, that's correct. In my
11 other work that's unpublished, that's -- that's
12 not correct.
13 Q. Okay. But I'm talking about this
14 particular piece.
15 So you would agree that the transistors
16 that are used here are conventional single crystal
17 silicon transistors?
18 A. Well, they're actually unconventional
19 because they have high voltage aspects to them,
20 but they are more standard. They're -- they're
21 not thin film transistors on glass.
22 Q. Would a person of ordinary skill in the
23 art characterize a single crystal silicon
24 transistor as a TFT?
25 A. Not in general.

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1 Q. Who fabricated this silicon backplane
2 that's discussed in the article?
3 A. Well, this is -- this particular
4 backplane came in a partnership with a third
5 company that's identified here. It's Boulder
6 Nonlinear Systems and it's a modification of one
7 of their standard silicon backplanes that we used
8 and we were able to then publish on.
9 But by no means was this project limited
10 to just this prototype or what we described here.
11 We had others where we -- we designed and at least
12 initially fabricated our own on -- on silicon as
13 well as other TFTs that were on glass to form the
14 active matrix.
15 Q. Now, did -- but this particular -- you
16 know, and I'll move to strike the last part as
17 nonresponsive.
18 The particular part here on -- dealing
19 with what's in this particular article, I think
20 you agree that this was made and provided by
21 Boulder Nonlinear Systems?
22 A. It was a modification of one of their
23 standard products for our project and then so we
24 modified it along with them. It's -- it's not a
25 standard thing from them.

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1 Q. Do you know if Boulder Nonlinear Systems
2 provides commercial products such as reflective
3 liquid crystal spacial light modulators?
4 A. That is one kind of product that they --
5 they provide and those can be used in various
6 applications, including near-to-eye displays.
7 Q. I think you mentioned that you, in some
8 of your recent projects, were looking at gallium
9 nitride TFTs?
10 A. Yes.
11 Q. Are those really for LEDs as opposed to
12 TFTs?
13 A. No, they're not.
14 Q. Is gallium nitride a material that's
15 used for fabricating LEDs?
16 A. Yes, it's a -- it's a very common and
17 very important one for green and blue.
18 Q. I think there's some acknowledgments and
19 there's a reference in your article to another
20 company called Goldeneye, Inc.?
21 A. In addition to the reference to Boulder
22 Nonlinear Systems, there's an additional company
23 Goldeneye, Incorporated, yes.
24 Q. And what were the contributions of that
25 company?

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1 A. Well, you can see in the Fig. 4 right
2 above the acknowledgments that the LEDs, the light
3 source for this projector, came -- or at least
4 part of it came from the company Goldeneye. They
5 had a -- at the time -- this is not recent, right.
6 This is 2007, 2008. They had a technology that
7 would produce light with LEDs and then collimate
8 it in an advantageous way and we were taking
9 advantage of that.
10 Q. And how were you taking advantage of
11 that?
12 A. Well, our display approach would --
13 needs light -- in this case, in this project
14 needed light that would be produced by LEDs but
15 also fairly well collimated efficiently. That was
16 important to us. So their technique would provide
17 that collimation for us, right. It's the light
18 source for our projector.
19 Q. And do you know if Goldeneye is involved
20 in making gallium nitride LEDs?
21 A. I don't recall to what extent they're
22 actual fabricating their own LEDs or to what
23 extent they're getting the dye from those that do
24 fabricate it.
25 Q. And do you have processing capabilities

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1 in your company to fabricate TFT-based backplanes?
2 A. No, our company is in many ways a
3 virtual company. We don't have independent office
4 or lab space in the company. We have multiple
5 employees, but we leverage the university and all
6 its resources for the physical aspects of our
7 research and product development that we do here
8 domestically and then we produce commercial
9 products with our Japanese manufacturing partner.
10 Q. And who is that?
11 A. I'm not sure I can say. I'm sorry.
12 Yeah, I mean, I'd like to tell you, but I'm just
13 not sure if it's public information.
14 Q. I understand.
15 All right. Why don't we take a break?
16 VIDEOGRAPHER: We're going off record.
17 This is the end of Media Unit Number 1. The time
18 is 11:10.
19 (Short recess.)
20 VIDEOGRAPHER: We're back on record.
21 This is the beginning of Media Unit Number 2 in
22 the deposition of Dr. Michael Escuti. The time is
23 11:26. Please continue.
24 BY MR. GIBSON:
25 Q. All right. If you could look at page 52

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1 of your declaration in the '204 matter.
2 A. I've got it.
3 Q. And these are the two figures we
4 discussed some yesterday.
5 What was your purpose of putting this
6 into the declaration in the '204 matter?
7 A. Well, the paragraphs explaining it
8 certainly contain my purpose. I can certainly
9 summarize that if you'd like.
10 Q. So you're saying paragraph 107 sets
11 forth your purpose?
12 A. It's certainly not limited to 107. It's
13 this -- I think there are -- there are subsequent
14 paragraphs that have to go carefully to notice
15 which paragraphs it ends on, but it certainly
16 includes 108, 109, 110 and 111. It's the whole
17 section discussing those figures.
18 Q. Okay. When you look at these two
19 figures, would you agree that there's a protective
20 overcoat and then an opening formed over it, or
21 formed into it might be a better way to say that?
22 A. That would be a better way to say it.
23 Both figures have a protective overcoat 241 and
24 there is an opening that is illustrated as being
25 somehow created within that overcoat.

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1 Q. Now, can a person of ordinary skill in
2 the art form the opening in the overcoat before
3 forming the overcoat itself?
4 A. Using standard semiconductor processing,
5 no, but there are exotic ways to do such a thing.
6 Q. Okay. But they would not be standard?
7 A. In my experience, they would not be
8 standard.
9 Q. Okay. So if we call the formation --
10 A. Well, let me make one more comment.
11 There is -- I'm sorry. Can I clarify my answer?
12 Q. You weren't finished, so go ahead.
13 A. Okay. In the sense that it is -- it is
14 most common to create openings in an insulating
15 layer in the way that we've been talking about,
16 that first the layer is deposited and then it's
17 etched away -- some opening is etched away, but
18 there's also a common technique which I don't
19 think is uncommon and this involves using a mask
20 to cover up a portion of the substrate that you're
21 depositing your elements on.
22 And in that case what's happening is, of
23 course, only part of the substrate is getting the
24 deposition of the insulating layer and then
25 another part is not and so that's another way to

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1 create openings in an insulating layer.
2 Q. All right. When you're looking at the
3 '204 patent as a person of ordinary skill in the
4 art, would you understand that the insulating
5 layer is being deposited and then it's being
6 etched to create the openings?
7 A. I'm -- I'm not sure that the '204 patent
8 requires that. It's certainly a preferred
9 example, but I don't recall if the '204 patent
10 requires that.
11 Q. Okay. When you look at the '204 patent,
12 you understand that's certainly one way it would
13 be done, correct?
14 A. Yes.
15 Q. And you would consider for an insulating
16 layer, that to be the more standard way it would
17 be done rather than using a mask?
18 A. I consider both of those techniques as
19 standard. Which one is used depends on where the
20 opening is being made and how big the opening is.
21 So I'm not sure I can point to one or the other as
22 being more standard.
23 Q. When you're talking about using a mask,
24 you're not forming an opening at that point, you
25 are depositing around a mask and then you're not

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1 actually opening up the insulation layer?
2 MR. SCHLITZER: Objection, form.
3 THE WITNESS: In that example, and to be
4 more specific applied here, if the mask was
5 literally some -- some kind of metal perhaps or
6 other substrate that's placed on the edge of a
7 display, it's another object that's being brought
8 down to the glass substrate during the deposition
9 process, then it would cover up that region during
10 that process.
11 And then when the deposition is done, it
12 can be, of course, removed and there would be an
13 opening in that insulating layer that would appear
14 because the material was never deposited there to
15 begin with.
16 BY MR. GIBSON:
17 Q. Okay. But let's assume that you have
18 deposited an insulation layer, no mask and then
19 you've created openings.
20 A. I can assume that, yeah.
21 Q. And let's call that formation of the
22 protective overcoat as step A.
23 A. I can assume that.
24 Q. And then forming the opening in the
25 protective overcoat is step B.

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1 A. Okay.
2 Q. And when I'm talking about forming the
3 opening, I'm talking about all the steps necessary
4 to accomplish that as step B.
5 Are you with me?
6 A. I think so.
7 Q. What is the sequence of those two steps?
8 A. It sounds to me that it's in your
9 assumption, right. You said first let's assume
10 that layer A is deposited wholly on the substrate
11 without any kind of mask, whether it's an external
12 mask or resist that's down there. And then after
13 that, the next step is to form the opening by, for
14 example, etching.
15 Q. So it's step A, then step B?
16 A. In your assumption, it is.
17 Q. Now, looking at the two figures you have
18 in your -- in your declaration, do you recognize
19 that there's another layer, the ITO layer?
20 A. There's several other layers. We talked
21 about them all yesterday, but both figures have an
22 ITO layer that's illustrated.
23 Q. Okay. So in my example, let's now call
24 the -- forming the ITO layer as step C.
25 A. If you'd like to assume that, that's

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1 fine.
2 Q. And can you list the different ways a
3 person of ordinary skill in the art would
4 recognize that exists for performing these three
5 steps --
6 MR. SCHLITTER: Objection, foundation.
7 BY MR. GIBSON:
8 Q. -- in sequence?
9 A. I'm not sure I understand your question.
10 Fig. C shows the example of first ITO A is
11 deposited and patterned on whatever is below it.
12 Then the protective overcoat 241 is applied and
13 then lastly, the opening is created and that's
14 consistent with what's in Sukegawa.
15 Alternatively is what's in D, where the
16 insulating film is applied first, then the opening
17 and then the ITO's deposited and patterned.
18 Q. Okay. But if we have these as three
19 steps, an A, a B and a C, one obvious sequence you
20 could do is A and then B and then C.
21 Are there any other ways to reorder
22 those three steps?
23 A. Well, the A, B, C order in your
24 assumptions -- or assuming the way you've defined
25 those steps would correspond to Fig. D at least

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1 partially. And Fig. C, however, would be
2 different. It would be C, A, B, consistent with
3 Sukegawa, not just my Fig. C.
4 Q. Right. So Fig. C, the steps would be
5 step C, then step A, then step B?
6 A. Yes.
7 Q. And then Fig. D, the steps would be A,
8 B, C?
9 A. Yes, but let's also keep in mind that
10 there could be other steps in between and
11 sometimes there has to be.
12 Q. I'm just talking about what we see in
13 these two figures.
14 It would be A, B, C for Fig. D?
15 A. Sure. I'll say it again. Fig. D would
16 correspond to A, B, C.
17 Q. Are there any other ways to -- in terms
18 of the sequence that we've been discussing, the
19 steps A, B and C, are there any other ways to do
20 it or are these the only two?
21 A. You're asking me is there any other
22 ordering of three things?
23 Q. No, not any three things, these
24 particular three things.
25 A. Well, in your assumptions you've said

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1 that A is a layer that's explicitly put down
2 everywhere, right, not masked, there's no lift off
3 resist. And step B is the -- whatever process is
4 used to create an opening in the layer that's put
5 down in step A. So those two have to go in that
6 sequence, A and then B in processing by your
7 assumptions. And so then the only two possible
8 variations would be for C to either be before A
9 and B or after.
10 Q. Do you still have Shiba around?
11 A. Yes, I have Shiba.
12 Q. If you look at Fig. 4?
13 A. I see it.
14 Q. Do you see the ITO layer?
15 A. I see two ITO layers.
16 Q. And what numbers are those?
17 A. Let me make sure. Layer 251 and 741.
18 Q. And where are you getting 741 from, what
19 part of the specification?
20 A. I'm not very good at word searching.
21 Electronically it's much easier.
22 Q. I mean, there may be other references,
23 but I was looking at Column 5, line -- it's
24 probably around line 38. I think it refers to --
25 Shiba refers to 741 as a connecting projection?

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1 A. Yes, yes, exactly. So these are --
2 thank you for finding it. This is the contact on
3 the counter substrate which extends out. It's the
4 portion of the ITO layer 7 -- I'm sorry -- the
5 counter substrate electrode which is explicitly
6 ITO, counter electrode 541, and the projection to
7 the left side is the portion -- I understand that
8 to be the portion of the electrode on the other
9 substrate that gets the contact from those pads
10 that are all around Shiba's display.
11 Q. Where does it call 741 a transparent
12 conductive layer or an ITO?
13 A. I don't think it's -- it's text -- that
14 text is in the specification, but that's what
15 Fig. 4 shows. The layer 741 goes from the left
16 side and all the way to the right. And on the
17 right side, I see 541, which is the pixel
18 electrode and pixel electrode 541 appears both in
19 Fig. 4, Fig. 6 and 541 is -- I think it's clear
20 from the specification that that is ITO.
21 Q. Well, and why would the patent call 741
22 something different than 541 if it was the same
23 thing?
24 A. That's a good question. If you look at
25 Fig. 3, there's a single conductive layer that has

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1 many of its regions identified with different
2 numbers. For example, the wiring 127, the pad
3 125, 731, the line 121, the terminal 751, that's
4 all formed from the same deposition step and then
5 these element numbers correspond to different
6 pieces of it. So it's very consistent with Shiba
7 to do that.

8 Q. Well, there you're talking about an
9 actual different -- different structures in
10 Fig. 3, right?

11 A. Well, yes, but 741 is a different
12 structure. We can see it in Fig. 1. So Fig. 1
13 has 741 on the bottom left. Of course Fig. 3 is
14 the expanded view of the box labeled A in Fig. 1
15 and we see 741 identified there and we can see
16 that there's a dashed line that runs all around
17 the display. That's labeled 541. That's the
18 counter electrode.

19 And then shooting off like peninsulas,
20 there are eight little protrusions of the same
21 dashed line, that same region and those are
22 labeled 741 and other numbers.

23 Q. Those are connectors, right? They're
24 connecting projections is what Shiba calls them?

25 A. They are. That's their function, but

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1 they are -- from Fig. 1 it's clear that it's all
2 the same conducting layer. And while I don't
3 think Shiba requires that it be formed of ITO, it
4 certainly is disclosed as such.

5 Q. Well, there's nothing that says 741 is
6 an ITO layer, correct?

7 A. The specification does not say it, but
8 it is disclosed in Fig. 4 and Fig. 1.

9 Q. Well, we may just have to disagree about
10 that. But the sealant -- I take it, your
11 reference earlier today that the sealant is over
12 the ITO was based on 741 being an ITO?

13 A. It's not. Fig. 6 also shows 541, the
14 ITO counter electrode, as in contact on the
15 counter electrode substrate with the sealant 113.

16 Q. Okay. So let's start with Fig. 4 then
17 we'll move to Fig. 6.

18 So Fig. 4, at the bottom of the figure,
19 you would agree that the sealant is not in contact
20 with the ITO, correct?

21 A. On the bottom substrate, I do agree with
22 that.

23 Q. And on the upper substrate, because you
24 believe 741 is an ITO layer, you would say there's
25 some contact with the sealant?

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1 A. Well, it's not my belief. It's what the
2 disclosure in Shiba shows in Fig. 1. So 741 is
3 part of the layer 541 and there are many other pad
4 protrusions. I mean, this is how the counter
5 electrode gets its potential, by those eight
6 connections.

7 Q. Well, the connection doesn't have to be
8 an ITO layer for there to be a connection, right?

9 A. It doesn't, you're correct, but that's
10 what's shown.

11 Q. As I said, we disagree about that, but
12 I'm just trying to get to what you're basing your
13 view on that the sealant is connecting to an ITO
14 layer and that's based on the connection -- at the
15 top of Fig. 4, there's -- there's some touching of
16 sealant and 741?

17 A. Fig. 6 clearly shows the sealant on the
18 counter substrate touching element 541.

19 Q. What --

20 A. In Fig. 4, it shows -- it shows what
21 might be a combination. It depends on where you
22 define what the boundary between 741 and 541 is.
23 So 541 is the counter electrode that must go
24 through most of the display, it's off to the right
25 side of Fig. 4. And then 741 appears to be this

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1 -- this protrusion which is in direct contact with
2 the glass substrate 501.

3 Q. And are you -- is it your view that
4 sealant is contacting the ITO layer in Fig. 4?
5 Let me -- is it your view that sealant is
6 contacting 741?

7 A. It appears that 741 is connected to both
8 element 115, which is the conductive resin or
9 the -- I think it's called the transfer material
10 made of a conducting resin, as well as a small
11 part of the sealant 113 there.

12 Q. All right. Then Fig. 6, at the bottom
13 of Fig. 6, you would agree that there's no contact
14 between the sealant and the ITO layer?

15 A. In Fig. 6, I agree that the bottom part
16 of the sealant does not contact ITO.

17 Q. And at the top, where do you think
18 there's a touching between the sealant and 541?

19 A. Element 541 is extending from the left
20 side of the figure of Fig. 6 to most of the way
21 across toward the right side, and it is in direct
22 contact with the majority of the sealant that's
23 illustrated in Fig. 6.

24 541 is called the counter electrode.
25 Column 5 has some language about that counter

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1 electrode around lines 24 through 28 or so. It
2 says, "In this case, since the counter electrode
3 541" is -- I'm sorry -- "counter electrode 541
4 made of ITO, has a relatively high resistance" and
5 then it goes on.
6 Q. And then what is 581?
7 A. In the moment I'm not finding the
8 description of 581, but since 581 appears to be
9 immediately adjacent to the liquid crystal layer,
10 then that corresponds to the orientation film. I
11 do suspect somewhere in Shiba he does disclose
12 what that's called, but I'm not finding it.
13 Q. Would that be an insulating layer?
14 A. It is typically formed of a polymer that
15 would be insulating, an insulating material. It's
16 not typically a conductor.
17 Q. And 581, does that also extend from the
18 left to the right in Fig. 6?
19 A. It extends partially, right. You can
20 see that it only stays within the liquid crystal
21 portion and ends immediately before the sealant
22 and there's a good reason for that's because that
23 very thin polymer film is even worse to adhere to
24 than ITO.
25 Q. Now, you would agree with me that the

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1 '204 patent is not directed to the fabrication
2 area that you're talking about where 541 extends
3 into the sealant area?
4 A. I don't think I can agree with the
5 second part of your characterization. Fig. 1
6 shows that element 541 goes all around the display
7 and has -- it's roughly a rectangle with these
8 eight protrusions. And some of those protrusions
9 are in the terminal region, but the two sides of
10 element 541 do correspond to the terminal
11 portions. And while Fig. 1 shows the top-down
12 view all around, Fig. 6 is not corresponding to a
13 terminal portion. But that's just Fig. 6.
14 Q. You would agree that Fig. 6 does not
15 correspond to a terminal portion?
16 A. Yes, I do agree with that. That would
17 be represented in Fig. 4, but also in other
18 regions of Fig. 1.
19 Q. And we're looking at -- you still have
20 the '204 patent so we can look at Claim 54.
21 A. I've got it.
22 Q. In Shiba, the 541 element that you've
23 just described, is that -- does that correspond to
24 anything in Claim 54?
25 A. Claim 54 applies to the active matrix

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1 substrate and the sealant that contacts it. It
2 mentions that there's a counter substrate facing
3 the substrate and, of course, the sealant has to
4 connect to that.
5 But I don't see an explicit electrode
6 mentioned in Claim 54 for that counter electrode,
7 but certainly it's implicit.
8 Q. But it's not something that's described
9 in the claims?
10 A. It is implicit to Claim 54. If you
11 didn't have it, you would -- most LCD modes would
12 not function; not all, but most.
13 Q. So would you say that the claims are
14 limited to that requirement that you're finding
15 implicit?
16 A. Maybe I'm being clumsy with the terms.
17 It's -- it's not explicit in the claim and as
18 such, the claim applies to both of those
19 circumstances.
20 Q. Is the -- you would agree that Claim 54
21 is not directed to the type of structure we see in
22 Fig. 6 with respect where we have element 541
23 connecting with the -- we have element 541 along
24 the top of the substrate?
25 MR. SCHLITTER: Object to form.

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1 THE WITNESS: Do you mean Fig. 6 of
2 Shiba?
3 BY MR. GIBSON:
4 Q. Yes.
5 A. Fig. 6 in Shiba is not a cross-section
6 of the terminal portion, but it's clear from
7 Fig. 1 that element 541 extends all around the
8 display. It necessarily has to and it certainly
9 extends over the terminal portions. So if we took
10 a cross-section there, that's a different matter.
11 Q. No, I'm not talking about that. I'm
12 talking about Fig. 6.
13 And because it's not part of the
14 terminal portion, you would agree that Claim 54 is
15 not directed to the way 541 is being used in
16 Fig. 6?
17 MR. SCHLITTER: Objection, form.
18 THE WITNESS: Fig. 6 in Shiba is not a
19 cross-section of the terminal portion, I agree to
20 that. But Fig. 1 makes it clear that the element
21 541 extends into the terminal portions in Fig. 1.
22 And one example of that is Fig. 4, which does have
23 the terminal portions and which does have the
24 sealant in contact with ITO element 541.
25

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1 BY MR. GIBSON:
2 Q. Well, it's -- you call it 741. Now
3 you -- Fig. 4 shows 741, not 541, correct?
4 A. That's not correct. Fig. 4 shows 741 on
5 the left side. It's the flat part that's in
6 direct contact with the upper substrate. But
7 element 541 is labeled on the right side and it
8 similarly extends across the substrate right above
9 the orientation layer, just like it is in Fig. 6.
10 Q. Where does 541 begin and 740 -- or I'm
11 sorry.
12 Where does 541 end and 741 begin?
13 A. I don't think that's the correct
14 characterization. I think 541 is talked about as
15 being the counter electrode and it includes the
16 entire region that's in the dashed line of Fig. 1.
17 And those protrusions that Shiba identifies as
18 741, 745 -- he's got eight numberings for those --
19 are a subset of the element 541.
20 Q. In Fig. 6, can you just mark in pen on
21 Shiba where you believe there's contact between
22 541 and the sealant?
23 A. On my copy here?
24 Q. Yes, please.
25 A. Anyone have a blue pen?

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1 MR. MANZO: What color do you want?
2 MR. GIBSON: Red is fine. It may stand
3 out more.
4 THE WITNESS: I'm not sure it's possible
5 for me to simply identify the interface of
6 contact, but I can identify where in 541 that the
7 sealant 113 does contact it, which is what I've
8 tried to draw.
9 BY MR. GIBSON:
10 Q. What do you mean by "interface"?
11 A. Well, the interface is a line that on
12 that printout is a black line. That's the surface
13 of contact. I can't simply draw a red line
14 through that, you wouldn't see it. So I'm
15 highlighting the portion of the layer 541 that
16 is -- you know, is the layer above which that
17 contact occurs.
18 Q. And does 541 -- you've got a lower part
19 of 541. Well, let me just hand it back to you.
20 Does 541 end on the left part where it's
21 touching the sealant?
22 A. The material that corresponds to 541
23 ends about more than halfway through the sealant
24 in Fig. 6. In Fig. 4, it's different. It does
25 extend out and protrude and form what Shiba calls

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1 the connecting protrusions -- I'm sorry,
2 connecting projections, 741 to 748.
3 Q. What's the -- let me just see if I can
4 show you. It may be harder to describe.
5 What is this part that's coming after
6 the sealant?
7 A. That's the rest of the layer.
8 Q. Of 541?
9 A. If I understand what you're referring
10 to, 541 corresponds to a layer that begins from
11 the left side and goes all the way to the portion
12 of it that I've marked.
13 Q. Right. And but you're also saying that
14 what you put in red is part of 541, correct?
15 A. Yes.
16 Q. All right. And what comes right after
17 what you've marked in red when you're going to the
18 left?
19 A. That's also 541.
20 Q. All right. That was my question.
21 A. Yes.
22 Q. How far does that extend?
23 A. Do you want me to draw it or to fill it
24 in maybe with blue?
25 Q. With blue, that would be great.

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1 A. (Indicating.)
2 So I've marked in blue the portion of
3 541 that is directly over the liquid crystal
4 region.
5 Q. And then along the top of the substrate
6 541 extends and then terminates in 521?
7 MR. SCHLITTER: Objection, form.
8 THE WITNESS: No, it doesn't at all. It
9 has no relationship to -- well, it does not end
10 with 521, which is a completely different element.
11 BY MR. GIBSON:
12 Q. Okay. Where does it end at the top of
13 the substrate or does it just continue on?
14 A. It continues off to the left across the
15 display. Of course what's illustrated here is
16 just one pixel, or one TFT more specifically, and
17 there should be more if it's a display.
18 Q. And then I may have asked you this
19 already, but would you agree in Fig. 4 at the
20 bottom of the sealant, there's not -- there's no
21 contact with an ITO layer?
22 A. On the bottom substrate of Fig. 4, there
23 is not contact of the sealant with an ITO layer.
24 Q. Now, this Fig. 4, this is the one that
25 you modified for your declaration?

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1 A. Yes.
2 MR. GIBSON: I'm going to mark this as
3 an exhibit in this matter so we have it. Let's
4 mark these two as the next in order.
5 (Documents marked as Deposition
6 Exhibit Nos. 1009 and 1010 for
7 identification.)
8 BY MR. GIBSON:
9 Q. And those are 1009 and 1010. And 1009
10 should be the drawing that you made on page 49
11 from your declaration.
12 Do you have that in front of you?
13 A. That's correct. It seems to be a
14 magnification of that.
15 Q. And as you stated yesterday, this is --
16 what's in your declaration here, this is your
17 attempt to draw out in color what is Fig. 4 in
18 Shiba?
19 A. I'm not sure I'd characterize it that
20 way. It is -- I'm trying to represent what is
21 explicitly in Fig. 4 of Shiba and I've left out
22 quite a lot of things and, you know, I'm using it
23 as a way to discuss the possibility of the ITO
24 layer being applied differently than what's
25 disclosed in Shiba.

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1 Q. All right. And then Exhibit 110 -- or
2 1010, excuse me, is your modified version of
3 Shiba?
4 A. It's my modification of Shiba's
5 disclosure according to Dr. Hatalis' hypothesis,
6 or at least one variation of his hypothesis.
7 Q. As you understood his hypothesis?
8 A. Yes.
9 Q. And you understood that from his
10 deposition testimony?
11 A. At least.
12 Q. And it may have also been from the
13 declaration that he submitted in this matter?
14 A. Yes, that's correct. For example, he --
15 I think he explicitly asserts that it would be
16 obvious to reorganize the layers of fabrication
17 in Shiba and I'm considering how that could
18 possibly be.
19 MR. GIBSON: Then if we could mark this
20 as 1012 -- 1011.
21 (Document marked as Exhibit Number 1011
22 for identification.)
23 BY MR. GIBSON:
24 Q. And Exhibit 1011 is another modification
25 of Shiba and we discussed this yesterday as well,

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1 correct?
2 A. Yes, we did.
3 Q. And yesterday you explained that this
4 modification would have two capacitors.
5 Do you remember that?
6 A. At least two capacitors, but yes,
7 there's two capacitors -- two capacitive
8 structures that are shown.
9 Q. And you said they'd be in series even
10 though the pixel electrode is electrically
11 connected to the source electrode?
12 A. In a very small portion of the source
13 electrode, the pixel electrode is being
14 illustrated as being connected.
15 Q. And that makes you say that they'd be
16 connected -- the capacitors would be connected in
17 series?
18 MR. SCHLITTER: Objection, form.
19 THE WITNESS: I'm observing that the
20 structure you're showing me is a sequence of three
21 conductors with insulating material in between
22 those three, and I'm observing that that is -- one
23 way to describe that is two capacitors in series.
24 BY MR. GIBSON:
25 Q. And the -- is there any reason that the

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1 contact portion between the pixel electrode and
2 the source electrode could not be made bigger?
3 Could one of ordinary skill in the art do that?
4 A. It depends. I didn't create this
5 figure.
6 Q. Well, if you just etched out more of the
7 protective overcoat on the left, you could create
8 a bigger connection between the blue and the
9 black?
10 MR. SCHLITTER: Objection, form.
11 THE WITNESS: Well, I suppose you could
12 do a lot of variations on -- on this figure, on
13 this hypothesis. Now, whether or not they would
14 be either obvious or successful is not clear to
15 me.
16 BY MR. GIBSON:
17 Q. If -- in this kind of a structure, do
18 you have to have the pixel electrode on top of the
19 storage electrode?
20 MR. GIBSON: Objection, form.
21 THE WITNESS: I don't understand this
22 structure at all beyond what you're showing me.
23 It's not my hypothesis. So what I see is a
24 cross-section of a structure and I'm commenting on
25 that. I can't -- I don't think I can speculate

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1 beyond that.
2 BY MR. GIBSON:
3 Q. Okay. So you can't tell me one way or
4 the other?
5 A. I can't tell you one way or the other.
6 Q. And what's -- in your mind, what's
7 causing the two capacitors is that -- well, strike
8 that.
9 Would you agree that one of ordinary
10 skill in the art would know how to etch the
11 protective overcoat so that it could be removed so
12 that it was no longer above the capacitor line Cj?
13 A. I do agree that one of ordinary skill
14 knows how to -- would know how to etch openings
15 into insulating films; for example, protective
16 overcoat 241. That would be possible, sure.
17 Q. So maybe I can just mark for you on
18 this. I've put a blue marking on the protective
19 overcoat to indicate if you etched to the right of
20 that blue marking, so you removed the protective
21 overcoat all the way from that blue marking to
22 the -- to the blue, you would then have a -- you
23 would no longer have that protective overcoat.
24 Someone of ordinary skill in the art would know
25 how to etch that off?

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1 A. It's a long question. Can you rephrase
2 it?
3 Q. Yeah, it wasn't -- it wasn't as clear as
4 I could make it.
5 So what I'm trying to find out is one of
6 ordinary skill in the art could etch the
7 protective overcoat off from where I made that
8 blue mark all the way over to where you see the
9 blue that's contacting with the black?
10 MR. SCHLITTER: Objection, form.
11 THE WITNESS: A person of ordinary skill
12 would know how to do that. As I've said, they
13 would know how to create openings in insulating
14 films. I've never seen that structure, so I don't
15 think any of these hypotheses are obvious to a
16 person of ordinary skill.
17 BY MR. GIBSON:
18 Q. Okay. But if you removed that extra
19 protective overcoat, as one of ordinary skill in
20 the art would at least know how to do that, would
21 that eliminate the two capacitors?
22 A. So you're now suggesting an additional
23 hypothesis?
24 Q. I'm suggesting a further removal of the
25 protective overcoat and asking you if one of

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1 ordinary skill in the art removed that additional
2 protective overcoat, would that eliminate the
3 issue you raise with two capacitors?
4 MR. SCHLITTER: Objection, form.
5 THE WITNESS: It's not clear to me what
6 you mean. Could you mark this up to represent
7 what you mean? Because it does sound like an
8 additional hypothesis. It's not what's shown,
9 right.
10 BY MR. GIBSON:
11 Q. It's probably easier to mark it on this.
12 So where I've marked in blue would be -- the
13 protective overcoat would be etched out there.
14 A. I see it.
15 Q. Would that then eliminate the two
16 capacitors issue that you raised yesterday?
17 A. In this additional hypothesis, I think
18 it may. It depends on probably the other
19 dimension as well.
20 Q. What other dimension?
21 A. This is two of the dimensions, right.
22 This is a three-dimensional structure, so the
23 third dimension is into the page.
24 Q. Okay. Do you know what the frame rate
25 frequency of displays was back in 1997?

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1 A. I have a rough understanding, but I
2 don't think I have a precise number for it.
3 Q. What's your rough understanding?
4 A. That the -- there are many different
5 rates and clock signals on a display. So are you
6 referring to the update rate on the screen itself
7 at the level of the TFTs?
8 Q. Yeah, the frame rate frequency.
9 A. I don't think there's one answer to that
10 even in 1997. I think it's typically in the range
11 of 80 to 100 hertz.
12 Q. And do you know what was a typical
13 display format in 1997?
14 A. There were many display formats that
15 were typical. There's no one answer to what is a
16 standard format.
17 Q. What were some of the standard or
18 typical formats?
19 MR. SCHLITTER: Object to scope. That's
20 beyond his declaration.
21 THE WITNESS: Shall I answer the
22 question?
23 MR. SCHLITTER: I'm not instructing you
24 not to answer.
25 THE WITNESS: Okay. Well, can you just

Page 90

1 clarify for me what you mean by "format"?

2 BY MR. GIBSON:

3 Q. Something like VGA, would you consider

4 that to be a display format back in 1997?

5 A. That resolution and video standard was

6 certainly well-known by that point. Whether or

7 not it's a display format, it depends on the

8 context, but yes, VGA, WVGA, there's SVGA. By

9 that point there were many.

10 Q. Any others that come to mind?

11 A. I don't think I could list with

12 confidence which ones were before 1997 and which

13 were after. So none others come to mind with any

14 surety.

15 Q. And do you know how -- a display, how

16 many lines and how many columns it had in 1997?

17 A. Likewise there's no one answer to that.

18 Q. Can you give me some examples?

19 A. I think an example would be displays

20 with, for example, 600-by-400 lines. As best I

21 remember, that would be a fairly high resolution

22 or medium resolution display.

23 Q. Any others?

24 A. Yeah, there are many others.

25 Q. Can you just list any that from 1997

Page 91

1 that come to mind?

2 MR. SCHLITZER: Objection, form,

3 foundation.

4 THE WITNESS: I'm going to have a hard

5 time being able to identify --

6 MR. SCHLITZER: Scope.

7 THE WITNESS: -- those things with --

8 with any certainty, mainly because I'm not sure

9 when they appeared and began to be used.

10 BY MR. GIBSON:

11 Q. If you look at again back at the '204

12 patent.

13 A. Yes.

14 Q. And look at Fig. 4A again.

15 A. I see it.

16 Q. And I think you yesterday listed two

17 advantages of the structures shown in Fig. 4A. I

18 think you listed low resistance and redundancy.

19 Do you remember that?

20 A. That's correct.

21 Q. Does the specification for the '204

22 patent refer to redundancy anywhere?

23 A. I'm not sure. I'd have to recollect my

24 memory.

25 Q. Please do.

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1 A. Are you asking specifically if the word

2 "redundancy" is in here?

3 Q. Or -- or words that would convey that

4 concept.

5 A. Okay. I'll take a minute and look.

6 Okay. I think I've reminded myself enough. I

7 apologize for the delay.

8 Certainly the reduced electrical

9 resistance is identified in multiple places, but

10 the -- there is redundancy that's mentioned, but

11 that's indeed mentioned with respect to the

12 peripheral driving circuits in, I think, Fig. 1.

13 So I'm recognizing that it's true about

14 his structure, that it would be -- it would have

15 redundancy, in addition to simply reducing the

16 resistance.

17 Q. That's not something that's identified

18 in the text that you were able to find as being an

19 improvement over the prior art?

20 A. The specification does not explicitly

21 talk about that, but I'm recognizing that the

22 specification does disclose it in its figures. I

23 mean, it's related to the fact that there's two

24 wires instead of one going at least partially in

25 the same direction.

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1 Q. Okay. Did you consider that to be an

2 improvement over the prior art?

3 A. I do.

4 Q. Okay. Do you understand that Shiba

5 specifically disclosed redundancy?

6 A. I'd have to see in Shiba where you're

7 referring to. I know wiring 127 has multiple

8 lines and that would also be redundant, six lines

9 next to each other. But are you referring to

10 something else?

11 Q. No, that's -- that's in the disclosure

12 that talks about line 27 that's in Column 6 that

13 we looked at before.

14 That also discloses an advantage for

15 redundancy, correct?

16 A. Which lines?

17 Q. It's the -- I think it's the lines we

18 looked at before in Column 6. In fact, there's

19 going to be two -- a two-layered structure?

20 A. Well, similar to the '204 patent, it

21 doesn't say it is a redundant structure, but I'll

22 observe that -- that at least some of the ways to

23 implement what's referred to in those two

24 sentences in Column 6, in lines 36 through 42,

25 that would also have an improved redundancy. But

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1 I'll also point out that, again, 127 is already
2 split -- split up into six lines and that by
3 itself has redundancy.
4 Q. All right. And I take it when you just
5 looked at the '204 patent --
6 A. Yes.
7 Q. -- you did consider Column 8 and
8 Column 9. They particularly talk about low
9 resistance, but they don't claim an advantage of
10 redundancy in Columns 8 and 9 of the '204?
11 A. Well, of course these columns aren't
12 claims. So they don't claim anything, but they do
13 refer to reducing the electrical resistance, but
14 not explicitly to the aspect of redundancy that
15 I'm observing is true.
16 Q. I wasn't using "claim" in the sense of a
17 patent claim. I was using it as an argument or a
18 statement.
19 But you would agree in Example 3 on
20 Column 8 and 9, there's no statement that -- about
21 there being an advantage of redundancy?
22 A. I don't see an explicit description to
23 that extent, but it is implicit to the structures
24 that are being talked about. I'm simply
25 recognizing that and I think one of ordinary skill

Page 95

1 would as well.
2 Q. In Fig. 4 -- strike that.
3 Example 3 is discussing Fig. 4A,
4 correct?
5 A. Example 3 refers to Fig. 4A and B at
6 least.
7 Q. Do you know if the '204 patent asserts
8 or states any advantages to using an ITO or
9 transparent conductive layer?
10 A. I don't recall any explicit mention of
11 the advantage of using ITO as opposed to anything
12 else. I think the '204 patent says that there
13 should be a transparent conductor. An ITO is one
14 possible choice and it's, of course, a very
15 standard choice.
16 Q. In 1997?
17 A. In 1997.
18 Q. What would a person of ordinary skill in
19 the art know about the advantages of using ITO in
20 1997?
21 A. Well, can you tell me what you mean by
22 an advantage compared to what?
23 Q. An advantage compared to another
24 alternative.
25 I mean, why would you use an ITO as

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1 opposed to some other kind of transparent
2 conductive layer?
3 A. I'm still not clear on what I'm
4 comparing to, so it's hard to say where the
5 advantage is, but I can comment on ITO if you
6 like.
7 Q. Particularly in 1997, why would a person
8 of ordinary skill in the art, why would they want
9 to use an ITO?
10 A. The LCDs that we're talking about in
11 this case are of a format where the electrodes
12 need to be both conductive and transparent. And
13 ITO is a material that's been known for a long
14 time that is a conducting oxide that is
15 transparent largely in the visible region.
16 So we can see through it, but it can
17 also hold a charge and act similar to metals and
18 other conductors. And I think it's become
19 standard because its deposition has become
20 well-known. It's been well studied and it's
21 obviously very successful even in 1997.
22 Q. And that was known in 1997, correct?
23 A. It was very well-known by then.
24 Q. And in your view, would one of ordinary
25 skill in the art be motivated to use an ITO in the

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1 terminal to connect to the flexible printed
2 circuit?
3 MR. SCHLITTER: Objection, foundation.
4 THE WITNESS: Can you be more specific
5 of the motivation that I'm commenting on?
6 BY MR. GIBSON:
7 Q. Let me ask it this way: To your
8 knowledge, in 1997, were ITO layers used in the
9 terminal pad region of commercial display
10 products?
11 A. I don't have specific knowledge of its
12 use in commercial display products. I don't have
13 any proprietary information and I never analyzed a
14 display for that question. But I do note that at
15 least two of the prior art pieces have ITO in the
16 terminal portion; Sukegawa, of course, below the
17 second insulating layer and Nakamoto from
18 yesterday.
19 Q. And are you familiar with any prior art
20 that discloses advantages of using an ITO layer in
21 the terminals used to make external connections to
22 the FPC?
23 A. Beyond the prior art that's in this
24 case, I don't know of any other specific
25 discussion on that, on the advantages of that.

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1 Q. But Sukegawa and Nakamoto would be
2 examples of advantages of that?
3 A. Yeah, there are disclosures mentioned,
4 some of those advantages, yeah. Well, to be
5 clear, I think Sukegawa does. I'm not sure -- I
6 don't recall what Nakamoto discloses about that.
7 It may just be that he discloses it and doesn't
8 comment on it.
9 Q. In Fig. 4A, we talked about the ITO
10 layer a little bit yesterday in terms of this
11 upside down L.
12 A. I see the upside down L. I have to
13 admit I don't remember our discussion yesterday.
14 Q. That's fine.
15 Could you pattern the ITO so that the
16 bottom part of the upside down L was not there?
17 A. Are you referring to the upper portion
18 that's horizontal, the highest most portion of the
19 ITO?
20 Q. Yes, yes.
21 A. That could be patterned away like the
22 rest of the ITO that is -- that is missing on the
23 left -- to the left side which would appear
24 underneath the sealant but, of course, it's been
25 etched away.

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1 Q. You could etch away all of the upside
2 down L if you wanted to, right?
3 A. I think it's at least possible to do
4 that kind of thing and I don't know if it's
5 practically feasible to really get precise about
6 that, probably not worth the trouble, but that's
7 my speculation. But nevertheless, it would still
8 meet the claims, especially Claim 54 that we
9 talked about explicitly.
10 Q. Without the upside down L, you say that
11 the claim language of 54 is still met?
12 A. Oh, certainly, yeah.
13 Q. In that situation where the -- you don't
14 have the upside down L part of the ITO, how would
15 a person of ordinary skill in the art know which
16 deposition process would come first between 113
17 and 114?
18 A. Just so we can be clear, could we draw
19 that so I can comment on it? Or could we mark up
20 a figure for that?
21 Q. If you'd like to mark up the figure, go
22 ahead. Or is this one you've already marked on?
23 A. It is. It's where I labeled -- should I
24 mark that one?
25 Q. I thought I had another copy of Fig. 4,

Page 100

1 but --
2 A. There are probably several copies of
3 Fig. 4 in my declaration.
4 Q. I'm sure there are copies of Fig. 4 in
5 your declaration. I've seen several myself. I
6 think the easiest thing to do, why don't you -- on
7 the Fig. 4A that's before you that's in the '204
8 patent that's Exhibit 1001, why don't you just
9 cross-hatch out --
10 A. Would you like to cross-hatch out the
11 portion you want to remove?
12 Q. (Indicating.)
13 Okay. I've done that in blue.
14 A. Thank you. Can you remind me the
15 question?
16 Q. Sure. So if we have the structure
17 that's before you in 4A the way I crossed it --
18 crossed it out, you would agree that that conforms
19 to Claim 54 still?
20 A. I do. The little piece that you've
21 crossed out has no bearing to me on Claim 54.
22 Q. And if you have the structure that I put
23 before you, how would a person of ordinary skill
24 in the art determine in which order layers 113 and
25 114 are formed?

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1 A. It would not be apparent from that
2 figure or from that cross-section alone.
3 Q. So you could form the ITO layer first
4 and then form the resin layer second?
5 MR. SCHLITZER: Objection, misstates his
6 testimony.
7 THE WITNESS: The figure -- the modified
8 figure doesn't give enough information to
9 determine the sequence of deposition during
10 fabrication between element 113 and 114.
11 BY MR. GIBSON:
12 Q. But it would be possible to form the ITO
13 layer first and then form the resin layer over the
14 ITO layer and then open up and etch the resin
15 layer out?
16 A. If that was done, of course that would
17 not meet the claim limitation, Claim 54.
18 Q. But you would agree that one of ordinary
19 skill in the art could use that process?
20 A. I must misunderstand your question.
21 Could you restate it?
22 Q. One of ordinary skill in the art could
23 put down the ITO layer and then put down the
24 insulating layer and then etch out the insulating
25 layer to reveal the ITO layer, correct?

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1 A. It does -- I do think that that is a
2 possibility for one of ordinary skill, but of
3 course, that is not what would meet Claim 54, if
4 that was done.
5 Q. So we could have a completed structure
6 and you wouldn't know whether it met Claim 54
7 unless you knew the steps that were used to
8 deposit the materials?
9 MR. SCHLITTER: Objection, form.
10 THE WITNESS: Well, Claim 54 has
11 limitations that relate to how contacts are being
12 formed. For example, there's a limitation near
13 the end of the claim that says there must be
14 direct contact through an opening between the
15 second conductive line and the transparent
16 conducting layer. And so if that contact is -- is
17 not through that opening, then it wouldn't meet
18 the claim. And so one would need to be able to
19 determine that that's true.
20 BY MR. GIBSON:
21 Q. And if you're looking at the modified
22 Fig. 4A, you don't know if that's true with that
23 figure unless you know the steps that were taken
24 to deposit and etch, correct?
25 A. Well, what I'm saying is this modified

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1 figure is inconclusive to that regard.
2 Q. So you wouldn't know if that type of a
3 structure was covered by Claim 54 unless you knew
4 the deposition and etching steps, is that correct?
5 A. Well, as far as this figure goes, I
6 think that's correct. But it's most likely the
7 case that if it was a real display, you could
8 analyze it in such a way that you could tell the
9 difference of whether the ITO was deposited before
10 or after the element 113 insulating film.
11 Q. But in terms of this figure, my question
12 you agree with?
13 A. In terms of the figure, it's
14 inconclusive. If a display was made in reality,
15 it could be analyzed where I think one of ordinary
16 skill could tell the difference.
17 Q. But you'd have to look at the particular
18 display to see if you actually could tell the
19 difference or not, correct?
20 A. You'd have to use an electron microscope
21 to analyze the layers and look at the shapes of
22 the interfaces and all that.
23 MR. GIBSON: Okay, why don't we take a
24 break?
25 VIDEOGRAPHER: We're going off record.

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1 This is the end of Media Unit Number 2. The time
2 is 12:47.
3 (Whereupon, the deposition in the
4 above-entitled cause was recessed to
5 1:51 p.m. this date.)
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1 AFTERNOON SESSION
2 VIDEOGRAPHER: We're now on record.
3 This is the beginning of Media Unit Number 3 in
4 the deposition of Dr. Michael Escuti and the time
5 is 1:51. Please continue.
6 EXAMINATION (Resumed)
7 BY MR. GIBSON:
8 Q. Welcome back. You understand you're
9 still under oath?
10 A. Yes.
11 Q. And have you discussed your testimony
12 with anyone at any of the breaks either today or
13 last night?
14 A. I have not discussed the testimony of
15 the ongoing deposition within the breaks of the
16 deposition.
17 Q. Does that mean you did discuss it last
18 night when there wasn't a break?
19 A. We -- we had a few comments about
20 yesterday's deposition this morning, but I don't
21 -- I don't think that's what you're referring to.
22 Q. What did you discuss this morning?
23 A. I don't recall.
24 Q. You don't recall any of the
25 conversations you had this morning regarding

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1 yesterday's testimony?
2 A. I don't. It was very brief and I don't
3 think had anything substantial in it.
4 Q. Let's talk about Sukegawa, which is
5 another patent that you considered in your
6 declaration for the '204 patent, is that correct?
7 A. Yes.
8 (Document marked previously as Exhibit
9 Number 1005 was presented.)
10 BY MR. GIBSON:
11 Q. I've got another one. Apologize for
12 that.
13 And would you agree that Sukegawa is
14 addressing the exposed part of the wiring that's
15 used for testing the contact with the FPC?
16 A. Could you clarify what you mean by
17 "addressing"?
18 Q. That's what it's directed to.
19 A. Among other things, it's -- it involves
20 the open portion of the terminal, the opening in
21 layer 9, so that the FPC can have electrical
22 contact with the structures below it.
23 Q. Right. And one of the problems with the
24 prior art was that there could be corrosion in
25 that open region?

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1 A. That is what is disclosed in Sukegawa
2 and Fig. 2 shows an example of that.
3 Q. Is Sukegawa directed to protecting a
4 wire under an insulating layer?
5 A. I'm not sure I can agree with that. It
6 is a consequence or part of the disclosure in
7 Sukegawa, but I don't think his disclosure is
8 focused on that.
9 Q. When we look at Fig. 2C, for example, if
10 the wiring 7 were to continue to extend under 9,
11 do you think there needs to be any other material
12 above it to protect it from corrosion?
13 A. I think the clear disclosure in Sukegawa
14 is that the layer 8, the transparent conductor,
15 should be always covering layer 7, the second
16 wiring or the upper wiring I should say here. And
17 if layer 7 is modified at all from what's
18 disclosed here, then layer 8 should also be
19 correspondingly modified to always cover it based
20 on the disclosure of Sukegawa.
21 Q. Isn't it talking about layer -- or isn't
22 Sukegawa discussing the importance of layer 8
23 covering layer 7 in the open region that's
24 designated 13 in 2C?
25 A. I think it's clear in Sukegawa that he

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1 discusses the importance of layer 8 protecting
2 layer 7 everywhere. His central invention, after
3 all, is to provide a double coverage where one of
4 those coverages comes from the transparent
5 conductor 8 and where the other one comes from
6 several things.
7 Q. But isn't that double coverage in the
8 terminal portion only?
9 A. It depends on what you define as the
10 terminal portion. Could you identify that for me?
11 Q. Well, what we're looking at, for
12 example, in 2C, would you consider that to be part
13 of the terminal portion?
14 A. Fig. 2C should include the terminal
15 portion, certainly.
16 Q. Is there somewhere in the display
17 portion where Sukegawa is saying there should be
18 double coverage with both a layer 8 and an
19 insulator 9?
20 A. Fig. 3C shows a TFT cross-section which
21 would be inside the display portion and, of
22 course, within the seal region. And Fig. 3D shows
23 the two substrates 100, 200, between which would
24 be the seal region -- well, the sealant, I should
25 say.

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1 So I think the clear teaching from
2 Sukegawa in the specification is that double
3 coverage is necessary of layer 7 and as long as it
4 is exposed to the environment outside the seal.
5 Q. Right.
6 A. So --
7 Q. I didn't mean to interrupt you.
8 A. Just to get to the final point in your
9 question then, the Fig. 3C doesn't have the
10 layer 8 in it because indeed, it's no longer
11 needed at that point because the additional
12 coverage comes from the other elements in the
13 display, including the counter substrate.
14 Q. And the sealant?
15 A. Well, the sealant is not disclosed and
16 so we have no idea what he would disclose about
17 that. He's silent about that.
18 Q. But as we discussed yesterday, one of
19 ordinary skill in the art would understand there
20 would be sealant?
21 A. Sukegawa discloses that there is sealant
22 and one of ordinary skill would understand that it
23 should be somewhere between substrate 200 and 100
24 in Fig. 3D, but not its position of course and in
25 this matter in particular, you're probing around

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1 where Sukegawa would end layer 8 in between
2 Fig. 3C and any of the other terminal portions and
3 he's silent on that.
4 Q. Where there is sealant, would that
5 provide extra protection for wiring 7?
6 A. It should. You know, it would be
7 speculation on my part to know if Sukegawa would
8 agree to that, but it could.
9 Q. And maybe you answered this, but I'm
10 not -- I'm not sure I got a clean answer to it.
11 Would you agree that the display portion
12 of Sukegawa does not teach covering wire 7 with
13 both a layer 8 and an insulating film 9?
14 A. I don't think it teaches one way or
15 another. It does show Fig. 3C, which is inside
16 the display portion and the metal 7A and, of
17 course, 7B does not have layer 8 on it clearly.
18 It's -- but it, of course, could be elsewhere.
19 He's silent on it.
20 Q. It's not shown in Fig. 3C?
21 A. Indeed, it's -- it's not shown above
22 layer 7. Of course Fig. 3C does have ITO. It's
23 on the left. It's 8A, forming the pixel
24 electrode.
25 Q. Right, but it's not covering wiring 7

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1 throughout the entire Fig. 3C?
2 A. It's only covering -- the ITO element 8
3 is only covering the element 7C on part of it and
4 it's not above it in other regions.
5 Q. And when you say that Sukegawa is
6 silent, you would agree there's nothing in the
7 text that says you're going to have wire --
8 layering wire 8 covering 7 and also being covered
9 by 9 in the display region?
10 A. Could you rephrase the question or make
11 it more concise?
12 Q. Sure. You said that Sukegawa is silent
13 on the need for 8 to be over 7 in the display
14 region, correct?
15 A. It seems that's largely correct. He has
16 an instance where it is over -- where they do
17 overlap and an instance where it doesn't.
18 Q. And you're looking at Fig. 3C that you
19 just testified about when you say that?
20 A. Yes.
21 Q. All right. And is there anything in the
22 text that describes 8 overlapping 7 in the display
23 region?
24 A. There is no disclosure on what should
25 happen in the display region, aside from Fig. 3C,

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1 about the relationship of layer 8 and 7.
2 Q. In looking at 2C, would one of ordinary
3 skill in the art understand that once wiring 7
4 goes under -- strike that.
5 Assume in 2C you continued wiring line 7
6 under insulator 9, but didn't continue the ITO
7 layer. Are you with me?
8 MR. SCHLITTER: Objection, form.
9 THE WITNESS: I can assume that, but of
10 course, I don't agree that that's at all
11 disclosed.
12 BY MR. GIBSON:
13 Q. Just assume it for me.
14 Would you -- do you believe there's any
15 need in terms of corrosion to have 8 continue
16 above 7 as 7 goes further under insulator 9?
17 A. I think the disclosure in Sukegawa is
18 clear on exactly this question. He wants double
19 coverage above wiring 7 because of the corrosion
20 potential and that corrosion comes from the air
21 that's above and around both in fabrication and in
22 the use of this device.
23 And so I think it's clear from Sukegawa
24 that if layer 7 is extended under the sealant,
25 then layer 8 must be, according to his teaching,

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1 extended as well, at least as far as -- as under
2 the sealant to provide that double coverage and
3 most likely a bit longer.
4 Q. What in Sukegawa are you relying on for
5 that answer?
6 A. His disclosure, his specification,
7 illustrations.
8 Q. Can you be specific?
9 A. Every cross-section that illustrates
10 both element 7 and element 8 has the indium tin
11 oxide 8 surrounding and going beyond wiring 7 on
12 all sides and that is because it's protecting it.
13 So it has to seal it all the way around.
14 So if we -- if we extend 7, Sukegawa is
15 clearly saying for corrosion protection, it needs
16 to also extend 8 and this corrosion protection is
17 principally necessary at least on the inside of --
18 I'm sorry -- on the outside of the sealant during
19 its use, but during fabrication there's also the
20 potential for corrosion and even in that case it
21 would likely be extended beyond as well, beyond
22 the sealant as well.
23 Q. And if 7 is extending into the display
24 region, is Sukegawa saying that 8 has to cover 7
25 into the display region as well?

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1 A. I think my answer's the same as it just
2 was before. He shows in Fig. 3C an example where
3 part of it does and part of it doesn't and he
4 doesn't say much more about the display portion
5 than that.
6 Q. And as one of ordinary skill in the
7 art or your -- in your view, would one of the
8 ordinary -- would one of ordinary skill in the art
9 at some point stop layer 8 over layer 7 once it
10 enters the display region?
11 A. Yes, and that's certainly what's
12 disclosed in Fig. 3C, in part of it.
13 Q. Where would that happen?
14 A. He doesn't show.
15 Q. So how would one of ordinary skill in
16 the art determine that?
17 A. It would depend on, I suppose, many
18 things, the design of -- of the display. I'm not
19 sure I can say if there's a single answer to that.
20 Q. In terms of the prior art, the prior art
21 shows 7 extending into 13 and beyond it, correct,
22 in Fig. 2C, for example?
23 A. In Fig. 2C, wiring 7 extends beneath
24 element 13, certainly, and beyond it.
25 Q. And in 3E, wiring 7 is stopped, correct?

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1 A. Well, Fig. 3E is the cross-section of
2 the terminal which has a top-down view in 3A. So
3 what you see is that wiring 7 is broken up into
4 two pads or rectangles. And in the cross-section,
5 yes, there's a separation between the two.
6 Q. If you look at 3C --
7 A. I see it.
8 Q. -- 7 is just covered by 9 there in --
9 let's say 7A is just covered by 9, is that
10 correct?
11 A. 7A and 7B are only covered by 9.
12 Q. And would you agree that that's
13 sufficient to prevent corrosion?
14 MR. SCHLITTER: Objection, foundation.
15 THE WITNESS: I can't say. The figure
16 speaks for itself. It's -- I have no idea if
17 Sukegawa would find that sufficient for that
18 purpose, but it is an example of what he's shown
19 here.
20 BY MR. GIBSON:
21 Q. All right. And there's not -- as we've
22 said before, there's no ITO layer over there to
23 show a double layer structure needed to protect
24 against corrosion, right?
25 A. In -- in Fig. 3C, which is a TFT

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1 inside -- well inside the display, it only has
2 layer 9 over layer 7A.
3 Q. And you would agree that 9 can be made
4 out of something like silicon nitrate -- nitride?
5 A. I would agree that that's an option and
6 Sukegawa does mention that.
7 Q. And that's a good protection layer
8 that's used commonly in the microelectronics
9 industry, is that right?
10 A. Yes, that's correct.
11 Q. And that was known to someone of
12 ordinary skill in the art in 1997 to be a good
13 protection layer?
14 A. It's certainly known to a person of
15 ordinary skill in 1997 that it's an insulating
16 material that can be used to protect the circuits
17 below. Whether it's characterized as good or not
18 probably depends on the context. Sukegawa seems
19 to teach that it's not good enough, at least in
20 the terminal portion.
21 Q. And that wasn't quite my question. So
22 I'll move to strike as nonresponsive.
23 Would someone of ordinary skill in the
24 art in 1997 recognize that silicon nitride could
25 be used as a protection layer in the

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1 microelectronics industry?
2 A. Yes.
3 Q. If we look at 3D?
4 A. I see it.
5 Q. And Fig. 3C, there's a -- in 3D there's
6 a gap between element 200 and 31A, is that
7 correct?
8 A. Yes.
9 Q. And then why don't you look at Fig. 2C?
10 A. I see it.
11 Q. And there's also a gap shown there as
12 well, is that correct?
13 MR. SCHLITTER: Objection, form.
14 THE WITNESS: Which gap are you
15 referring to in Fig. 2C?
16 BY MR. GIBSON:
17 Q. Well, let me ask it like this: If
18 you're -- in 3D, the display portion, you're going
19 to have that where 200 and 100 in the beginning of
20 that, where 100 and 200 meet?
21 A. The precise beginning of that is not
22 clear. Some of the other prior art identifies the
23 display area as well within that. Sukegawa
24 doesn't, I think, precisely provide the start of
25 that.

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1 Q. But either in what we see in Fig. 3D or
2 off to the left of what would be in Fig. 3D, we're
3 going to have a display area, correct?
4 A. Yes.
5 Q. And when you're -- as I understand it,
6 there's going to be a layer 7 that's going to be
7 connected through that gap in 3D?
8 A. I'm not sure what you mean about
9 Fig. 3D. Maybe you could point me to it or draw
10 it. Of course layer 7 is not shown in Fig. 3D.
11 Q. Right. But would you assume that there
12 is a layer 7 in Fig. 3D based on what you see in
13 the other figures?
14 MR. SCHLITTER: Objection, form.
15 THE WITNESS: Well, the other parts of
16 Fig. 3 show an FPC and the terminal portion and
17 that does include Fig. 7. How that precisely
18 matches up with Fig. 3D is not clear. It's not
19 shown. But I do expect that there is a wiring 7
20 or 7-1, 7-2 in Fig. 3D, largely underneath the FPC
21 and the anisotropic conducting film that's
22 identified there.
23 BY MR. GIBSON:
24 Q. Would you expect that it's going to
25 extend beyond what we have as 31A, the left of

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1 31A, do you expect layer 7 is going to extend
2 beyond that?
3 A. Figs. 3E, for example, 3B, show that
4 there's at least some portion of wiring 7 that is
5 to the left, however slightly, but to the left of
6 the FPC in element 10.
7 Q. And can you tell from Figs. -- in
8 Fig. 3D how far that's going to extend to the left
9 after 31A ends?
10 A. It's -- it's not shown in Fig. 3D, so I
11 can't tell. But one thing I can tell is that in
12 Fig. 3E and 3B, the terminal -- I'm sorry -- the
13 substrate 200 is not included or shown, nor is the
14 sealant. So wherever it is, it has to be the left
15 of any wiring 7 in the terminal portion in this
16 disclosure.
17 Q. And that's just because you see that
18 there's the substrate 200 isn't shown in 3E?
19 A. It's because the sealant is not shown
20 and the substrate is not shown.
21 Q. Okay. But even if you look at say 3B,
22 no substrate 200 is shown in 3B, correct?
23 A. The substrate is not shown in 3B either,
24 but nor is the FPC 31 and its elements 10,
25 et cetera, right. Fig. 3B is largely the same as

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1 Fig. 3E with that difference.
2 Q. All right. And the sealant's not shown
3 in Fig. 3B either, correct?
4 A. That's correct, it's not.
5 Q. But we would know or a person of
6 ordinary skill in the art would know that there's
7 going to be sealant and a substrate 200 even
8 though they're not shown in 3B?
9 MR. SCHLITTER: Objection, form.
10 THE WITNESS: Well, those elements --
11 well, the substrate 200 and 100 are shown in 3D
12 and a person of ordinary skill knows that the
13 sealant has to be between those and it would be
14 most common to place that offset from the edge as
15 we've talked about.
16 BY MR. GIBSON:
17 Q. But what I'm asking you specifically is,
18 in 3B, there's no sealant shown even though we
19 know it's going to be there?
20 MR. SCHLITTER: Objection, form.
21 THE WITNESS: Fig. 3B does not show
22 sealant. A person of ordinary skill would not put
23 the sealant in the terminal portion. That's why
24 it's not shown.
25

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1 BY MR. GIBSON:
2 Q. You would agree that the terminal
3 portion connects the display portion, correct?
4 A. Perhaps through other portions that we
5 might identify, but certainly the substrate 100 in
6 3D corresponds to the element 1 in Fig. 3B, 3C,
7 3E.
8 Q. If you look at 2C --
9 A. I see it.
10 Q. -- and wiring 2, you would consider that
11 to be a scan line?
12 A. That corresponds to the gate metal
13 layer, so that is the same metal layer as the scan
14 lines. There may be a difference between calling
15 it a scan line in this portion or not, but it's
16 certainly the same metal layer.
17 Q. As the scan line?
18 A. As the scan line. Sukegawa, of course,
19 calls it the lower layer metal wiring 2.
20 Q. But one of ordinary skill in the art
21 would understand that's going to be a scan line,
22 right?
23 A. No, not necessarily. It's a -- it's a
24 metal that's been deposited in the same layer as
25 the scan lines, but there's no disclosure to limit

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1 it to that. It could be used for other things.
2 Q. And if we're talking about a bottom gate
3 TFT, would you assume 2 is a scan line?
4 A. A person of ordinary skill cannot assume
5 that.
6 Q. Okay. What's your understanding of a
7 bottom gate TFT?
8 A. It's a TFT that has its gate positioned
9 underneath the semiconducting layer, so at the
10 bottom of the TFT.
11 Q. And if you assume that the -- let's
12 assume that you've got a bottom gate TFT and that
13 wiring line 2 is a scan line.
14 A. I can assume that.
15 Q. And that's a structure that someone
16 would see in industry?
17 A. It's one of the possibilities that
18 certainly a person sees in industry, yeah, and
19 that's disclosed in Sukegawa in Fig. 3C, for
20 example.
21 Q. And in Fig. 3C we have a bottom gate TFT
22 with 2A. That would be a scan line?
23 A. Well, Sukegawa calls 2A a gate
24 electrode. At least in this portion, it's a gate
25 electrode. It should also be part of the scan

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1 line, but I'm not sure that's expressly in the
2 specification.
3 Q. Okay. But one of ordinary skill in the
4 art would understand that it would be part of the
5 scan line?
6 A. This gate metal 2A should be part of the
7 scan line.
8 Q. And what if it's a top gate TFT -- or
9 let me first ask you, are you familiar with top
10 gate TFTs as a term of art in the industry?
11 A. Yes, that's the situation where the
12 structure is at least nominally inverted and the
13 gate is the upper metal.
14 Q. And then 2A, would that still be a scan
15 line?
16 A. In -- in that case, 2A would still be
17 the gate electrode.
18 Q. Which would connect to a scan line?
19 A. Which would be part of the scan line,
20 yes.
21 Q. So if you look at 3E, and 3E shows both
22 wiring 2 and a wiring 7?
23 A. It shows a lower -- let's see. What
24 does he call it? He shows a lower layer metal
25 wiring 2 and an upper layer metal wiring 7 in

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1 Fig. 3E. Well, to be more precise, he shows in
2 Fig. 3E that the upper layer metal wiring is split
3 up into two parts, 7-1 and 7-2.
4 Q. And it's connected through wiring 2?
5 A. The two portions of wiring 7, 7-1 and
6 7-2, have electrical contact through the lower
7 layer metal wiring 2 as well as the transparent
8 conductor 8.
9 Q. But if we go to the right -- if you look
10 at 3E -- well, strike that.
11 As we go into the display portion and we
12 go from 3E to say 3C, is there going to be a
13 connection that's made -- it's not shown here, but
14 there's going to be a connection that's made
15 between wire 7 and wire 2?
16 MR. SCHLITTER: Objection, form.
17 THE WITNESS: Well, that -- that is not
18 expressly disclosed in Sukegawa.
19 BY MR. GIBSON:
20 Q. Isn't it necessary to connect the data
21 line to the scan line?
22 A. I think it's expressly incorrect to do
23 so. You'd wind up with a nonfunctioning pixel.
24 Q. I may have asked it incorrectly.
25 But how are you going to connect to your

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1 data lines in what's disclosed in 3C?
2 A. Well, 3C includes a source and a drain.
3 Of course those are the data lines or involve the
4 data lines, for example, 7A. And the rest of the
5 disclosure of Sukegawa is about the terminal
6 portion and there are many ways to connect what's
7 shown in the terminal portion to that wiring 7.
8 One way would be that somewhere off to
9 the left of these terminal portions inside the
10 display to form an opening in insulator 3 and have
11 contact through that opening with the upper layer
12 metal wiring 7, or whatever you want to call it,
13 7A. That seems to be -- to me to be the first
14 thing that would come to the mind of a person of
15 ordinary skill looking at Sukegawa.
16 Q. And that's -- the structure you've just
17 discussed has the advantage of -- doesn't require
18 extra contacts and would conserve space?
19 MR. SCHLITTER: Objection, form.
20 THE WITNESS: I'm -- I'm not sure what
21 you mean by those advantages. I don't see it has
22 any consequence on the amount of space being used.
23 And I'm not sure what you mean by extra contacts.
24 One terminal is generally used to contact to one
25 data or scan line.

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1 BY MR. GIBSON:
2 Q. Well, you're not -- you're not having to
3 use a third wiring, for example, to connect your
4 -- to have 2 to connect to 7; you can open up a
5 hole and have 2 connect to 7?
6 A. That's correct.
7 Q. So that's simpler than using a third
8 wiring, for example?
9 A. I'm not sure I'd use the phrase "third
10 wiring" in this context because, you know, it's a
11 claim term. So it would be an additional -- let
12 me -- let me clarify.
13 So if we did what I described, then
14 layer 7A can have electrical contact through the
15 opening in insulator 3 to the lower metal wiring 2
16 without an additional wiring somewhere. But I
17 don't want to characterize any of these as third
18 wirings.
19 Q. And I wasn't looking for that. I'm just
20 saying there's not an additional wiring that's
21 going to be required the way you suggested you're
22 avoiding that additional wiring, correct?
23 A. Yes, that's -- that's true in the
24 suggestion that I offered.
25 Q. And that saves space?

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1 A. Saves space compared to what?
2 Q. Compared to having a third wiring. I'm
3 sorry, I don't want to use the term "third
4 wiring."
5 It's better to avoid having additional
6 wiring, it takes up extra space, it is an extra
7 manufacturing step, right?
8 MR. SCHLITTER: Objection, form,
9 foundation.
10 THE WITNESS: I'm trying to follow you,
11 but I don't know what you mean by a "third
12 wiring."
13 BY MR. GIBSON:
14 Q. I'm using the word "additional wiring"
15 now that you used --
16 A. Okay. Well, can you tell me what you
17 mean by "additional wiring"?
18 Q. Well, how you suggested it. I'm not --
19 you're the -- you're the expert here. What -- if
20 you were going to not open up a contact hole
21 between -- in layer 3 for 7 and 2 in Fig. 3C and
22 instead you use an additional wiring, wouldn't
23 that be more complicated than just opening up a
24 hole in 3 for contact between 7 and 2?
25 MR. SCHLITTER: Objection, form and

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1 foundation.
2 THE WITNESS: Whether or not it's more
3 complicated depends. I don't have enough
4 information to make any kind of statement to that
5 regard --
6 BY MR. GIBSON:
7 Q. Would you think --
8 A. -- in general.
9 Q. Would you think that it would take extra
10 space?
11 A. It depends.
12 Q. Do you think it would -- well, strike
13 that.
14 In your view in 1997, would it have
15 been -- for the ordinary person of skill in the
16 art been more common to use an additional wiring
17 or just open up contact holes in 3 for 7 and 2 to
18 be in contact?
19 MR. SCHLITTER: Objection, foundation.
20 THE WITNESS: I can't say if there was a
21 preference in that case.
22 BY MR. GIBSON:
23 Q. You don't know one way or the other?
24 A. There's not enough information to know
25 one way or the other.

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1 Q. Would you agree in 1997 that a person of
2 ordinary skill in the art was aware of opening up
3 contact holes in layer 3 as depicted in Fig. 7C to
4 enable a connection between 7A and 2A or between
5 wiring 7 and wiring 2?
6 A. No, I'm not aware of anything like that.
7 It would make the TFT not work.
8 Q. And maybe I misspoke when I said 7A and
9 2A.
10 When -- you're talking about opening up
11 contact holes in 3, you were talking about
12 having -- enabling a connection between 7 and 2,
13 correct?
14 A. The one option -- not the only option,
15 but one option would be indeed to create an
16 opening somewhere inside the display portion, an
17 opening in layer 3, so that some portion of 7A
18 could have contact with the lower layer metal
19 wiring 2 through that opening.
20 Q. And that was something that was known to
21 one of ordinary skill in the art in 1997?
22 A. It was known as one of multiple options.
23 Q. So if we look at -- still looking at the
24 figures on -- where we have Figs. 3A, 3B and 3C?
25 A. I see it.

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1 Q. Is 2A the gate of the TFT?
2 A. 2A is the gate electrode of that TFT.
3 Q. And what is to the left of the gate
4 electrode?
5 MR. SCHLITTER: Objection, form.
6 THE WITNESS: Immediately to the left is
7 the insulator 3, of course in that same horizontal
8 direction. Above that are the other elements of
9 the TFT, including one of the source drain
10 electrodes and the pixel electrode, 8A, formed of
11 the same transparent conductor, most commonly is 8
12 in the terminal portion.
13 BY MR. GIBSON:
14 Q. So you would agree that 8A is the pixel
15 electrode?
16 A. I do. That's what Sukegawa calls it,
17 the pixel electrode, 8A.
18 Q. And you would agree that above the pixel
19 electrode, there is no layer 9?
20 A. I can't agree to that. Layer 9
21 partially overlaps 8A.
22 Q. Well, the part that -- there is part of
23 it that -- part of 8A right there that does not
24 have a insulating layer 9 on top of it, correct?
25 A. There is a portion of 8A that is not

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1 covered by insulator 9, that's true.
2 Q. Do you know why that would have been
3 removed?
4 A. I think I do.
5 Q. And why is that?
6 A. Well, the response of the liquid crystal
7 layer depends on the electric field that's
8 produced in it. And having additional insulators
9 between the electrode that's applying the
10 potential to the liquid crystal generally requires
11 higher voltages to switch. So it's a kind of
12 negative direction in most -- most contexts and
13 while it's not catastrophic, it's generally to be
14 avoided.
15 Q. So in other words, if you remove 9, you
16 can use a smaller voltage?
17 A. The voltage to get the same optical
18 effect in the liquid crystal layer would be lower
19 if you removed 9.
20 Q. And if you -- could you extend 8A, the
21 pixel electrode all the way to the terminal
22 portion to the left?
23 A. I don't think so. It sounds like you're
24 saying can we make some very large portion of the
25 display a single pixel. That would seem like a

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1 very strange display.
2 Q. Right. Because you need to have lots of
3 pixels to have a good display?
4 A. Usually.
5 Q. And so you would agree that you're going
6 to have a number of pixel electrodes in the
7 display portion that are going to extend out to
8 the terminal portion, you're not just going to
9 have one?
10 MR. SCHLITTER: Objection, form.
11 THE WITNESS: I don't think you're going
12 to have any pixel electrodes in the display
13 portion that are going to extend to the terminal
14 portion.
15 BY MR. GIBSON:
16 Q. Instead you're going to have a plurality
17 of pixels in the display portion?
18 A. Certainly a plurality of pixels and each
19 one should have its corresponding pixel electrode
20 in a -- at least approximately rectangular shape
21 connected to the TFT.
22 Q. To the right of the TFT we see 7A?
23 A. Yes.
24 Q. And I think we agreed that that is a
25 data line?

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1 A. Yes.
2 Q. Is the data line in the -- in the
3 display region continuous across the display?
4 MR. SCHLITTER: Objection, form.
5 THE WITNESS: What do you mean by
6 "continuous"?
7 BY MR. GIBSON:
8 Q. Does it start at one edge and then
9 terminate at the opposing edge?
10 A. Most of the time that's the way a data
11 line is configured, but certainly not always.
12 Q. And the scan lines, are those continuous
13 across the display?
14 A. Same answer, most of the time they will
15 extend from one side to the other side, but not
16 always.
17 Q. And would you agree that the scan lines
18 are isolated from each other?
19 A. Individual scan lines should be
20 electrically isolated from each other to function.
21 Q. And I think as you pointed out a couple
22 times to me, the scan lines should also be
23 electrically isolated from the data lines?
24 A. Yes, that's a general principle that
25 needs to happen for the TFT to work.

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1 Q. And the data lines are also isolated
2 from one another?
3 A. Yes.
4 Q. And in Fig. C3 (sic) we have the
5 insulating layer 9 above 7A?
6 A. In Fig. 3C we do, yes.
7 Q. All right. And as this line 7A extends
8 across the display region, is 9 going to be over
9 the entire length of that?
10 A. It's not shown, so it's not clear. It's
11 not disclosed.
12 Q. Is there anything to suggest that it
13 would not?
14 A. There's nothing explicit that suggests
15 it would not. I just note that in Fig. 3C,
16 layer 9 does end over the pixel region.
17 Q. But as we're extending to the terminal
18 portion, would you expect 7A to -- as 7A is
19 extending, to have 9 over it?
20 A. I can't say one way or another. It's
21 just not shown.
22 Q. And in Fig. 3C, the only place we don't
23 have layer 9 is where we have the pixel electrode,
24 8A, correct?
25 A. That's true in Fig. 3C.

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1 Q. And everywhere we see 7A, we see layer 9
2 on top of it, correct?
3 MR. SCHLITTER: Objection, form.
4 THE WITNESS: In Fig. 3C, that is
5 correct.
6 BY MR. GIBSON:
7 Q. And Fig. 3C shows the line 7A is going
8 to be extended toward the terminal portion?
9 A. Well, it shows it extending off the
10 illustration and then there's an additional arrow
11 showing us the direction of the terminal portion
12 on that side. It doesn't say whether it actually
13 extends out there.
14 Q. Would a person of ordinary skill in the
15 art understand that it would extend that way?
16 A. There's not enough information given to
17 know one way or the other. Well, on the other
18 hand, I mean, we have lots of terminal portions
19 disclosed here and clearly the metal 7 does not
20 extend all the way there in a continuous fashion.
21 It has to end at some point.
22 Q. But you're not sure at what point that
23 is?
24 A. It's not clear where that is, but what
25 is clear is it does have to end before it gets to

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1 the terminal portion according to the disclosure
2 in Sukegawa.
3 Q. If you look at 3E --
4 A. I see it.
5 Q. -- the scan line 2 is what's going from
6 the terminal region to the display portion.
7 Do you know where that is going to --
8 let me ask it this way: Is that going to go all
9 the way across through the display portion?
10 A. Element 2 is the conductor that is
11 illustrated in Fig. 3E that does go into the
12 display portion. I disagree that it is identical
13 to the scan line, as we talked about earlier.
14 But it is the single conductor, the only
15 one that does extend to the left towards the
16 display portion. So I think one of ordinary skill
17 would understand that at least that does go toward
18 the display portion.
19 Q. And if you matched it with 3C, would you
20 then understand that 2 would be the scan line?
21 A. Well, I wouldn't go quite that far. I
22 think if Fig. 3E were assumed to be connecting to
23 Fig. 3C, then I think it's appropriate to say that
24 element 2 in 3E does connect to element 2A in 3C.
25 Whether or not they should be characterized as all

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1 the same scan line, it's not clear. That
2 connection is not shown.
3 Q. Do you think that if you're connecting
4 3E to 3C, that 2 could be a data line?
5 A. Let me see if I understand your
6 question. You're asking me can the terminal in
7 Fig. 3E be used to connect to the data line?
8 Q. No. I'm asking you if you're connecting
9 3E to 3C if 2 is going to be the data line.
10 MR. SCHLITTER: Object to form.
11 THE WITNESS: If the terminal in 3E is
12 used to connect to the gate electrode 2A in
13 Fig. 3C, then it can be that that whole line could
14 be referred to as the scan line. It's not
15 necessary to be so, but that's certainly a common
16 configuration.
17 BY MR. GIBSON:
18 Q. I mean, would you expect that someone of
19 ordinary skill in the art would understand that
20 configuration when looking at Fig. 3C and 3E?
21 A. Well, one of ordinary skill would
22 understand that that's one possibility. I don't
23 think they would see the disclosure in Sukegawa as
24 requiring that. After all, element 2A is
25 identified differently, not only by numbering, but

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1 by description from element 2 and neither of them
2 are called scan lines.
3 Q. But you think that one of ordinary skill
4 in the art would recognize that it would be a
5 common configuration to have 2 be a scan line if
6 you were connecting Fig. 3 to Fig. 3C?
7 A. One of ordinary skill I think could find
8 that as a common, typical situation.
9 Q. If we look at 3A of Sukegawa.
10 A. I see it.
11 Q. And Sukegawa, I think, uses the term
12 "tape carrier package 300"?
13 A. Yes, that's correct. Of course that's
14 not shown in Fig. 3A.
15 Q. Would you understand that the tape
16 carrier package also could be a flexible printed
17 circuit?
18 A. As a general matter, yes. Those are
19 terms that are often used interchangeably.
20 Q. Would you understand that the flexible
21 printed circuit is going to overlap Fig. 3A?
22 A. Well, the tape carrier package that is
23 disclosed overlaps part of it. It's illustrated
24 in, for example, 3E, kind of the right portion of
25 what's shown in Fig. 3A.

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1 Q. When you say "the right portion," what
2 are you referring to?
3 A. Well, if we look at Fig. 3E, we see the
4 anisotropic conducting film 10 which extends from
5 the rightmost -- almost the rightmost portion of
6 the terminal region, not quite, but almost, most
7 of the way across the opening that's been formed
8 in insulating film 9.
9 And so if we look at the Fig. 3A, we can
10 see that the opening in element 9 is also shown
11 and so that anisotropic conducting film goes from
12 the right side of Fig. 3A only part of the way
13 through, at least approximately halfway through.
14 Q. And would you agree that when we're
15 looking at Fig. 3A, there's transparent conductive
16 film 8 where the flexible printed circuit can
17 overlap and then connect?
18 A. There is a portion in Fig. 3A, as well
19 as in 3E where the anisotropic conducting film is
20 in direct contact with the ITO and that is through
21 the opening in layer 9. That's not shown in
22 Fig. 3A, but we could identify that region. I
23 think I did in my declaration.
24 Q. And then in -- and the flexible printed
25 circuit is going to have a certain dimension,

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1 correct?
2 A. It's a physical object, so of course it
3 has dimensions.
4 Q. So even if you have plenty of available
5 contact area, you're limited by the size of the
6 wiring on the flexible substrate 31, for example?
7 A. Could you rephrase the question?
8 Q. Sure. I mean, regardless of how big
9 your contact area is in your terminal portion, the
10 contact -- the actual area where there's contact
11 is limited by the size of what you have as 31?
12 A. I'm not trying to be difficult, but I
13 don't understand the question.
14 Q. Okay. Well, you see -- in 3D you see
15 31A and 31B?
16 A. Yes, I do.
17 Q. What would you call those two things?
18 A. Well, element 31 is the flexible wiring
19 substrate of the tape carrier package 300 and 3 --
20 31B is the copper foil wirings. I don't see yet
21 what 31A is. I could find that, but clearly the
22 conductive portion is 31B.
23 Q. And you say that's part of the flexible
24 substrate?
25 A. It appears that both 31A and 31B

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1 together form element 31 and that is the flexible
2 wiring substrate.
3 Q. And you're going to connect that to your
4 terminal portion?
5 A. It's shown in Fig. 3E, for example, as
6 connecting through the anisotropic conducting film
7 to the conductors in the terminal portion.
8 Q. And the film is element 10?
9 A. Yes, thank you.
10 Q. And your contact area is going to be
11 limited by the size of the wiring on the flexible
12 substrate, correct?
13 MR. SCHLITZER: Objection, form.
14 THE WITNESS: That will be one of the
15 limitations in this context, but there are many
16 others.
17 BY MR. GIBSON:
18 Q. That would be one?
19 A. That would certainly be one.
20 Q. And if the flexible printed circuit is
21 smaller than the available contact area, would you
22 agree then the area -- well, let me do it this
23 way.
24 Why don't you turn to page 40 of your
25 declaration?

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1 A. I've got it.
2 Q. Which I know in your original it's in
3 color. There's a -- I think here it's shown in a
4 darker shade of gray, which you had in red in your
5 declaration.
6 Do you see that darker shade?
7 A. Well, the darker shade, of course, is
8 indicating the opening in layer 9 through which
9 the ITO is exposed for contact.
10 Q. That's your contact area?
11 A. That's a subset of the contact area most
12 likely.
13 Q. Why is it only a subset?
14 A. Most likely the anisotropic conducting
15 film overlays much more than just that little
16 area.
17 Q. Where is it actually connecting then, to
18 that area?
19 A. Well, direct contact could be outside
20 that opening and -- but electrical contact would
21 be just in that opening.
22 Q. And if the flexible printed circuit
23 substrate is smaller than that available area,
24 that could be a possibility, correct?
25 A. Well, I think it's unlikely. I think

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1 the way design -- the design process generally
2 goes at least would be that a particular size of
3 the terminal is determined based on other
4 constraints and then a tape carrier package or
5 flexible substrate is chosen or designed to meet
6 exactly what's needed in that terminal portion.
7 Q. But let's assume that you've got a
8 flexible substrate that's smaller than the
9 available area.
10 A. I can assume that, sure.
11 Q. Would you agree then that the shaded
12 area is not limiting the resistance?
13 A. In this unlikely assumption and
14 hypothesis, I can agree to that.
15 Q. Then let's assume if the flexible
16 printed circuit is larger but the resistance of
17 the contact formed is within the specifications of
18 the system, would you then agree that the shaded
19 area is not limiting the performance of the LCD
20 display?
21 MR. SCHLITZER: Object to form.
22 THE WITNESS: I can't agree to that in
23 general, even under your assumptions.
24 BY MR. GIBSON:
25 Q. Okay. In the event that the shaded area

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1 is limiting the resistance, couldn't one of
2 ordinary skill in the art increase its size?
3 A. That is one option available to those of
4 ordinary skill but, of course, that then makes the
5 entire terminal portion larger and, of course,
6 there are other terminals next to this that aren't
7 illustrated and there's a -- there's a limit to
8 that.
9 Q. Let's take a look at Watanabe.
10 A. Thank you.
11 (Document marked previously as Exhibit
12 Number 1004 was presented.)
13 BY MR. GIBSON:
14 Q. And this is another one of the prior art
15 pieces that you considered?
16 A. Yes, it is.
17 Q. Do you know how wide a typical seal
18 region of an LCD is?
19 A. There is no one answer to that.
20 Q. What would be typical?
21 A. In -- in 1997, I think a typical region
22 would be in the range of millimeters.
23 Q. Can you give me an estimate?
24 A. It depends on the display.
25 Q. But millimeters, not centimeters?

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1 A. I think for the larger sized displays,
2 it could even get large enough to be close to the
3 scale of a centimeter, not multiple centimeters,
4 clearly not.
5 Q. And would that be true in 1997?
6 A. Yeah, I think that the upper bound for
7 the size of a seal region would be -- could be as
8 high as tens of millimeters but not that many tens
9 of millimeters.
10 Q. Do the adhesive properties of the seal
11 material affect the weight -- the width of the
12 seal region?
13 A. They certainly do. And Shiba, for
14 example, goes to some effort to complement those
15 properties by structuring the bottom surface so
16 that it has a little more surface area and thereby
17 use the same set of materials but shrink the width
18 of the seal region and not compromise the
19 adhesion.
20 Q. And so if you have a stronger adhesive,
21 it requires less width?
22 A. That may be, but subject to other
23 constraints.
24 Q. Such as?
25 A. Well, from one material to another, a

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1 seal may be stronger or not. And let's say we
2 have two materials that are both equally strong in
3 their adhesion properties, but there are other
4 factors in displays that are very important.
5 First would be lifetime behavior -- or I
6 should say reliability to temperature variations
7 in the lifetime of the display and that may be
8 different, quite apart from the initial adhesive
9 properties of the two sealants, and there are
10 other considerations like that.
11 Q. All right. But you would agree that in
12 general, a stronger adhesive requires less width?
13 A. In general I can agree to that.
14 Q. And would you give -- would you agree
15 that a given seal material has an optimum width
16 that can be established?
17 MR. SCHLITTER: Objection, foundation.
18 THE WITNESS: It depends on how you're
19 defining "optimum."
20 BY MR. GIBSON:
21 Q. A width that is neither too wide nor too
22 small.
23 MR. SCHLITTER: Same objection.
24 THE WITNESS: If you define it that way,
25 then you can -- you can -- a person of ordinary

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1 skill could find the optimum according to those
2 constraints, whatever you want to define those
3 for.
4 BY MR. GIBSON:
5 Q. Well, for example, you don't want to use
6 more sealant material than you need, right?
7 A. As a general principle, I can agree with
8 that.
9 Q. And you don't want to use less than you
10 need, that wouldn't have good results?
11 A. As a general principle, I can agree with
12 that.
13 Q. If you look at -- I think it's page 55
14 of your declaration.
15 You have a couple figures there and
16 those are coming out of Watanabe?
17 A. Yes, the figures on page 55 are
18 reproductions, without modification as far as I
19 can tell. Oh, I think in my declaration, I may
20 have highlighted something in them, but they are
21 Fig. 9 and Fig. 5 from Watanabe.
22 Q. And I think regarding Fig. 5 -- I
23 thought you made a statement regarding Fig. 5.
24 Let me -- it's on the previous page. If you go
25 ahead and read that to yourself.

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1 MR. SCHLITTER: What page are you
2 referring to?
3 MR. GIBSON: Page 54.
4 THE WITNESS: Yeah, I've reviewed it.
5 BY MR. GIBSON:
6 Q. All right. So the sentence that begins
7 "Fig. 5 below shows," what do you mean by that
8 statement?
9 A. Well, there's two sides to what I'm
10 meaning there. First, I'm observing that when
11 compared to the prior art that's disclosed in
12 Watanabe, Fig. 5 has a substantially wider seal
13 region by a factor of 8 or so, maybe 5. All
14 right. That's just what's illustrated in the
15 figures.
16 In addition, I'm observing that -- I
17 also mean to say that central to the -- to the
18 disclosure in Watanabe is the presence of the
19 adjustment layers or adjusting layers -- let me
20 get the term right -- the adjustment layers in the
21 sealing region around the lead portions. And by
22 doing so, what he's of course trying to do is
23 create a more equal gap between the substrates.
24 And when compared to the prior art, of
25 course there's a less equal gap in the prior art.

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1 And so if we look at Shiba, for example, Shiba
2 improves his adhesion by having peaks and valleys,
3 an uneven surface, which in Fig. 5 Watanabe is
4 removing or at least minimizing and decreasing by
5 the presence of those gap adjusting layers. So to
6 compensate for that, it seems that he is forced to
7 widen the sealant region.
8 Q. All right. So is it your testimony then
9 that the width of the sealing region in Watanabe
10 is -- it doesn't depend upon the seal material,
11 but rather by the design of the gap adjusting
12 layers?
13 A. That's not my testimony.
14 Q. Well, what you said here was, Watanabe
15 is removing or at least minimizing and decreasing
16 the presence of the gap adjustment -- adjusting
17 layers, so to compensate for that, it seems that's
18 forced to widen the sealant region.
19 And what I'm trying to understand is are
20 you -- so you're saying he's widening the sealing
21 region because he's using these gap adjusting
22 layers?
23 A. That's what he's showing. He's -- he's
24 not just illustrating the sealant region as being
25 wider for the convenience of his illustrating.

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1 He's widening it because he has removed unevenness
2 from the seal region and, therefore, he needs a
3 wider seal to get the same adhesive strength even
4 with the same materials as compared to the prior
5 art that he has.

6 Q. Is there any statement that you're
7 basing this on or is it just on Fig. 5?

8 A. I'm recognizing what's true about his
9 disclosure in Fig. 5 as compared to Fig. 9. I
10 don't think he states this, but I'll note that
11 it's also exactly the same principle that Shiba
12 uses to narrow the seal region.

13 Q. All right. But is there anything
14 where -- is there anything that -- other than
15 Fig. 5 and your comparison to Fig. 9 of the prior
16 art, is there anything else that you have to
17 support your statement?

18 A. Well, in addition to that would be the
19 other examples and embodiments that he has of
20 adjustment layers and all of them have a wider
21 seal region. It seems to be every embodiment has
22 a wider seal region than the prior art that he's
23 showing.

24 Q. What are you referring to in particular?

25 A. Well, I'm referring to -- we were just

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1 talking about Fig. 5 and -- in compared with
2 Fig. 9. Fig. 9 is the prior art and it does not
3 have gap adjusting layers and clearly, Watanabe is
4 saying that that has a more uneven lead portion
5 surface underneath the sealant.

6 So Fig. 5 adds those adjustment layers
7 and the unevenness is reduced and as a
8 consequence, we also see that the seal region is
9 wider. And that general trend is also true as
10 well in Fig. 1, Fig. 6, Fig. 8A which has a seal
11 region that is not a constant width, so it's a
12 little more nuanced, but they all have a wider
13 seal region than the prior art.

14 Q. And you're observing that from just
15 looking at the proportions of the figures where
16 the sealing region is shown compared to the prior
17 art?

18 A. I'm doing more than that. As I've just
19 explained, there's a technical reason why that
20 should be.

21 Q. All right. Is that technical reason
22 explained by Watanabe?

23 A. To some extent it is explained. He's
24 clearly reducing the unevenness of the bottom
25 substrate that's central to his invention.

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1 Q. Where does it say that he will then
2 require wider space for the sealant?

3 A. He shows a wider space in all of his
4 inventions.

5 Q. Where does he say that by decreasing the
6 unevenness, he has to have a wider space for the
7 sealant?

8 A. I don't recall that he says something to
9 that regard. He discloses it in his
10 illustrations.

11 Q. But there's nothing disclosed in the --
12 in the text itself that this wider sealing range
13 -- this wider sealing area is required?

14 A. There is not a discussion of that in the
15 specification, but the principle is -- is true
16 nonetheless. If he wants to keep the same seal
17 strength and he has removed unevenness, then he
18 will need a larger width to a seal region as he's
19 illustrating.

20 Q. What if he just uses stronger sealant?

21 A. That might be done.

22 Q. He doesn't rule out using a stronger
23 sealant?

24 A. He doesn't speak about changing
25 sealants. He's silent on that.

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1 Q. Are there any quantitative measurements
2 that you're using to form this conclusion?

3 A. What do you mean by "quantitative
4 measurements"?

5 Q. Dimensions, are there any dimensions
6 that you're using?

7 A. I'm observing the illustrations from the
8 figure and recognizing the effect of his invention
9 on the evenness of the bottom substrate.

10 Q. And the figures themselves don't have
11 dimensions, correct?

12 A. There's no scale provided for them, no.

13 MR. GIBSON: If we could change the
14 media and we'll take a break.

15 VIDEOGRAPHER: Going off record. This
16 is the end of Media Unit Number 3. The time is
17 3:15.

18 (Short recess.)

19 VIDEOGRAPHER: We're back on record.
20 This is the beginning of Media Unit Number 4 in
21 the deposition of Dr. Michael Escuti. The time is
22 3:31. Please continue.

23 BY MR. GIBSON:

24 Q. And before we broke, we were looking at
25 some of the figures from Watanabe.

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1 Would you agree that these figures are
2 frequently not drawn to scale?
3 A. Are you referring to the figures in the
4 Watanabe patent?
5 Q. The figures in patents in general, do
6 you agree they're frequently not drawn to scale?
7 A. In general, that's -- that's true. I
8 would -- I would agree that most commonly scale is
9 not provided in patent figures.
10 Q. And do you have any reason to believe
11 that these are drawn to scale?
12 A. Well, based on the absolute dimensions,
13 they're not likely drawn to scale. After all, the
14 dimensions of the seal regions in all of this are
15 substantially similar to the area of the nine
16 pixels that are disclosed and, of course, there's
17 more than nine pixels in the displays that would
18 typically be imagined here.
19 But the -- but most of my comments we
20 just discussed about refer to the relative
21 comparison between the figures, not the absolute.
22 Q. And the comparison you're making between
23 the figures, is that what you're talking about,
24 Fig. 9 to Fig. 5, for example?
25 A. Well, for example, when comparing Fig. 5

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1 to Fig. 9, the prior art, the seal region relative
2 to the other dimensions is substantially smaller
3 as illustrated and the seal in Fig. 5 is larger as
4 illustrated.
5 Q. Would you expect that Fig. 9 is drawn to
6 scale?
7 A. I don't suspect that any of these
8 figures are drawn to an absolute scale.
9 Q. Would you understand that artwork such
10 as this in patents is designed to describe certain
11 features or to show distinctive features?
12 MR. SCHLITTER: Objection, foundation,
13 form.
14 THE WITNESS: Well, that's my
15 understanding, that the figures in patents are
16 meant to convey some kind of relationship between
17 the elements and not an engineering drawing.
18 BY MR. GIBSON:
19 Q. And they don't show all the -- usually a
20 figure doesn't show all the elements?
21 A. It's quite common that a figure does not
22 show all the elements.
23 Q. And particularly in art such as this
24 where there are so many elements that if you tried
25 to show them all, you probably wouldn't have a

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1 figure that you could understand?
2 A. In many cases, that is true. Of course,
3 that's not always true, but yes.
4 Q. Would you agree that in Fig. 5, the
5 distinctive features are trying to show the
6 absence of substrate gap adjusting regions?
7 MR. SCHLITTER: Objection, form.
8 THE WITNESS: Maybe you mean -- you
9 should rephrase.
10 BY MR. GIBSON:
11 Q. You mean the presence?
12 A. Well, could you just restate the
13 question for me? I don't understand it.
14 Q. All right. Fig. 5 is trying to show
15 something regarding substrate gap adjusting
16 regions, correct?
17 MR. SCHLITTER: Objection, form.
18 THE WITNESS: Well, Fig. 5 is referred
19 to as a plan view showing a liquid crystal display
20 apparatus according to the second embodiment of
21 the present invention. There are gap adjusting
22 layers identified, 25 and 27, in that figure.
23 BY MR. GIBSON:
24 Q. And those are discussed in the text of
25 the specification, correct?

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1 A. Elements 25 and 27 are indeed discussed
2 in the text.
3 Q. And if you look at 3A and 3B --
4 actually, let's look at -- I meant to look at
5 '204, 3A and 3B.
6 A. So I've got 3A and 3B of the '204 before
7 me.
8 Q. All right. And those are -- 3A is
9 showing an adjustment layer 301?
10 A. 3A shows multiple adjustment layers 301.
11 Q. And 3B calls 301 an adjustment film, but
12 you would understand that to be an adjustment
13 layer?
14 A. I understand that those are the same
15 element and I don't think there's a difference
16 between the adjustment layer terminology and
17 adjustment film terminology.
18 Q. And would you agree that they're located
19 next to the external connection lines?
20 A. One of the adjustment layers is adjacent
21 to the external connection lines but, of course,
22 there are many others that are not. The second
23 one is to the left of the first adjustment layer.
24 Q. Okay. But there is one adjustment layer
25 shown next to the external connection line in both

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1 3A and 3B?
2 A. There is one shown that is next to the
3 first external connection line.
4 Q. And if you look at 6A and 6B of the '204
5 patent.
6 A. I see it.
7 Q. These show adjustment layers 501 and
8 502, is that correct?
9 A. It does, both 6A and 6B include those
10 two elements.
11 Q. And they're both below and next to the
12 external connection lines?
13 A. Some of them are below and some are to
14 the side and external to it.
15 Q. Where are the first and second
16 conductive layers in the '204 patent?
17 A. In these figures?
18 Q. Or with reference to these figures.
19 A. Well, by the terms first and second
20 conductive layers, are you referring to Claim 54
21 claim terms?
22 Q. That's fine. You can use Claim 54.
23 A. Well, I think that the first
24 conductive -- what's the language? Okay. The
25 first conductive line over the substrate also then

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1 has other limitations on it and that is not in
2 Fig. 6A and B. But I can, of course, identify
3 that there are two metal layers in 6A and 6B that
4 are patterned for different purposes.
5 Q. Okay. What are those?
6 A. Well, the lower conductive layer that's
7 shown in 6A and 6B is used to form multiple first
8 adjustment layers 501, and the second conductive
9 layer that's been deposited and patterned forms
10 both the external connection lines 108 and the 502
11 second adjustment layers.
12 Q. Do those overlap with the sealant?
13 A. In 6A and 6B, all of those elements are
14 underneath the sealant.
15 Q. And what would you understand the
16 purpose of the first and second conductive layers
17 to be?
18 A. Do you mean in these figures 6A and 6B?
19 Q. We can start with that.
20 A. Well, I'll have to refresh my memory on
21 the figures in the specification. In Figs. 6A and
22 6B, a cross-section of a -- the cross-section of B
23 to B prime, which appears most likely somewhere
24 else in a plan view of the display -- I'm not sure
25 where, we can find it -- but it appears that the

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1 two conductors that are labeled the external
2 connection line are an external connections to
3 something external to the sealant, whereas all the
4 other adjustment layers that are conductors serve
5 to adjust the height difference.
6 Q. Isn't their purpose to create a uniform
7 gap?
8 A. The adjustment layers would have that
9 purpose, yes.
10 Q. And would you understand that to be the
11 purpose in Claim 54 as well?
12 A. The purpose of what in Claim 54?
13 Q. To conduct -- the first and second
14 conductive layers?
15 A. In Fig. 6A and 6B, I don't see a
16 structure that meets the limitations Claim 54.
17 Q. But Claim 54 does refer to a first and
18 second conductive layer, correct?
19 A. It does, of course with many limitations
20 on it.
21 Q. What's your understanding of the purpose
22 of those layers?
23 A. Are you referring to the layers that are
24 in 6A or are you referring to the first and second
25 conductive layers in Claim 54?

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1 Q. The latter.
2 A. The claim doesn't say what the purpose
3 of those are. It speaks to the relative
4 relationship of the first and second conductor and
5 the connection between them and what's above them
6 and regions of -- of them, but doesn't say what
7 the purpose of them is.
8 Q. What would one of ordinary skill in the
9 art, after reading the '204 patent, learn would be
10 the purpose of having those two layers?
11 MR. SCHLITTER: Objection, form.
12 THE WITNESS: In Claim 54, one
13 possibility is what's illustrated in Fig. 4A,
14 where there's a connection that goes across the
15 sealant toward the display portion. So they serve
16 to connect the terminal to something inside the
17 display.
18 BY MR. GIBSON:
19 Q. Any other purpose?
20 A. That's one example. There certainly
21 could be other purposes.
22 Q. What would they be?
23 A. Can you rephrase the question for me to
24 be maybe more specific?
25 Q. Yes. So what would one of ordinary

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1 skill in the art understand from reading the '204
2 patent would be the purpose of the first and
3 second conductive layers in Claim 54?
4 MR. SCHLITTER: Objection, form.
5 THE WITNESS: Another purpose would be
6 to reduce the unevenness in the gap, whether or
7 not they extend it all the way through the sealant
8 and into the display portion.
9 BY MR. GIBSON:
10 Q. Any other purpose?
11 A. Not that I can think of right now.
12 Q. If we look back at Watanabe, Watanabe
13 Fig. 5.
14 A. Yes.
15 Q. There's gap adjusting layers 25 and 27?
16 A. I see them.
17 Q. And what is the purpose of those?
18 A. The purpose of those gap adjusting
19 layers is to provide an equal gap between the two
20 substrates, ultimately to improve display image
21 quality and display image contrast as he says in
22 Column 3.
23 Q. Where in a typical LCD display is the
24 sealant located?
25 A. In the '204 patent, there are multiple

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1 figures that -- that show it, Fig. 5, for example,
2 and I think that's fairly emblematic. It's
3 typically around most of the periphery of the
4 display, but there needs to be an opening, at
5 least one opening -- sometimes there's many
6 openings -- so that the liquid crystal can be
7 filled after the seal has been applied and the two
8 substrates have been joined.
9 Q. And would you understand that the
10 substrate gap adjusting layers 25 and 27 overlap
11 with the sealant in Watanabe?
12 A. In Fig. 5, they completely overlap. In
13 other figures they partially overlap.
14 Q. And so you would agree that typically
15 the sealant is along the edge of the display; it's
16 not, for example, in the middle of the display?
17 MR. SCHLITTER: Object to form.
18 THE WITNESS: It is typically on the
19 periphery or edge of the display.
20 BY MR. GIBSON:
21 Q. And if you could look at Fig. 3A of the
22 '204 patent.
23 A. I see it.
24 Q. You see there's again adjustment layer
25 301?

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1 A. I see it.
2 Q. And that's located across the sealant?
3 A. As illustrated, those adjustment layers
4 do extend from one side to the other and also
5 extend away from the terminals.
6 Q. And they're in parallel to the external
7 connection lines?
8 A. In this portion it is, but clearly from
9 looking in Fig. 5, that's not the case everywhere.
10 Q. But in 3A, that's what we're seeing, a
11 parallel adjustment layer and external connection
12 lines?
13 A. In 3A, it shows the adjustment layers
14 that are at least illustrated in parallel with the
15 external connection lines.
16 Q. And in 6A, which we looked at a minute
17 ago, the two adjustment layers 501 and 502 are
18 located across the sealant?
19 A. They do extend from one side of the
20 sealant to the next and underneath.
21 Q. And they're also in parallel to the
22 external connection lines?
23 A. While 501, the first adjustment layers
24 are in parallel, 502 is not. 502 is illustrated
25 as orthogonal.

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1 Q. If we look at Shiba.
2 A. Which part?
3 Q. Oh, Fig. 3. I don't think we've talked
4 a lot about Fig. 3 yet. We wouldn't want to leave
5 that one out.
6 Are the signal lines identified as X1,
7 X2, X3, X4?
8 A. No.
9 Q. What lines do you believe those are?
10 A. Those are referred to as the data lines,
11 for example, Column 5, line 5.
12 Q. Would you understand that one of
13 ordinary skill in the art might use data and
14 signal lines?
15 A. Yes. I think I'm getting tired and --
16 and didn't hear them as essentially the same thing
17 to a person of ordinary skill, but yeah, I agree
18 that they -- they can be used interchangeably.
19 Q. But I will agree with you that the
20 patent does call them data lines.
21 A. I didn't mean to be so abrupt, but it's
22 been a long -- long few hours.
23 Q. I understand.
24 The data lines, would you agree that
25 they're located across the sealing region,

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1 region 11?

2 A. In Fig. 3, they're clearly illustrated
3 as going from the terminals across the sealant
4 region and into the display portion or the -- I
5 guess it's called the display area in Shiba.

6 Q. And if you look at Fig. 1, do you see
7 the data lines in Fig. 1?

8 A. I don't see the lines themselves,
9 they're not illustrated X1, X2, X3, but there are
10 the elements 7-11 through 7-18 that hold the
11 connections to the data lines and immediately
12 above them on the substrate would be the data
13 lines.

14 Q. So they're fed into the display from the
15 bottom of Fig. 1?

16 A. That's -- that's generally correct, yes.

17 Q. And how do the data lines get their
18 signal in Shiba?

19 A. Do you mean what connects to the
20 terminal regions around the data lines?

21 Q. Right. Isn't there a driver?

22 A. There may be. I don't recall. I'd have
23 to look to find out. Maybe you can point me to
24 where that is. There is a driver board mentioned
25 in, for example, Column 5. I don't think it's

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1 illustrated or numbered.

2 Q. Would you understand that driver board
3 would be at the bottom of Fig. 1?

4 A. The driver board could be -- could be
5 arranged somewhere near the bottom of Fig. 1.
6 It's not disclosed and I think there are many ways
7 to do it. Certainly one could put it off to the
8 side and then connect it up that way. But I think
9 a very common way to do it would be to have the
10 driver board at the bottom or near the bottom of
11 Fig. 1.

12 Q. And in Fig. 1, would you understand
13 these data lines to go across the sealant region
14 the same way it's shown in Fig. 3?

15 A. I'm sorry. I should correct myself just
16 very briefly. There are identified the X driver
17 circuit boards 800 and the Y driver circuit boards
18 900, and it's that wide rectangle that's at the
19 bottom and the right side respectively of Fig. 1.
20 So they are numbered. They are illustrated.

21 Q. Okay. Appreciate that.

22 So the data lines that are going into
23 Fig. 1 from the bottom, would you understand those
24 would go across the sealant region the same way as
25 shown in Fig. 3?

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1 A. Fig. 3 is, after all, an expansion of
2 the box labeled A in Fig. 1 and what's true in
3 Fig. 3 should be true in Fig. 1 as well for that
4 portion.

5 Q. And in Fig. 1, do you see where the scan
6 lines are?

7 A. I see where their driver is and where
8 their wirings in those big blocks are, 7-21 to
9 7-24, but I don't see the lines themselves.
10 They're not illustrated.

11 Q. Would you agree the scan lines are going
12 to be fed into the display from the right-hand
13 side of Fig. 1?

14 A. I do agree, in a manner that's at least
15 similar and analogous to Fig. 3, they would extend
16 from the wiring film and go underneath the sealant
17 toward the display area. Although, the one
18 important difference is they would also have to
19 cross the wiring 127.

20 Q. Right, because we don't have the wiring
21 127 at the bottom of the rectangle?

22 A. That's correct because it would short
23 all those lines.

24 Q. But you would expect the scan lines to
25 go in from the right-hand side and be connected to

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1 the drivers you pointed out, 7-21 to 7-24?

2 A. Yes, that's correct.

3 Q. Are the -- looking at the bottom side of
4 Fig. 1 and the right side of Fig. 1 where the
5 lines are going in, how are they positioned
6 relative to the sealant?

7 A. I'll try to answer your question, but
8 you may need to rephrase it. The data lines and
9 the scan lines extend across the sealant in a way
10 that might be characterized as orthogonal to it,
11 not parallel to the sealant.

12 Q. Could you call it transverse?

13 A. I think that's an alternate way to
14 express it, yeah.

15 Q. And if you look at Fig. 3 again of
16 Shiba, you can see wiring lines 127?

17 A. Yes, I see that. That would be an
18 example of something that's not transverse.

19 Q. They would be parallel to the sealant?

20 A. Most of wiring 127 is largely parallel
21 to the sealant, yes. Of course there are portions
22 where that's not true, but they're very small.

23 Q. And would you -- do you see that those
24 wiring lines 127 cover the region 111 of the
25 sealant in places?

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1 A. I might reverse the order of that. The
2 seal region 111 covers a substantial portion of
3 wiring 127, at least four of the lines. Two of
4 the lines are, of course, inside the seal region
5 and not under it.
6 Q. And then there's several that are under
7 it?
8 A. There are several that are under it and
9 those are pictured in Fig. 6.
10 Q. And then would you agree that there are
11 portions of the sealant region that have no wiring
12 lines 127?
13 A. I do agree. For example, in Fig. 1, the
14 entire bottom horizontal portion of the seal does
15 not have wiring 127 under it.
16 Q. But even in Fig. 3, you can see areas
17 where there's sealant and no wires, correct?
18 A. Yes.
19 Q. And looking back at Watanabe, would you
20 agree that Watanabe discloses an adjustment layer?
21 A. To be precise, he calls it a gap
22 adjusting layer and he discloses several kinds.
23 Q. Is that the gap adjusting layer
24 electrically isolated from the auxiliary line?
25 MR. SCHLITTER: Objection, form --

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1 THE WITNESS: I don't --
2 MR. SCHLITTER: -- and foundation.
3 THE WITNESS: I don't think that
4 Watanabe references an auxiliary line at all.
5 BY MR. GIBSON:
6 Q. Is the -- you say there's multiple gap
7 adjustment layers.
8 Do you agree there's at least two in
9 Watanabe?
10 A. There's at least two designs of those
11 adjusting layers.
12 Q. And you have a 25 and a 27 in Watanabe,
13 correct?
14 A. In Fig. 5, there's adjusting layer 25
15 and 27, and they correspond to those that are near
16 the scan and data lines respectively.
17 Q. Is 25 electrically isolated?
18 A. All of the adjusting layers in Fig. 5
19 are said to be electrically isolated from the lead
20 portions that they're next to.
21 Q. They're electrically isolated from the
22 TFTs?
23 A. That's correct.
24 Q. And they're electrically isolated from
25 the flexible printed circuit?

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1 A. Yes, that's correct.
2 Q. Would you agree that the gap adjusting
3 layers that are described in Watanabe can also be
4 made from the same material that's used to form
5 the signal lines?
6 MR. SCHLITTER: Objection, form and
7 foundation.
8 THE WITNESS: Well, Column 12 and
9 beginning in line roughly 49 and then that
10 following paragraph discusses that issue and I
11 think maybe more generally says that the material
12 of the substrate gap adjusting layers 25 and 27 is
13 the same, in this embodiment at least, as the
14 material used in the first embodiment. And then
15 he goes on to clarify it could be used as the same
16 material as the signal lines and so on.
17 BY MR. GIBSON:
18 Q. So you would agree?
19 A. That's -- I do agree that's what's
20 disclosed in Watanabe.
21 Q. So let's look back at Shiba for a
22 moment.
23 In Fig. 4 where you identified Item 741,
24 that's on the counter substrate, correct?
25 A. Yes, the connecting protrusion 741 and

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1 its brothers and sisters are on the counter
2 substrate.
3 Q. And that's where you say the sealant is
4 contacting an ITO layer is on the counter
5 substrate, is that correct?
6 A. I am saying that, but it's not limited
7 to that. As you can see, 741 is part of what it
8 -- what the sealant 113 contacts. But as the
9 layer -- the ITO layer comes up -- well, I should
10 say it comes diagonally down and then across the
11 display, it's -- it's labeled 541. So it touches
12 both of those regions.
13 Q. But that's on the counter substrate
14 side?
15 A. It is all on the counter substrate side.
16 Q. And there is no sealant touching the ITO
17 on the substrate side?
18 A. Not that's disclosed in Shiba.
19 Q. And if you look at Fig. 6, the sealant
20 that you're saying is touching an ITO layer is
21 again on the counter substrate?
22 A. In Fig. 6 it's even more clear and yes,
23 it's the counter substrate that has the ITO
24 directly touching at least partially the sealant.
25 Q. And the sealant at the -- on the

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1 substrate is not touching the ITO layer, correct?
2 A. Well, just to be clear, the other
3 substrate we're talking about is element 200 in
4 this answer and the question previously and that's
5 called the array substrate. So yes, I agree the
6 sealant is not touching the ITO on the array
7 substrate or element 200.
8 Q. Now, if you look at the '204 patent, if
9 we look at Claim 54.
10 A. I've got it.
11 Q. And in Claim 54, we're talking about the
12 array substrate when we look at the first use of
13 the word "substrate"?
14 A. If the substrate has thin film
15 transistors and pixel electrodes and it is an LCD
16 device, then yes, it would correspond to the array
17 substrate in Shiba.
18 Q. All right. And we know in the very
19 bottom line on Column 19, it says a counter
20 substrate facing a substrate.
21 Do you see that?
22 A. Yes, I do.
23 Q. Is there any other mention about what
24 the counter substrate is going to be -- strike
25 that.

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1 Is there any mention in the -- in
2 Claim 54 about what is going to be on the counter
3 substrate?
4 A. Claim 54 is silent on that.
5 Q. So the limitations about what's on the
6 substrate -- strike that.
7 The limitations that follow in claim --
8 in Column 20 apply to what is on the array
9 substrate, correct?
10 A. Except for the first limitation, I think
11 that's correct, at least for Claim 54.
12 Q. And the limitation you're referring to
13 is a liquid crystal material provided between the
14 substrate and the counter substrate?
15 A. That's correct.
16 Q. So we know there's going to be a liquid
17 crystal material in between the two substrates
18 from that limitation, correct?
19 A. From that limitation, of course the
20 first one where it's called the liquid crystal
21 display device.
22 Q. All right. And then everything after
23 that is applying to what's on the array substrate,
24 correct?
25 A. It applies to what's referred to here as

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1 the substrate.
2 Q. Which we would understand to be an array
3 substrate based on the fact that it's got a TFT
4 and pixel electrodes and it's for an LCD display,
5 correct?
6 A. Well, that's not the language of this
7 specification.
8 Q. I understand. But someone of ordinary
9 skill in the art would understand that we're
10 talking about the array substrate there, not the
11 counter substrate?
12 A. Well, the language of array substrate is
13 what's used in Shiba. I'm simply holding to the
14 language that the spec in -- in the '204 which
15 refers to these two substrates as simply a
16 substrate in Claim 54 which, yes, would include
17 the thin film transistors and the active matrix
18 electronics and the counter substrate, which would
19 be the other one. And certainly nearly all of the
20 limitations in Column 20 apply to the substrate
21 which would have the active matrix on it.
22 Q. And would that also be the same for
23 Claim 31?
24 A. I'll have to verify.
25 Claim 31 includes in its first few

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1 limitations a very similar set that refers to the
2 substrate having thin film transistors and a
3 counter substrate facing that first substrate. So
4 what we just talked about in Claim 54 I think
5 applies to Claim 31.
6 MR. GIBSON: All right. Why don't we
7 take a break? I want to check my notes and then
8 I'll probably wrap up.
9 VIDEOGRAPHER: We're going off record.
10 The time is 4:13.
11 (Short recess.)
12 VIDEOGRAPHER: We're now back on record.
13 The time is 4:24. Please continue.
14 MR. GIBSON: I don't have any further
15 questions at this point, but I'll reserve my right
16 to ask additional questions if you ask questions.
17 MR. SCHLITTER: Okay. I have a topic.
18 EXAMINATION
19 BY MR. SCHLITTER:
20 Q. So would you please refer to the '204
21 patent?
22 A. I've got it.
23 Q. Do you recall what the objectives are of
24 the invention that's described in the '204 patent
25 or any of the inventions that's described in the

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1 '204 patent?

2 A. In the '204 patent, there's multiple

3 objectives and one of them certainly is to provide

4 a reduced resistance from the terminal portion to

5 the display portion.

6 Q. Is there any other?

7 A. There's a -- another that involves the

8 objective of reducing the gap unevenness by means

9 of the adjustment layers that are provided.

10 Q. Any other objectives that you can

11 recall?

12 A. There are two other objectives, as best

13 I recall. One of them is to provide a strong

14 adhesion of the sealant to the lower substrate.

15 And the last objective I can recall is to provide

16 a reliable connection to the FPC in the terminal

17 portion.

18 Q. Would you refer, please, to Claim 31?

19 A. I've got it.

20 Q. Do you see that one of the limitations

21 in Claim 31 is an auxiliary line?

22 A. I do see in Claim 31 the auxiliary line

23 limitation.

24 Q. And another limitation is an external

25 connection line?

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1 A. Yes, and there's several limitations on

2 that external connection line.

3 Q. What is the objective of the '204 patent

4 with respect to the adjustment -- strike that.

5 What is the objective of the '204 patent

6 with respect to the auxiliary line and the

7 external connection line?

8 MR. GIBSON: Objection, scope.

9 THE WITNESS: Well, the objective in

10 that case has to do with the -- reducing the

11 resistance of the connection from the terminal

12 portion at least partially into the sealant but

13 potentially beyond.

14 BY MR. SCHLITTER:

15 Q. What -- are there any limitations in

16 Claim 31 that relate to the objective of lowering

17 resistance?

18 A. Well, those -- those two lines and the

19 connection that's formed between them, the

20 electrical connection, would serve that objective.

21 Q. Would they serve any other objective?

22 MR. GIBSON: Objection, scope.

23 THE WITNESS: They may. They also

24 present a difference in height under the seal

25 region and so they could provide a given height in

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1 that region and that could be serving one of the

2 other objectives I mentioned.

3 BY MR. SCHLITTER:

4 Q. You also mentioned unevenness. Is there

5 any limitation in Claim 31 that relates to the

6 objective of preventing unevenness of the gap?

7 A. Yes, certainly. There's an adjustment

8 layer that's identified and that adjustment layer

9 must extend under the sealant and that goes to

10 that objective.

11 Q. Are there any limitations in Claim 31

12 that relate to the objective of providing strong

13 adhesion of the sealant?

14 MR. GIBSON: Objection, scope.

15 THE WITNESS: Yes, there is. There's a

16 limitation that says the sealant is in direct

17 contact with the second insulating film and so

18 that's toward that objective.

19 BY MR. SCHLITTER:

20 Q. Is there any limitation -- well, are

21 there any other limitations in Claim 31 that

22 relate to the objective of stronger adhesion?

23 MR. GIBSON: Objection, scope.

24 THE WITNESS: Well, at least indirectly,

25 the transparent conductive film is expressly not

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1 in contact with the sealant. There's a limitation

2 that says the flexible printed circuit over an

3 electrical contact with the external connection

4 line through a transparent conductive film.

5 So there is a transparent conductive

6 film involved and the next limitation that I

7 already read about, the sealant, dictates that it

8 should not -- it should not be under the seal.

9 BY MR. SCHLITTER:

10 Q. Are there any limitations in Claim 31

11 that relate to the objective of providing a

12 reliable connection to the FPC?

13 MR. GIBSON: Objection, scope.

14 THE WITNESS: The limitation I just read

15 about the flexible printed circuit goes to that

16 end. It's meant to be an electrical contact with

17 the external connection line through the

18 transparent conductive film.

19 BY MR. SCHLITTER:

20 Q. Is there any limitation in Claim 54

21 relating to the objective of lowering resistance?

22 MR. GIBSON: Objection, scope.

23 THE WITNESS: Well, after this long day,

24 I think my answer is still the same, that there's

25 an auxiliary line that's provided, there's an

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1 external connection line that needs to overlap it
2 and have electrical contact to it, and this can be
3 used toward that objective.
4 BY MR. SCHLITTER:
5 Q. Are there any limitations in Claim 54
6 relating to the objective of preventing unevenness
7 of the gap between the substrates?
8 A. Yes, the adjustment layer that's
9 provided in the limitation says an adjustment
10 layer -- at least part of the adjustment layer
11 extending under the sealant goes toward that
12 objective.
13 I'm sorry. I just noticed that in the
14 second round of questions, you started to ask me
15 about Claim 54 instead of Claim 31 and I didn't
16 track that. So I apologize. Maybe you should --
17 Q. This is why I can't find it.
18 A. -- re-ask -- ask me again. As I said,
19 it's been a long day. I'm sorry. Could you
20 perhaps ask me again?
21 Q. Okay. With respect to Claim 54, are
22 there any limitations in Claim 54 that relate to
23 the objective of reducing resistance?
24 MR. GIBSON: Objection, scope.
25 THE WITNESS: Yes, there's a first

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1 conductive line, a second conductive line and
2 these are both mentioned that -- to have a
3 limitation where they are in electrical contact
4 and this is -- these are the elements that serve
5 that objective.
6 BY MR. SCHLITTER:
7 Q. Are there any elements of Claim 54 that
8 relate to preventing unevenness of the gap between
9 the substrates?
10 A. Yes, there's a conductive layer over the
11 substrate that's provided for that purpose.
12 Q. Is there any other limitation relating
13 to the conductive layer that pertains to the
14 objective of preventing unevenness of the gap?
15 MR. GIBSON: Objection, form.
16 THE WITNESS: Well, the full claim
17 limitation that mentions a conductive layer reads
18 a conductive layer over the substrate. And then
19 an additional limitation says wherein the
20 conductive layer is electrically isolated from the
21 other conductive elements that are listed.
22 And there's another limitation that
23 identifies that the sealant should overlap at
24 least part of the conductive layer to provide
25 that -- a reduction of the unevenness underneath

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1 the sealant.
2 BY MR. SCHLITTER:
3 Q. Are there any limitations in Claim 54 of
4 the '204 patent that pertain to the objective of
5 providing strong adhesion of the sealant?
6 MR. GIBSON: Objection, scope.
7 THE WITNESS: Yes, there is. There is a
8 sealant that is limited to be in direct contact
9 with the second insulating film.
10 BY MR. SCHLITTER:
11 Q. Do any of the other elements of Claim 54
12 pertain to this objective --
13 MR. GIBSON: Objection, scope.
14 BY MR. SCHLITTER:
15 Q. -- of providing strong adhesion of the
16 sealant?
17 MR. GIBSON: Objection, scope.
18 THE WITNESS: The other elements refer
19 to a transparent conductive layer that are over a
20 first region of the second conductive line and
21 it's clear that the FPC is meant to connect in
22 that region as well, and that also corresponds to
23 allowing the sealant to connect directly to the
24 second insulating film without the ITO below it.
25

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1 BY MR. SCHLITTER:
2 Q. Are there any limitations in Claim 54
3 that relate to providing a reliable connection of
4 the FPC?
5 MR. GIBSON: Objection, scope.
6 THE WITNESS: Yes, there is. There's a
7 limitation that reads the second conductive line
8 and the flexible printed circuit are in electrical
9 contact through the transparent conductive layer.
10 MR. GIBSON: Why don't you go off the
11 record?
12 VIDEOGRAPHER: We're going off the
13 record. The time is 4:38.
14 (Short recess.)
15 VIDEOGRAPHER: We're back on record.
16 The time is 4:40. Please continue.
17 BY MR. SCHLITTER:
18 Q. Earlier today you were asked about
19 Example 3 of the '204 patent and Figs. 4A and 4B
20 and you said that redundancy was implicit in the
21 structure that's disclosed in the '204 patent.
22 Do you recall that?
23 A. I vaguely recall that discussion.
24 Q. Are there any limitations in the -- in
25 Claim 31 that relate to redundancy?

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1 A. There are no limitations in Claim 31 or
2 Claim 54 that require that, but if the two
3 conductors in both of those claims are connected
4 through the first inter-layer film or the first
5 insulating film, there will be at least partial
6 redundancy by that connection.
7 Q. Is it necessary to know the process by
8 which a structure is made in order to determine
9 whether it is covered by Claim 31 or Claim 54?
10 MR. GIBSON: Objection, form.
11 THE WITNESS: It is certainly not
12 required to know the process by which it's made to
13 determine if it meets Claim 51 (sic) or the
14 structure in Claim 54.
15 MR. SCHLITTER: What specifically was
16 your objection?
17 MR. GIBSON: I objected to form.
18 MR. SCHLITTER: In what respect? In
19 what respect?
20 MR. GIBSON: The -- let me look back at
21 the question. The question was compound and
22 vague.
23 BY MR. SCHLITTER:
24 Q. Let me restate the question.
25 Is it necessary to know the process by

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1 which a structure is -- has been made in order to
2 determine whether it is covered by Claim 31?
3 MR. GIBSON: Objection, form.
4 THE WITNESS: That is not required.
5 BY MR. SCHLITTER:
6 Q. When I say "covered by Claim 31," do you
7 understand that to mean whether it infringes
8 Claim 31?
9 A. That's how I'm understanding your
10 question, yes.
11 Q. Do you need to know the process by which
12 a structure is made in order to determine whether
13 it infringes Claim 54?
14 MR. GIBSON: Objection, form.
15 THE WITNESS: A person of ordinary skill
16 does not need to know that to determine
17 infringement of Claim 54. To be clear, does not
18 necessarily need to know that.
19 MR. SCHLITTER: I have no further
20 questions.
21 MR. GIBSON: I have a couple of
22 follow-up questions.
23 EXAMINATION (Further)
24 BY MR. GIBSON:
25 Q. Would you agree with me that if you --

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1 well, in 1997 it was well-known to one of ordinary
2 skill in the art that if you had two lines running
3 in parallel, that could lower the resistance
4 versus just having one line?
5 A. That general principle was well-known by
6 1997 and Shiba shows a good example of that.
7 Q. And in terms of the -- we covered some
8 of this in the '403 (sic) -- the '204 doesn't
9 describe any sort of problem with sealant
10 connections, correct?
11 A. In your question you referred to '403.
12 I assume you mean the '413?
13 Q. I'm sorry. The '413 we talked about
14 yesterday.
15 A. Well, the specification is the same in
16 both of those patents. So whatever I said about
17 that applies to the '204 patent as well.
18 Q. Right. There's nothing in the '204
19 patent that discusses that somehow the prior art
20 as having trouble having the sealant connect to
21 the substrate or bond with the substrate?
22 A. There is -- as I've said in our
23 discussion about the '413 specification, that
24 there is no explicit discussion of that in the
25 '204 specification.

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1 Q. And, in fact, it was well-known in the
2 art that a sealant would bond better with an
3 insulating layer than a transparent conductive
4 layer as of 1997, correct?
5 A. As a matter of general principle, that
6 was known, but as we talked about also in that
7 discussion, that's subject to other constraints
8 and Shiba shows an example of that as well.
9 Q. All right. But you would agree with me
10 that it was well-known to people of ordinary skill
11 in the art in 1997 that a sealant would bond
12 better with an insulating layer than an ITO layer,
13 correct?
14 A. As a general matter, yes.
15 MR. GIBSON: I have nothing further.
16 MR. SCHLITTER: I have nothing further.
17 VIDEOGRAPHER: This concludes the
18 videotaped deposition of Dr. Michael Escuti and
19 the end of Media Unit Number 4. The time is 4:47.
20 We're now off record.
21 (Whereupon, the following proceedings
22 were had off the video record:)
23 MR. GIBSON: So after discussing a
24 couple of the exhibits that were marked, we
25 decided to mark them with additional numbers next

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1 in order. The '204 patent, which the witness made
 2 some annotations on, we're going to mark as
 3 Exhibit 1012. And the Shiba patent, which is
 4 Patent No. 5,684,555, where the witness made some
 5 markings on, we are going to mark that as
 6 Exhibit 1013.
 7 MR. MANZO: You hadn't used those
 8 numbers before.
 9 MR. GIBSON: Not today.
 10 MR. SCHLITTER: That's fine.
 11 (Whereupon, the proceedings concluded
 12 at 4:50 p.m.)
 13
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1 CERTIFICATE OF CERTIFIED SHORTHAND REPORTER
 2 I, Sandra L. Rocca, a State of Illinois
 3 licensed Certified Shorthand Reporter, License No.
 4 084-003435, do hereby certify:
 5 That on the 6th day of September, 2013,
 6 at 9:49 a.m., 115 South LaSalle Street, Chicago,
 7 Illinois, the deponent MICHAEL J. ESCUTI, Ph.D.
 8 personally appeared before me;
 9 That the said MICHAEL J. ESCUTI, Ph.D.
 10 was duly sworn by me to testify and that the
 11 foregoing was stenographically recorded and
 12 constitutes a true record of the testimony given
 13 and the proceedings had at the aforesaid
 14 deposition;
 15 That the deposition terminated at
 16 4:50 p.m.;
 17 That the reading and signing of the
 18 deposition was not waived, and the deposition was
 19 submitted for signature. Pursuant to Rule 30(e)
 20 of the Rules of Civil Procedure, if deponent does
 21 not appear or read and sign the deposition within
 22 30 days, or make other arrangements for reading
 23 and signing, the deposition may be used as fully
 24 as though signed, and this certificate will then
 25 evidence such failure to appear as the reason for

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1 UNITED STATES PATENT AND TRADEMARK OFFICE
 2 BEFORE THE PATENT TRIAL AND APPEAL BOARD
 3 INNOLUX CORPORATION,)
 4)
 5 vs.) IPR2013-00068
 6 SEMICONDUCTOR ENERGY) U.S. Pat. No.) 8,066,204
 7 LABORATORY CO., LTD.,)
 8)
 9 Patent Owner.)
 10 I, MICHAEL J. ESCUTI, Ph.D., being first
 11 duly sworn, on oath say that I am the deponent in
 12 the aforesaid deposition taken on September 6,
 13 2013; that I have read the foregoing transcript of
 14 my deposition, consisting of pages 1 through 193
 15 inclusive, and affix my signature to same.
 16 _____ as it now appears
 17 _____ as it now appears with corrections
 18 MICHAEL J. ESCUTI, Ph.D.
 19
 20
 21 SUBSCRIBED and sworn to
 22 before me this _____ day of
 23 _____, 2013.
 24 _____
 25 Notary Public

Page 193

1 signature not being obtained;
 2 That I am not counsel for nor related to
 3 any of the parties herein, nor a relative or
 4 employee of such attorney or counsel for any of
 5 the parties hereto, nor am I interested directly
 6 or indirectly in the outcome hereof.
 7 IN WITNESS WHEREOF, I have hereunto set
 8 my hand and seal of office this _____ day of
 9 _____, 2013.
 10
 11
 12 SANDRA L. ROCCA, CSR, RPR, RMR, CRR
 13 CSR License No. 084-003435
 14 Expires May 31, 2015
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