## **SEL EXHIBIT 2023**

INNOLUX CORPORATION v. PATENT OF SEMICONDUCTOR ENERGY LABORATORY CO., LTD.

IPR2013-00066

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Page 1
          UNITED STATES PATENT AND TRADEMARK OFFICE
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          BEFORE THE PATENT TRAIL AND APPEAL BOARD
3
     INNOLUX CORPORATION,
4
               Petitioner,
5
                                    )No. IPR2013-00068
6
          vs.
                                    )Patent 8,068,204
     PATENT OF SEMICONDUCTOR
7
     ENERGY LABORATORY CO., LTD., )
8
               Patent Owner.
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13
      VIDEOTAPED DEPOSITION OF MILTIADIS HATALIS, PH.D.
14
                       Irvine, California
15
16
                     Tuesday, July 2, 2013
                            Volume I
17
18
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20
     Reported by:
21
     DENISE BARDSLEY
     CSR No. 11241
22
     Job No. 1683385
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     PAGES 1 - 153
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	Page 2
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	)Patent 8,068,204
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	ENERGY LABORATORY CO., LTD., )
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	Patent Owner. )
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14	Videotaped deposition of MILTIADIS HATALIS,
15	PH.D., Volume I, taken on behalf of Patent Owner, at
16	3 Park Plaza, Suite 1100, Irvine, California,
17	beginning at 9:14 a.m. and ending at 5:44 p.m. on
18	Tuesday, July 2, 2013, before DENISE BARDSLEY,  Certified Shorthand Reporter No. 11241.
19	certified shorthand Reporter No. 11241.
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	Page 3
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	Page 4
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	Page 5	
1	INDEX	
2	WITNESS EXAMINATION	
3	MILTIADIS HATALIS, PH.D.	
	VOLUME I	
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5		
6	BY MR. MANZO 7	
7		
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10	EXHIBITS	
11	(NONE)	
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		Page 6
1	Irvine, California, Tuesday, July 2, 2013	,
2	9:14 a.m.	
3		
4	THE VIDEOGRAPHER: Good morning.	
5	We are on the record at 9:14 a.m. on July	09:14:45
6	2nd, 2013. This is the video-recorded deposition of	
7	Dr. Milt Hatalis. My name is Scott Slater, here	
8	with our court reporter, Denise Bardsley.	
9	We are here from Veritext Legal Solutions	
10	at the request of counsel for the patent owner.	09:15:07
11	This deposition is being held at 3 Park Plaza, Suite	
12	1100, in Irvine, California 92614. The caption of	
13	this case is Innolux Corporation versus Patent of	
14	Semiconductor Energy Laboratory Co., Ltd., Case	
15	No. IPR2013-00068, Patent 8,068,204.	09:15:34
16	Please note that audio and video recording	
17	will take place unless all parties agree to go off	
18	the record. The microphones are sensitive and may	
19	pick up whispers, private conversations or cellular	
20	interference. I am not authorized to administer an	09:15:57
21	oath, I am not related to any party in this action,	
22	nor am I financially interested in the outcome in	
23	any way.	
24	May I please have an agreement from all	
25	parties that we may proceed?	09:16:08

		Page 7
		rage /
1	MR. MANZO: So agreed.	
2	MR. GIBSON: I agree.	
3	THE VIDEOGRAPHER: Thank you.	
4	At this time will Counsel and all present	
5	please identify themselves for the record.	09:16:17
6	MR. MANZO: I'm Edward Manzo for the patent	
7	owner, from the firm law firm of Husch Blackwell,	
8	LLP. With me is my partner, Mark Murphy, from the	
9	same firm, and also with us is	
10	MR. SCHLITTER: Stan Schlitter from Steptoe	09:16:38
11	& Johnson, also for the patent owner.	
12	MR. GIBSON: Stan Gibson on behalf of the	
13	petitioner.	
14	THE VIDEOGRAPHER: Thank you very much.	
15	Will the court reporter please administer the oath.	09:16:47
16		
17	MILTIADIS HATALIS, PH.D.,	
18	having been administered an oath, was examined and	
19	testified as follows:	
20		
21	EXAMINATION	
22	BY MR. MANZO:	
23	Q Good morning, Professor.	
24	A Good morning.	
25	Q I am placing before you a copy of the Shiba	09:17:11
	2 2 3 3 5 7 2 3 3 5 7 2 3 3 5 7 2 3 3 5 7 2 3 3 5 7 2 3 3 5 7 2 5 7 2 5 7	

		Page 8
1	reference, which was marked as Exhibit 1003	
2	previously, and it bears Patent No. 5,684,555.	
3	Is it okay with you if we just refer to	
4	that as Shiba in these proceedings?	
5	A That will be fine.	09:17:40
6	Q Now, before we dig into the examination, we	
7	asked you a lot of questions yesterday morning about	
8	your background, and we're hoping that we don't need	
9	to repeat all those today and that we can rely on	
10	yesterday's testimony with regard to your background	09:18:29
11	and other examination on technical issues.	
12	Is that agreeable, Counsel?	
13	MR. GIBSON: His background is fine; I	
14	don't believe it's changed from yesterday to today.	
15	I'm not sure what you mean by rely on other	09:18:44
16	examination on technical issues. Certainly we don't	
17	need to cover his background again.	
18	MR. SCHLITTER: We intend to use both the	
19	transcripts, probably, from yesterday and today and	
20	both the '413 IPR and '204 IPR if that turns out to	09:19:04
21	be appropriate. There are a lot of questions that	
22	relate to the same exhibits in both	
23	MR. GIBSON: I don't disagree with that. I	
24	just don't know what it means to use the same	
25	technical understanding, so it is just too broad of	09:19:20

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		Page 9
1	a term.	
2	If we have an objection to using something	
3	in a different one, then I don't know what that	
4	would be. At this point I'd have to see how we are	
5	using it. I don't think we need to repeat questions	09:19:32
6	from yesterday.	
7	MR. SCHLITTER: Okay.	
8	MR. MANZO: Thank you.	
9	Q Professor	
10	A Before we proceed, since you're not going	09:19:41
11	to be asking me any more questions about my	
12	background, may I make a comment related to some of	
13	the questions you asked me yesterday, for the	
14	record?	
15	Q That's somewhat unusual.	09:19:59
16	What are they? Technical issues?	ļ
17	A No, no. They are related to my experience	
18	in patent litigation. You asked me a lot of	
19	questions in those areas and you asked me,	
20	percentagewise, about the income I received from	09:20:16
21	patent litigations and so on.	
22	Q Sure. Why don't you go ahead.	
23	A So ·	
24	Q Let me ask you a question.	
25	Could you please clarify some of the issues	09:20:27

		Page 10
1	that were raised yesterday that you thought about	3
_	further overnight regarding your background.	
2		
3	A Sure. Thank you.	
4	You asked me a lot of questions about my	
5	litigation experience against a series of case	09:20:40
6	against SEL, and you also asked me a lot of	
7	questions related to, percentagewise, the income I	,
8	received from patent litigation work. And I was	
9	going to make a comment that compared to the experts	
10	that wrote the two response the two expert	09:21:03
11	reports for the last two patents that I was	
12	involved, I find out that I have less litigation	
13	cases than them and that their rates are	
14	significantly higher than mine.	
15	So I just want to clarify, that case, that	09:21:24
16	some of them also work for more than one case,	
17	several cases on behalf of SEL.	
18	You comment that I worked against SEL,	
19	that's because some of my clients were being sued	
20	not my clients, but some of the law firms were being	09:21:42
21	sued by SEL, so I represented defendants in this	
22	case, or in some case. And the other experts	
23	represented SEL against some of the same clients.	
24	So my litigation experience and my income	
25	is less than what it is, than the other experts.	09:22:03

		Page 11
1	Q I see.	
2	A Thank you.	
3	Q Thank you.	
4	Professor, I'm placing before you a copy,	
5	also, of Exhibit 1001, which is the patent involved	09:22:26
6	in this inter partes review, Hirakata U.S. Patent	
7	8,068,204.	
8	Are you familiar with that patent?	
9	A Yes.	
	Q And I'm also placing before you a copy of	09:22:49
10		03.22.23
11	the declaration that was filed as Exhibit 1007 in	
12	this case.	
13	Could you look at that Exhibit 1007 and see	
14	if that's the declaration that you signed?	
15	A Yes, it is.	09:23:41
16	Q Could you please tell me if I'd like to	
17	know how this declaration was prepared. And so my	·
18	first question is: Did you do the search for prior	
19	art that you applied in this patent in this	
20	declaration?	09:24:11
21	A No, I did not.	
22	Q How did you come to cite these particular	
23	references?	
24	A I was provided, by the counsel, some prior	
25	art references, and I selected the most relevant for	09:24:27

		Page 12
1	this particular content.	
2	Q Did you begin by reading the patent in	
3	question, Hirakata, Exhibit 1001?	
4	A Of course. I read '204, I tried to	
5	understand what '204 what is the subject matter	09:24:53
6	of '204, what is the objective of '204, what are the	
7	specifications and the claims of '204, and then I	
8	reviewed the pieces of prior art that I was provided	
9	and selected the ones I thought was the most	
10	relevant from that pool.	09:25:16
11	Q And your selection was based on the claims	
12	at issue; is that right?	
13	A Correct.	
14	Q How did you prepare for today's deposition?	
15	A I reviewed my declaration, I reviewed the	09:25:51
16	petition for the inter partes review, I reviewed the	
17	board decision, I reviewed the initial response of	
18	the patent owner, I also reviewed the request for	
19	rehearing by the patent owners, the decision of the	
20	board on the rehearing request and, of course, I	09:26:21
21	review the '204 patent and the three patents that	
22	are cited in my declaration and which were accepted	
23	by the board as a basis for these hearings and the	
24	patent by Shiba, the patent by Sukegawa and the	
25	patent by Watanabe.	09:26:48

	Page 13
1	I don't remember the exact patent numbers.
2	If you want, I can refer to.
3	Q No.
4	A It is referred to in my declaration.
5	Q That's fine. 09:26:59
6	And you met with your counsel for four
7	days I'm sorry. You met with counsel for
8	petitioner for four days in preparing for these two
9	days of depositions?
10	A Correct. 09:27:15
11	Q In those four days did you cover both
12	patents or did you have four days for each patent?
13	Four days for the one and four more days for the
14	other one?
15	A No, we covered both, and we did not work 09:27:27
16	full time because two of those days were the
17	weekend.
18	Q Let me start with the Shiba reference,
19	which I think is the one you relied on primarily; is
20	that correct? 09:28:00
21	A Correct. The Shiba, Sukegawa and Watanabe.
22	Q Does Shiba have an objective or desire to
23	maximize the display area relative to the outside
24	dimensions of the panel?
25	A Yes, it does. 09:28:49

		Page 14
1	Q Can you explain how Shiba goes about	
2	achieving that objective, briefly?	
3	A One of the issues in making a panel one	
4	of the steps in making a panel is the part that	
5	assembles and seals the two counter substrates in an	09:29:20
6	area which is referred to as the seal area, and that	
7	area occupies is all around the display and has a	
8	certain width along the transverse direction of the	
9	seal that is a distance that a lot of people tried	
10	to minimize because the whole seal area is kind of a	09:29:52
11	wasted area. It is needed in order to seal the	
12	liquid crystal within, but it doesn't serve any	
13	other purpose.	
14	If one tried to make a seal very narrow,	
15	there are problems that the seal is not very	09:30:15
16	effective. If you make it very wide, you waste more	
17	area for that one.	
18	So the wiring that Shiba is proposing	
19	enables the user from a narrower sealant. That's	
20	one of the objectives in Sukegawa sorry in	09:30:46
21	Shiba, and that's one of the approaches that he's	
22	taking.	
23	And another approach that he's taking, to	
24	reduce the dimensions of the panel.	
25	And here I like to make a comment, since	09:31:10

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		rage 15
1	you were combining the proceedings from yesterday	
2	and today, that yesterday I was making a lot of	
3	statements about the that someone skilled in the	
4	art would know that minimizing the waste area and	
5	trying to make as much as possible of the display	09:31:28
6	area relative to the size of the glass is very	
7	relevant.	
8	As you see here, it was an issue that was	
9	well known and a lot of researches attempted to	
10	maximize the size of the display relative to the	09:31:50
11	size of the glass that's used to make that display.	
12	So the other continuing for Shiba is the	
13	other approach that Shiba is introducing is means to	
14	provide signals to the display only from two sides	
15	and relative to prior art that were providing	09:32:24
16	signals from four sides or three sides, and that	
17	would require more bonding area for pads and so on	
18	in these other sides. So by having two sides,	
19	that's an improvement.	
20	And in order to access the pads, which are	09:32:40
21	away from the areas, from the terminal regions from	
22	the pad areas that receive the signals and the power	7
23	from the outside circuit board, Shiba is proposing a	ı
24	specific wiring that will have benefits of reduced	
25	resistance and improved manufacturing yield by	09:33:15

	Page 16
eliminating manufacturing defects, in essence by	
having a two-layer wiring structure.	
And there may be other things. If you	
want, I can read more to Shiba and give you a bit of	
a summary. From the top of my head, I can refer to	09:33:36
Shiba and give you more detail if I missed	
something, but	
Q Let me ask some more specific questions.	
Does Shiba aim to reduce the width of the	
seal region without lowering the strength of	09:33:51
adhesion between the two substrates?	
A That is correct, by creating a wiring in a	
specific way that will have more surface contact	
area with the sealant, one can reduce the width of	1
the sealant, while still maintain the strength of	09:34:14
adhesion between the two substrates.	
Q And, finally, does Shiba also declare that	
his or her invention does not increase the number of	
manufacturing steps?	
A That is correct.	09:34:42
Q Let me invite your attention to Figure 3.	
And, as I see it, Figure 3 has a lower	
portion and an upper portion. Does that	
characterization seem reasonable to you?	
	having a two-layer wiring structure.  And there may be other things. If you want, I can read more to Shiba and give you a bit of a summary. From the top of my head, I can refer to Shiba and give you more detail if I missed something, but  Q Let me ask some more specific questions.  Does Shiba aim to reduce the width of the seal region without lowering the strength of adhesion between the two substrates?  A That is correct, by creating a wiring in a specific way that will have more surface contact area with the sealant, one can reduce the width of the sealant, while still maintain the strength of adhesion between the two substrates.  Q And, finally, does Shiba also declare that his or her invention does not increase the number of manufacturing steps?  A That is correct.  Q Let me invite your attention to Figure 3.  And, as I see it, Figure 3 has a lower portion and an upper portion. Does that

		Page 17
1	Q Okay. So what, generally, is the lower	
2	portion of Figure 3?	
3	A The lower portion is the external flexible	
4	wiring that comes and attaches to the glass, and the	
5	two pieces make contact in the terminal regions	09:35:38
6	where you have the pads via anisotropic conducting	
7	field.	
8	Q So you referred to two pieces. Is the	
9	first piece the lower part and the second piece the	
10	upper part?	09:36:35
11	A The upper part is the glass substrate that	
12	contains the display area, the wiring, the seal, the	
13	counter substrates.	
14	Of course, not everything is shown at this	
15	level of details, but I can refer to the exact	09:36:55
16	Watanabe is calling the elements.	
17	Q I don't need that.	
18	A Okay. It isn't called the array substrate,	
19	it's the glass substrate.	
20	Q Okay. So where is the display region of	09:37:16
21	this LCD depicted in Figure 3?	
22	A The display region is within the seal area.	
23	The seal area is element 111, 111.	
24	Q Do you mean that the display area is within	
25	the region circumscribed by the seal area 111?	09:37:56

	· · · · · · · · · · · · · · · · · · ·	
		Page 18
1.	A No, I mean it is inside.	
2	111 is two lines. It's run parallel like a	
3	band. And within that band, over here, the upper	
4	left corner is the start of the display. It is the	
5	display surrounded by the 111 band.	09:38:23
6	Q Yes.	
7	A So it is inside.	
8	It extends beyond the inside line of 111.	
9	Q And relative to what's been marked as X1,	
10	X2, X3, X4 and X5, where is the display area?	09:38:41
11	A X1 is the first signal lines, data signal	
12	lines, also referred to as data lines. So X1, X2,	
13	X3, X4 is the first data lines of the array. And	
14	there may be hundreds of such lines, which it starts	·
15	from the X1, and then you put one next to each other	09:39:25
16	at specific interval, which is determined by the	!
17	spacing between pixels. And depending upon the form	
18	of the display and how many lines you have, you have	
19	640 of such lines if it's a VGA, monochrome, 640	
20	times three if it is VGA color, and so on.	09:39:50
21	Q And so	
22	A And then these are the data signal lines.	
23	And you also have the scanning lines. The scanning	
24	lines are orthogonal to the data lines. They start	
25	from the opposing side of the display. The 201d is	09:40:20

		Page 19
1	one of the short sides of the display. The opposing	
2	side of that one is at 201c, so the scanning lines	
3	start from that side and they propagate orthogonal	
4	to the X lines, which are the data lines. And	
5	either the first or the last is the one that	09:40:57
6	terminates right past X1.	
7	And the first one the first one,	
8	probably, it goes up to where that square label	
9	125a.	
10	Q Okay.	09:41:20
11	A Stops short of that one.	
12	Q Okay. Thank you.	,
13	So if you looked at Figure 1, for example,	
14	together with Figure 3, it is fair to say that	
15	Figure 3 is the bottom left corner of Figure 1 in a	09:41:32
16	blowup view?	
17	A In Figure 1 there is a rectangle drawn	
18	called A. So Figure 3 is a magnification of the	
19	content within that rectangle labeled A.	
20	Q Good. So the display area is up and to the	09:41:53
21	right of Figure 3, generally speaking, yes?	
22	A Yes. It starts from this corner and	
23	propagates	
24	Q Okay.	
25	A up and to the right.	09:42:14

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		Page 20
1	Q Now, in Figure 3 is area 731 a power supply	
2	pad? That's that box off to the corner of the	
3	sealant region.	
4	Do you see it?	
5	A Yes, I see it.	09:42:34
6	Q Is that a power supply pad?	
7	A It is the pad that provides the power to	
8	the counter substrate. It is referred in column 5,	
9	line 51, as the power supply pad 731.	
10	And what it does is it receive a signal	09:43:24
11	from the common pad 751. And the common pad 751	
12	receives the power or the signal from the external	
13	wiring 711.	
14	And then that through wiring, that signal	
15	goes to 731, and then through a conductive means,	09:43:55
16	like an epoxy that contains conductive particles,	
17	731 is making contact to the counter substrate that	
18	will be bonded to the substrate that is shown to the	
19	glass substrate 201.	
20	The two substrates will be bonded through	09:44:34
21	the seal, but the seal is not conductive. The	
22	seal or the epoxy that will be placed on the pad	
23	731 is a conductive one, and that will provide the	
24	connection to the ITO layer that it covers the	
25	entire surface of the counter glass substrate, which	09:44:55

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		Page 21
1	is the counter electrode for the pixels.	
2	Q Thank you.	
3	Just generally speaking, in Figure 4, just	
4	below Figure 3, is the display region to the right	
5	and the terminal region to the left?	09:45:43
6	A In that cross section?	
7	Q In that cross section, yes.	
8	A Correct. The right, the left with respect	
9	what with respect to the seal, yes.	
10	Q I was talking about left and right with	09:46:07
11	respect to the page that's on, but that's also with	
12	respect to the seal.	
13	So to the left of the seal, somewhat to the	
14	left of the seal, is a terminal region, correct?	
15	A Outside, correct.	09:46:23
16	Q All right. That's outside. And to the	
17	right of the seal you can call inside, and that's	
18	where the display region is?	
19	A Correct. That's where you have a specific	
20	gap between the two substrates, and the liquid	09:46:34
21	crystal is contained within.	ļ
22	Q And the pad 731 that you testified about a	
23	little while ago is shown in Figure 4 about near	
24	the bottom about 1 inch from the left side.	
25	Do you see it?	09:46:58

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		Page 22
1	A I see it.	
2	Q And then above that there is a substance or	
3	a part 115. Is that the what the patent refers	
4	to as transfer material? I think you called it an	
5	epoxy that's conductive.	09:47:16
6	A Correct.	
7	Q So that transfers electrical potential to	
8	the counter substrate, which is part, it looks like,	
9	501; is that correct? Well, the counter substrate,	
10	whatever number it is.	09:47:46
11	A The whole concept is 500. 501 is the glass	
12	substrate. On the counter substrate on the	
13	counter substrate you have the ITO layer.	
14	Q Okay. Now, going back to Figure 3, just to	
15	confirm a few small things, is pad 731 electrically	09:48:31
16	in common with the line 121 dash what looks like a 1	
17	or an I?	
18	A I think it is a 1.	
19	Q You think. Okay. Yes or no?	
20	MR. GIBSON: Objection; form.	09:48:54
21	THE WITNESS: What do you mean	
22	"electrically in common"? It is formed by the	
23	extent it is the same metal. It's form is a big	
24	rectangle in the pad 731, and then the metal	
25	continues and forms that wiring 121. They are all	09:49:16

		Page 23
1	pre-made by the same material. And that material is	
2	the material of the data lines.	
3	BY MR. MANZO:	
4	Q Okay.	
5	A Could be made with other material, but the	09:49:29
6	preferred embodiment, it is from the data lines, it	
7	is listed.	
8	Q I'm sorry. You are looking for something?	
9	A Looking at the material that is made, like	
10	in column 6, line 25, "In the case where TFTs of	09:50:07
11	bottom gate type are used as switching devices as in	
12	the above embodiment, the power supply pads 731 to	
13	738, the common pad 751, the third wiring lines,	
14	121-1 to 121-4, the inter-connecting pad 125a, and	
15	125b the second wiring lines 123-1 to 123-4, and the	09:50:43
16	first wiring line 127 can be formed in the same step	
17	of forming the data lines Xi."	
18	So it is the same material. And the mask	
19	that is used has a big rectangle where it is labeled	,
20	731, it has a narrow wiring line 121, and so on.	09:51:14
21	So they are not different pieces, they are	
22	formed all from the same initial metal layer that	
23	was put and covered the entire substrate, at least	
24	the remaining metal that's left.	
25	Q Okay. So I think that all I'm trying to	09:51:38

		Page 24
1	say is that they are electrically in common. They	
2	could be part of the same layer, but not be	
3	connected, right, because of masking strike that.	
4	That's unclear.	
5	When a person puts down a layer of metal,	09:52:05
6	it is governed by the mask; is that right?	·
7	MR. GIBSON: Objection; form.	
8	THE WITNESS: Well, you put the metal and	
9	cover the entire substrate, and then you use a	
10	masking step followed by an etching step to the	09:52:24
11	regions of the metal to form the different elements.	
12	Some of those regions, such as the lines	
13	X1, X2, X3, X4, they are long continuous lines	
14	starting from the pad 761 and continue to the other	
15	end of the display and stopping short of the other	09:52:53
16	sealant, of the opposing side of the sealant.	
17	Other regions, such as the ones that I just	
18	quoted from the patent, namely, 731, 121, 751, 123,	
19	125 and 127, they are all continuous metals and just	
20	say different ways, but all part of one continuing	09:53:29
21	pattern that is connected.	
22	BY MR. MANZO:	
23	Q Fine.	
24	A It's there is no any discontinuity.	
25	From 751 you can go to all those regions and still	09:53:52

		Page 25
1	be on the same metal.	
2	And so, for example, if 751 you put like	
3	5 volts in 751, you give it the potential of 5	
4	volts, then all that wiring in that pad will be held	
5	at 5 volts.	09:54:30
6	Q Okay. So if you put 5 volts on 751,	
7	there's also going to be 5 volts on 121-1 and 731	
8	and 123-1 and 127 and 125 well, I'm not sure	
9	about 125a strike 125a is that correct?	
10	A 125a is also part of it. It is a square	09:54:54
11	piece of metal.	
12	Q Okay.	
13	A From Figure 3. And then there are other	
14	elements from Figure 1 that also will be at 5 volts.	:
15	It will be the 732 well, the 732 receives but	09:55:08
16	from 751 it will be 731, 735, 736, 737, 738, so it	
17	will be multiple pads that will receive that signal.	
18	And it continues, also, on the other side.	
19	Q Professor, where is 732 sorry?	
20	A You see that in Figure 1.	09:55:43
21	But I said strike that one because 732	
22	receives a signal from another pad, not the 751.	
23	Q Okay.	
24	A But the one from 751 goes to 731 and then	
25	through the wiring 127 goes to 735, -36, -37, -38,	09:55:59

		Page 26
1	and it continues and gets connected up to 734.	
2	Q Pardon me. Where is 734?	
3	A You can see that in Figure 1.	
4	Q I see.	
5	A It's the lower right corner.	09:56:30
6	Q Thank you.	
7	A The wiring 727 forms almost like a pi, a	
8	Greek pi letter and kind of surrounds the display	
9	starting from 751 back.	
10	Q I'm not sure how it resembles the Greek	09:57:12
11	letter pi.	
12	A It is like a P, it is like this, that and	
13	then that.	
14	Q Oh, with regard to the I what you're	
15	saying is that looking at Figure 1, which is a plan	09:57:34
16	view, the lines 127, if they start at the bottom	
17	left, rise upward and then traverse across to the	
18	right and then traverse downward?	
19	A Correct.	
20	Q And that's what the Greek letter pi	09:57:52
21	somewhat looks like?	
22	A Yes.	
23	Q Now, is it correct that lines 127 are not	
24	electrically in common with the data line pads shown	
25	in Figure 3 as 761, 762, 763 and 764?	09:58:19

		Page 27
1	MR. GIBSON: Objection; form.	
2	THE WITNESS: You are using you	
3	introduced new terminology, "electrically in	
4	common," which is not I'm not familiar with this	
5	term.	09:58:40
6	BY MR. MANZO:	
7	Q All right. Let me	
8	A Electrically connected or same potential?	
9	What do you mean? Touching?	
10	Q Let me ask you a broad, open-ended	09:58:49
11	question, then.	
12	A Okay.	
13	Q Is there a relationship between the power	
14	supply pad 731 and the data line pad 761, 2, 3 and	
15	4?	09:59:04
16	A The common pad 751, that is connected, and,	
17	as we said earlier, the 5 volt in 751, it will be	
18	5 volt in 731. That is controlled by the external	
19	wiring, which is labeled 821a, which is the first	
20	external wiring from the element 711 from the lower	09:59:32
21	part of the lower part not the Figure 4, say	
22	Figure 3, the wirings 821a, that's the wiring that	
23	provides the signal to common pad 751.	
24	Q Okay. And to the right of 821a there are	
25	further lines?	10:00:00

		Page 28
1	A Right. Other pads. Each one connected to	
2	a data line, and they receive signal from the	
3	external wirings, other external wirings. If those	
4	external wirings happen to be the same voltage as	
5	the 751, then there may be one of the data wiring	10:00:30
6	may also be in the same voltage, but that's because	
7	the signal is received and happens to be in the same	
8	voltage.	
9	So if, for example, they all get 5 volts,	
10	5 volts will appear to all of them. Maybe the	10:00:44
11	5 volts only go to 751, and 761 get 1 volt, and 762	
12	get 2 volts, 3 volts or 4 volts, they can all be	
13	separate, separate potentials, independently	
14	determined by the outside circuit.	
15	Q Okay. So it's fair to say that pad 751,	10:01:02
16	761 through 764 are five independent pads?	
17	A Correct.	
18	Q Now	
19	A There are four as we said, there will be	
20	hundreds of these pads. There will be four pads	10:01:32
21	that will have the same voltage as 751, and you will	
22	be able to see that in Figure 1.	
23	Q I think you described those	,
24	A Okay.	
25	Q a little while ago.	10:01:48

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		rage 25
1	A But all the other pads, they will be	
2	independent.	
3	Q Okay. Is the sealant marked as region 113	
4	in Figure 4?	
5	A The sealant material	10:02:11
6	Q I just want to know the part number of the	
7	sealant.	
8	A Yeah, 113 is the sealing material.	
9	Q And you mentioned that in your declaration	
10	at paragraph 48, I think.	10:02:35
11	Could you confirm that on page 15?	
12	A Yes. So 113 is the sealant material and	
13	111 is the seal region; in other words, what is the	
14	projection or the regions on Figure 3 where the	
15	sealant will be applied.	10:03:04
16	Just clarify, the 111 and 113.	
17	Q Going back to 111, then, in Figure 3, some	
18	of the well, looking at the reference number	
19	111 do you see it?	
20	A Yes, the seal region.	10:03:30
21	Q And just to the right of the number 111	
22	there are these lines marked in a bracket as 127?	
23	A Correct.	
24	Q Now, some of those are in solid lines and	
25	some of those are in broken lines.	10:03:43

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		Page 31
1	metal, which is all those things are the same	
2	potential because all part of the same metal, which	
3	is continuous, is the external connection line.	
4	Q Is there anything in Figure 3 that you	
5	regard as an auxiliary line within the meaning of	10:06:03
6	the claims of the '204 patent?	
7	A The purpose of the line that the patent	
8	labels 127 127, column 6, line 5, it reads,	
9	"refers to wiring line 127 is drawn from the	
10	interconnect in pad 125a," and so on, and then it	10:06:55
11	says, "as shown in Figure 1, the first wiring line	
12	127 has got a longer segment, shorter side, 201d, to	
13	the second longer side, 201b and connected to the	
14	power supply pad 735 to 738 through the branch wired	
15	line. Then the first wiring line 127 is guided	10:07:25
16	along the first shorter side to 201c to the first	
17	longer side 201a."	
18	So the wiring 127, the first wiring 127,	
19	runs a very long distance.	
20	And if we look further down column 6 in the	10:07:51
21	line 37, it reads, "The first wiring lines 127 may	
22	be formed in the step of forming scanning lines Yj	
23	and the data lines Xi, respectively, thereby	
24	constituting a two-layer structure. In this case,	
25	if the layers are constantly connected to each	10:08:23

		Page 32
1	other, the wiring defect can be prevented and the	
2	manufacturing yield can be improved."	
3	In that section it reads that the 127 is	
4	it has two layers, and one layer made by the data	
5	wiring metal, that's the upper layer, that's the	10:08:46
6	auxiliary line sorry that's the external	
7	connection line that we have identified a moment	
8	ago, and the lower metal is low lower layer is	
9	made by the material of the scanning lines, and	
10	that's the auxiliary line.	10:09:04
11	Q Okay. So is it your testimony that the	
12	external connection line and the auxiliary line have	
13	the same number in Figure 3?	
14	A They are overlapping one on top of the	
15	other, and they occupy the same area.	10:09:36
16	Now, the auxiliary line is indicated in	
17	this wiring element 127. So both auxiliary lines	
18	and external connection lines are within the element	
19	127.	
20	Q Okay. Professor, I'd like to spend a	10:10:09
21	moment trying to understand the relationship between	
22	Figure 3 and Figure 4.	
23	Do you have an understanding of any such	
24	relationship?	
25	A I don't know what you mean. I do	10:10:50

		Page 33
1	understand what's in Figure 3 and 4 and all the	20.50
2	other figures, and Shiba, as I studied as well.	
3	Q Well, what is Figure 4?	
4	A Figure 4 is a cross section along the line	10 11 16
5	that in Figure 3 it shows a-a prime.	10:11:16
6	Q That's what the patent says, isn't it?	
7	A I believe so.	
8	Q Okay. So if we were to look and slice	
9	through this structure at the line a-a prime, we	
10	would theoretically see what is shown in Figure 4;	10:11:50
11	is that right?	:
12	A Correct.	
13	Q Professor, I had some problems verifying	
14	that because, for example, the pad 731 in Figure 3	
15	is not along that slice or that line from a-a prime,	10:12:24
16	and yet it's shown in Figure 4.	
17	Do you see that?	
18	A Yeah, I see that. But it is not uncommon	
19	for the artist to try to depict essential elements,	
20	that occasionally I've seen in other works that the	10:13:08
21	lines typically when you see a cross section is	
22	like a slice through one continuous straight line,	
23	but I've seen in other that the line can actually be	
24	like almost like some arbitrary shaped line in which	
25	the elements that will be along that arbitrary	10:13:33

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		Page 34
1	shaped line for the purpose of the teachings are	
2	illustrating in the cross section.	
3	That's done in order to save space so the	
4	author will not have to draw two or three or more	
5	cross sections to illustrate the relative location	10:13:57
6	of the different elements to try to include them in	
7	the same figure for illustration purposes.	
8	Q In your experience, don't draftsmen usually	
9	indicate that they are doing that on the plan view	
10	so you can avoid having to wonder where the cross	10:14:25
11	section is taken?	
12	A Well	
13	MR. GIBSON: Objection; form.	
14	THE WITNESS: These documents are meant to	
15	be read by people with ordinary skill within the	10:14:42
16	art. And the people use them to learn and be	
17	educated, and they already have a certain background	
18	and a certain experience that thinks that may not be	
19	clear to someone who is not skilled in the art, it	
20	is clear for them. So you point out some	10:15:09
21	differences in the artwork. I don't think that	
22	would be a big issue for someone skilled in the art.	
23	BY MR. MANZO:	
24	Q Okay. Looking at Figure 4, can you trace a	
25	signal path from the terminal region into the	10:15:42

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		1490 00
1	display region?	
2	A Well, in this particular cross section,	
3	the as you point out, it doesn't the terminal	
4	pads, it doesn't go to the display regions, it	
5	provides a connection to the element 731. So it	10:16:23
6	terminates there.	
7	Outside the seal region, it shows the cross	
8	section along 751 to 123, and inside the seal region	
9	it shows transistors and different elements,	
10	everything, the pixel.	10:17:01
11	Q Now, before you were talking about the	
12	person skilled in the art who would understand these	
13	drawings.	
14	What would such a person have been or how	
15	do you describe such a person as of 1997?	10:17:18
16	A Well, in general terms, it would be a	
17	person who has enough experience to be able to read	
18	those documents and be able to understand their	
19	meaning and be able to understand the teachings and	
20	be able to take the teaching and implement	10:17:43
21	structures and be able to combine different piece of	Ē
22	arts to create new structures that contain elements	
23	from different pieces of prior art.	
24	Q Can you describe their education and their	
25	experience in terms of degrees and whether they are	10:18:35

		Page 36
1	a circuit designer or a process design engineer?	
2	A Well, this is an interdisciplinary field,	
3	so you have people with different backgrounds coming	
4	from different fields. You can have people with a	
5	science background, like physics and chemistry, but	10:19:01
6	you can also be from material science or you can be	
7	from an engineering field, such as electrical	
, 8	engineering, chemical engineering.	
9	And those people, they have some courses as	
10	part of their education, which is relevant to this	10:19:34
11	technology. They also receive further training at	
12	the workplace, and we discussed about that	
13	yesterday, whether they are process engineers or	
14	circuit designers.	
15	I believe after a few years in the field,	10:19:56
16	all these peoples from different background, they	
17	will be able to understand these prior art pieces	
18	such as Shiba.	
19	Q Does Figure 4 show an external connection	
20	line and an auxiliary line?	10:20:36
21	A No. It shows you asked me if it shows	
22	an external connection line and an auxiliary line.	
23	It shows an external connection line.	
24	Q Which one is the external connection line?	
25	A It is 751. It is connected to 731, which	10:21:12
	1	

		Page 37
1	continually is labeled 751 same line, different	
2	portion is labeled 121-1 and another portion, same	
3	setting, is label 731.	
4	This is only a segment of the line, the	
5	metal wiring that is formed starting from 751. We	10:21:58
6	discussed earlier about the other pieces that are	·
7	formed from the same material, they are	
8	continuously that go under the seal, like 123 and	
9	127, which are not shown in Figure 4 but are shown	
10	in Figure 3.	10:22:26
11	Someone skilled in the art 751 is shown	
12	as a single layer in this.	
13	Q Do you see a storage capacitor in Figure 4?	
14	A Yes.	
15	Q Is it formed of these three layers, a	10:23:20
16	electrode Cj, or C subscript j, an insulator 211 and	
17	an electrode 251?	
18	A In column 4, line 7, it reads, "The storage	
19	capacitor is constituted by the pixel electrode 251	
20	and the storage capacitor lines Cj."	10:24:31
21	Q Why is a storage capacitor formed or shown	•
22	in Figure 4?	
23	A Well, the artist, as we discussed earlier,	
24	is trying to depict some critical elements outside	
25	the sealant and critical elements inside the	10:25:08

	Page 38
sealant. So the critical elements inside the	
sealant is a pixel structure, and the elements	
and the storage capacitor is one element within the	
pixel. So the artist try to show how that storage	
capacitor is formed.	10:25:37
Q What factors would impact the size of the	
storage capacitor?	
A What do you mean by that?	
Q Well, for example, does the thickness of	
insulator 211 impact the size of the storage	10:26:07
capacitor?	
A The capacitance depends on the thickness of	
the dielectric, which is in between the two	
electrodes, it is actually inversely proportional to	
the thickness of the insulating layer between them.	10:26:31
211 is the gate dielectric and is in between the two	
electrodes.	,
So as you increase the thickness, the	
capacitance will decrease, the storage capacitance	
will decrease.	10:26:57
Q Professor, if two insulators are stacked	
one on top of the other, is it technically possible	
to remove only the upper insulator by etching	
without causing any damage to the lower insulator?	
MR. GIBSON: Objection; form.	10:27:57
	sealant is a pixel structure, and the elements and the storage capacitor is one element within the pixel. So the artist try to show how that storage capacitor is formed.  Q What factors would impact the size of the storage capacitor?  A What do you mean by that?  Q Well, for example, does the thickness of insulator 211 impact the size of the storage capacitor?  A The capacitance depends on the thickness of the dielectric, which is in between the two electrodes, it is actually inversely proportional to the thickness of the insulating layer between them. 211 is the gate dielectric and is in between the two electrodes.  So as you increase the thickness, the capacitance will decrease, the storage capacitance will decrease.  Q Professor, if two insulators are stacked one on top of the other, is it technically possible to remove only the upper insulator by etching without causing any damage to the lower insulator?

		Page 39
1	THE WITNESS: Is this part of these	
2	proceedings?	
3	BY MR. MANZO:	
4	Q I don't think that that's a responsive	
5	answer.	10:28:23
6	A No, I tried to put it within the context	
7	whether I missed something here, and I want to say	
8	something in Shiba is referring to it, and I want to	
9	put in the context of the '413 patent and the prior	
10	art that I review and try to give you a response.	10:28:38
11	I don't recall that something that came out	
12	as a part of the '413 and the three patents that we	
13	are referring to today and we need to provide	
14	deposition testimony of those three.	
15	Q Well, with regard to the knowledge of the	10:29:01
16	person ordinarily skilled in the art in 1997, if	
17	there are two stacked insulators and you needed to	
18	etch through one of them, can it be done without	
19	causing damage to the lower of the two insulators?	
20	MR. GIBSON: Objection; form.	10:29:28
21	THE WITNESS: And can I ask you can you	
22	please point out where that question is related to	
23	the proceedings to the material that I have been	
24	called to testify here so I can put my answer in a	
25	context?	10:29:43

		_
		Page 40
1	But I don't think the material that's	
2	something I have studied to give you a thorough	
3	answer today.	
4	BY MR. MANZO:	
5	Q So are you saying that, technically, you're	10:30:25
6	not aware of that?	
7	MR. GIBSON: Objection; relevance.	
8	THE WITNESS: I'm saying that the question	
9	that you are asking is in a subject matter that I	
10	have not review and I'm not prepared to give you a	10:30:44
11	response today because I don't think that question	
12	deal with any of the material that I have studied.	
13	And I have been informed that you can ask anything	
14	you wish, but I'm prepared to discuss the prior art	
15	that I have in front of me and not to give you	10:31:05
16	answers I'll try but you asked me something	
17	and I asked for guidance so I can place my answer in	
18	proper context. But I do not recall this question	
19	come, that it was something that I had to deal with.	
20	BY MR. MANZO:	10:31:25
21	Q Well, you're experienced with etching; is	
22	that right?	
23	A Yes.	
24	Q Have you tried to etch stacked insulators	
25	and just etch your way to the top level of two	10:31:45

		Page 41
1	stacked insulators without any over etching of the	
2	lower insulator?	
3	MR. GIBSON: Objection; relevance.	
4	THE WITNESS: I have worked with many	
5	insulators and different substrates, and I have to	10:32:12
6	review my notes. If you guide me to some specifics,	
7	I may be able to give you a response, but this is a	
8	very big open-ended question that you are asking.	
9	MR. MANZO: Why don't we take a short	
10	recess.	10:32:36
11	THE VIDEOGRAPHER: We are off the record.	
12	The time is 10:32 a.m. on July 2nd, 2013. This is	
13	the end of video number 1 of the continuing	
14	deposition of Dr. Milt Hatalis.	
15	(Recess.)	10:32:52
16	(Whereupon Conrad Szuladzinski	
17	replaced Scott Slater as the.)	
18	THE VIDEOGRAPHER: This is the continuation	
19	of the deposition of Milt Hatalis. My name is	
20	Conrad Szuladzinski, and I am the videographer. And	11:13:38
21	we are back on the record at 11:13 a.m.	
22	BY MR. MANZO:	
23	Q Professor, in paragraph 48 of your	
24	declaration, which is Exhibit 1007, you referred to	
25	wiring lines with a two-layer structure. And you've	11:14:39

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		Page 42
1	referred to the passage, I think, at column 6,	
2	earlier this morning about the two layers; is that	
3	right?	
4	A Yes.	
5	Let me add, two-layer structure that are	11:15:06
6	overlapping, but there is an insulating layer in	
7	between and which is partially connected.	
8	Q Can you describe how the two layers are	
9	created and form the line that you're talking about,	
10	please, and include if you would, explain why you	11:15:35
11	conclude that there is insulation there.	
12	A Okay. Column 6, line 36, is our section	
13	where it reads, "Moreover, the first wiring lines	
14	127 may be formed in the step of forming the	
15	scanning lines Yj, and the data lines Xi."	11:16:22
16	So if we go to different sections of the	
17	patent trying to understand what are those two	
18	layers are formed and what occurs in between them,	
19	you go to column 4 at line 1, it reads, "Each	
20	scanning line Yj is used as the gate dielectric of a	11:17:07
21	TFT 221."	
22	Then you go further down in column 4, line	
23	12, it says, "As shown in Figure 4, this scanning	
24	lining Yj and the storage capacitor line Cj are	
25	formed on the glass substrate 201. The scanning	11:17:31

		Page 43
1	line Yj itself is used as the gate dielectric of the	
2	TFT 121. A gate dielectric 211, having a laminated	
3	structure of silicon oxide and silicon nitride, is	
4	formed on the gate dielectric Yj."	
5	So in these sections it talks about right	11:17:55
6	on top of the glass substrate, you are forming the	
7	scanning lines so you leave portions of the film	
8	that was deposited onto the glass substrate to form	
9	the different wirings and the scanning lines, and	
10	capacity lines is a set of lines, and the portion of	11:18:19
11	the wiring on 127, as we read earlier, is formed at	
12	that time. And this is the lower layer.	
13	Q Made of?	
14	A The material which is used to make this	
15	scanning lines. And the sentence at column 4, line	11:18:42
16	15, which reads, "Gate dielectric having a laminated	
17	structure of silicon oxide and silicon nitride is	
18	formed the gate dielectric."	
19	It shows that after the scanning line is	
20	put down and is patterned to form different elements	11:19:02
21	that remain, then the dielectric is formed and the	
22	dielectric is deposited by, typically, some sort of	
23	a CPT technique that it covers the entire substrate.	
24	And then the then, based on the sections	
25	of column 6, line 31 that reads, "If the layers are	11:19:36

		Page 44
1	proximately connected to each other" that section	
2	means that after the gate dielectric has been	
3	formed, some step is occurring to open contact holes	
4	on top of the gate material, the scanning line	
5	material. And then we put down the material of the	11:20:09
6	data lines.	
7	And then that material has been patterned	
8	to form the data lines and the upper layer of the	
9	wiring 127.	
10	So what you have is the lower layers, made	11:20:31
11	of the scanning line material, you have the gate	
12	dielectric 211 in between, you have openings in the	
13	gate dielectric, and then you put down the data line	
14	material and you pattern that one and it forms the	
15	upper layer.	11:20:58
16	Q In creating the gate dielectric that you	
17	read about, the gate dielectric 211, and the	
18	specification at column 4 says, at line 16, "silicon	
19	oxide and silicon nitride," is there control over	
20	where those regions where those dielectrics end	11:21:29
21	up?	
22	MR. GIBSON: Objection; form.	
23	THE WITNESS: Well, what do you mean by	
24	"control"?	
25	BY MR. MANZO:	11:21:37

	Page 45
O Is there a masking step or patterning?	
	11:21:51
-	22.22.31
substrate. It is a masking step and an etching step	11:22:07
that will follow the deposition process.	
And if one wants to create the what is	
referred to in column 6, the if one is to enable	
the layers to be partially connected, so someone	
skilled in the art says if the layers are constantly	11:22:30
connected, if so, if someone wants to build a	
double-layer structure, then you have you need to	
create an opening in the gate dielectric to at	
least one opening to enable the partial connection.	
BY MR. MANZO:	11:22:49
Q How do you know that the dielectric is not	
etched away from the lower layer? What is the	
teaching?	
A What do you mean "lower layer"?	
Q From the lower layer of lines 127.	11:23:06
	And if one wants to create the what is referred to in column 6, the if one is to enable the layers to be partially connected, so someone skilled in the art says if the layers are constantly connected, if so, if someone wants to build a double-layer structure, then you have you need to create an opening in the gate dielectric to at least one opening to enable the partial connection.  BY MR. MANZO:  Q How do you know that the dielectric is not etched away from the lower layer? What is the teaching?  A What do you mean "lower layer"?

	Po	age 46
1	A Well, you have a masking step, right? And	
2	the masking step protects the great majority of the	
3	surface area where the dielectric layer will remain	
4	and only leaves exposed the portions of the area	
5	where you want that oxide and nitride film to be	11:23:32
6	removed so it will expose the underlying layer of	
7	127 so that the upper layer of 127 will come down	
8	the hole and touch the lower layer. And, by doing	
9	that, will create the partial connection that's	
10	referred to in column 6.	11:24:02
11	In that etching process you need to remove	
12	the entire gate dielectric, so you need to remove	
13	both the silicon oxide and the silicon nitride so	
14	the lower metal layer that were made by the scanning	
15	line metal is exposed. So then the upper layer	11:24:23
16	metal when it is deposited, it will touch it, it	
17	will make direct contact with it.	
18	Q Are you forgive me, Professor, but now	
19	I'm even more confused.	
20	You put down the scanning line layer and	11:24:53
21	then you define lines?	
22	A Correct.	
23	Q And then you put down nitride and oxide as	
24	dielectrics, and then you etch, correct?	
25	A Then you do a photo step and you pattern	11:25:12

		Page 47
1	the regions that you want to etch.	
2	Q Okay.	•
3	A Define the regions that you want to etch.	
4	Q Now, does this reference, Exhibit 1003,	
5	tell you whether you want to etch or not whether	11:25:26
6	you want to mask or not mask the nitride and oxide	
7	over line 127?	
8	A Well, it refers to a means to make a	
9	partial connection. That means the upper layer is	
10	touching some points, but not in all points, of the	11:25:52
11	underlying layer.	
12	Q Okay. Is there any other support for that,	
13	other than the word "partial" or "partially"?	
14	A Well, someone skilled in the art, as we	
15	were discussing yesterday, will know if there are	11:26:17
16	two layers and there is an insulating layer in	
17	between. We spent quite a bit of time yesterday to	
18	discuss that connection is done, someone skilled in	
19	the art will know that you have two layers, two	
20	metal layers and an insulating layer in between,	11:26:34
21	like an insulator has been sandwiched by the two	
22	metal layers, then you open contact holes; in other	
23	words, depicted in the prior art.	
24	Q But	
25	A That was something was well known in the	11:26:57

		Page 48
1	art.	
2	Q Well, Professor, how do we know that,	
3	according to this patent, the dielectric should be	
4	left on top of the first formed part of lines 127	
5	and not stripped away during the etching step?	11:27:16
6	There are two choices. Let me pause.	
7	Aren't there two choices, you can put the dielectric	
8	down and either leave it there or you can take it	
9	away, right?	
10	And you've chosen one way, and I'm saying	11:27:40
11	why is it that way instead of the other way?	
12	A I mean, what do you mean by leave it there	
13	or take it away?	
14	Q Okay. So the question is, you're citing	
15	this paragraph in the middle of column 6 to say that	11:27:54
16	there is a two-layer structure, two conductors?	
17	A Right, separated by an insulating layer,	
18	because in the sequence there is that layer that	
19	comes in between them. There is a specific order	
20	with which the layers are laid down.	11:28:08
21	Q Okay. And there will be insulator on top	
22	of the bottom layer	
23	A Correct.	
24	Q during the process of forming this?	
25	Now, you advise that there is also a	11:28:23

		Page 49
1	masking and etching step. And it seems to me that	
2	you are presuming that these lines 127 will be	
3	masked so that the nitride and oxide will remain	
4	there, or so that you'll just mask and etch contact	
5	holes instead of striping them away completely.	11:28:44
6	And my question to you is, where does that	
7	come from out of this reference?	
8	MR. GIBSON: Objection; form.	
9	THE WITNESS: What do you mean by striping	
10	completely?	11:29:03
11	BY MR. MANZO:	
12	Q Well, you said that	
13	A There's no masking step?	
14	Q Well, you said that there is a masking	
15	step.	11:29:11
16	A Right.	
17	Q And you leave you put a mask over the	
18	parts you want to remain before you etch. And then	
19	you etch, and then you remove the mask and you have,	
20	still intact, the parts that you wanted to have	11:29:22
21	remaining; is that correct?	
22	A I tried to visualize a structure that or	
23	the steps that need to occur to create what the	
24	inventor here is trying to teach. And the inventor	
25	is trying to teach a structure in which the two	11:29:42

	·	Page 50
1	layers are partially connected to each other.	
2	The inventor does not teach that you form	
	127 where the two layers are in direct contact and	
3		
4	overlapping over the entire length of the line. So	
5	I'm trying to see how one will create a structure	11:30:07
6	which are constantly connected.	
7	If I understand your reasoning, you tried	
8	to somehow create both the partial connection and,	
9	at the same time, remove the entire dielectric. And	
10	if you remove the dielectric, then the lines will	11:30:29
11	not be constantly connected.	
12	Q How will they be connected, hypothetically?	
13	A Well, I'm trying to understand your	·
14	structure. You say you remove the gate dielectric	
15	completely.	11:30:46
16	So in your response that I studied it was a	
17	drawing where the two metals were overlapping a	
18	hundred percent over that distance of the drawing	
19	that you had indicated there, and that drawing does	
20	not show that the layers are constantly connected.	11:31:08
21	They show that they are connected over the entire	!
22	length of the line.	
23	I'm trying to understand how you can remove	
24	the entire dielectric and still be able to get the	
25	partial connection.	11:31:27

		Page 51
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1	Q All right. So I understand your answer to	
2	mean that if you remove the entire dielectric, you	
3	would not have partial connection, which the patent	
4	refers to, the Shiba patent refers to at column 6,	
5	lines 39 or 40, and, instead, you would have a	11:31:49
6	complete full connection between the two conductive	
7	layers of lines 127; is that right?	
8	MR. GIBSON: Objection; form.	
9	THE WITNESS: Well, you start discussing	
10	another hypothetical way of making it, and I'm	11:32:37
11	trying to understand your hypothetical way, and I	
12	was asking you questions.	
13	And now you're telling me I give you	
14	answers, I was meaning mostly within the context of	
15	a question that was asking you, what do you mean by	11:32:57
16	removing the dielectric and how that removal will	
17	then satisfy the partial connection.	
18	So I'm still trying to understand your	
19	what is the structures you are proposing is an	
20	alternative to the structures that I have proposed	11:33:18
21	and which was well known in the art of how to	
22	connect two layers separated by an insulating layer,	
23	and the connection typically involves formation of	
24	contact holes and establishing a contact where the	
25	metal the upper layer will go down the hole and	11:33:44

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	touch the lever metal	
1	touch the lower metal.	
2	BY MR. MANZO:	
3	Q Well, thank you for your explanation, but	
4	your explanation presumes that there is insulation	
5	between the two layers. And the part of the Shiba	11:34:05
6	reference that you've cited for that is not in this	
7	discussion in the middle of column 6, lines 36 to	
8	42, it is somewhere else, and it is talking about	
9	other parts of the structure.	
10	And since there is a masking and etching	11:34:30
11	step, you are presuming that the masking and etching	
12	step is such that as to leave the insulator on top	
13	of 17 between the two layers, and I'm suggesting	
14	that maybe it doesn't work that way.	
15	And all I'm asking you is what is the	11:34:50
16	support for you saying that there is insulation	
17	between those two conductive layers? Because I	
18	don't see it between lines 36 or -7 and 42.	
19	A You make a long statement, so let me start	
20	from the first aspect or first topic of your	11:35:14
21	statement. And you state, "Your explanation	
22	presumes that there is insulation between the two	
23	layers," so let's focus just on that part.	
24	Q Okay.	
25	A I read sections in from Shiba that list	11:35:34

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		Page 53
1	the order with which the layers are put down, and I	
2	said that following those steps, it will lead to an	
3	insulating layer between the wiring formed from the	
4	scanning line material and any wiring formed between	
5	the data line material. And you seem to even	11:36:07
6	question that par.	
7	Why you say presume? You're reading	
8	differently in the specifications that I do? You're	
9	questioning even the presence of the dielectric.	
10	When the dielectric goes down, it goes everywhere.	11:36:28
11	It's not that it's put selectively in some regions,	
12	it is not put putting everywhere. So the dielectric	
13	is everywhere, on top of anything that is formed	
14	from the scanning line material.	
15	Q I understand that.	11:36:52
16	Now, according to what I think you said,	
17	there will be a masking and etching step	
18	A Okay.	
19	Q is that right?	
20	A Yeah, so we agree that on top of the	11:37:06
21	scanning line material there is dielectric.	
22	And I think we are agreeing that now there	
23	will be a masking step and an etching step.	
24	Are we on the same page, wavelengths?	
25	Q Let's say I follow you. And my question is	11:37:24

		Page 54
1	really this, now we're getting to the heart of the	
2	question	
3	A Okay.	
4	Q the question is, where does the mask go?	
5	I appreciate that there is masking. And	11:37:38
6	here's my point. If I want to mask over there	
7	someplace to my left, it doesn't mean that I'm also	
8	going to mask over here, someplace to my right.	
9	And can we agree with that?	
10	MR. GIBSON: Objection; form.	11:37:57
11	THE WITNESS: Masking step means that you	
12	leave what's known as the photoresist layer, which	
13	is an organic material that is photosensitive and	
14	which is patterned by exposure and followed by a	
15	developing step. So through that whole process,	11:38:17
16	part of the organic material, the photoresist,	
17	remains and protects anything which is underlying	
18	it.	
19	And the region that did not contain the	
20	photoresist, that will be exposed, whether it be the	11:38:33
21	chemical whether it be wet chemical means or dry	
22	chemical means those regions that will not be	
23	protected by the photoresist will be exposed to the	
24	etching.	
25	BY MR. MANZO:	11:38:47

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		Page 55
1	Q Good. Now, what part of Shiba tells you	The state of the s
2	that photoresist should cover the dielectric on top	
3	of the scan line material being used to make the	
4	lower layer of lines 127?	
5	A Well, you need to create a connection that	11:39:18
6	will at the end will be a partial connection.	
7	That 127 the lower layer of 127 will be partially	
8	connected to the upper layer of 127; that in the two	
9	regions where the two layers overlap, the connection	
10	will be a partial one.	11:39:39
11	Isn't that what is reading on column 6,	
12	line 40? "The layers are partially connected to	
13	each other."	
14	Q Okay. So you are relying on the word	
15	"partially"?	11:39:58
16	A I'm relying on the word "partially," and	
17	also relying on whatever we talk yesterday from,	
18	say, Sukegawa, which was another piece of prior art	
19	which also been used in these proceedings to show	
20	that someone skilled in the art will know that you	11:40:12
21	have two layers separated by dielectric and you want	
22	to establish a connection between the two layers,	
23	you the means to do so is to open contact holes.	
24	And when doing so, you establish two	
25	parallel lines that are separated by a dielectric.	11:40:39

		Page 56
1	Every now and then the two lines are attaching each	
2	other, so the two lines have the same potential,	·
3	they form a lower resistance line than if you just	
4	have one or the two. And Shiba says that,	
5	furthermore, you also gain additional advantage, if	11:41:02
6	you say that one of the layers has a wiring defect,	
7	let's say there is a particle that it creates an	
8	opening, creates a discontinuity in that portion,	
9	the other layer, it is at a different level, it will	
10	continue carrying the signal and will bypass the	11:41:24
11	defect portion.	
12	Q May I ask you also about other	
13	possibilities?	
14	Does the passage the paragraph at column	
15	6, beginning at line 35 sorry 37 that you've	11:41:46
16	cited, the moreover paragraph	
17	A Yes.	
18	Q is it possible that that means that	
19	there are lines 127 formed of two different layers	
20	and that they are side by side, running in parallel,	11:42:02
21	except occasionally crossing over one another?	
22	MR. GIBSON: Objection; form.	
23	THE WITNESS: Well, if we look at the	
24	Figure 3, I don't think your statement is supported	
25	by the drawings in Figure 3.	11:42:42

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		Page 57
1	We see that all the lines of 127 originate	
2	from the pad 125a, I think 125a is referred to	
3	pad 125 is referred to as inter-connecting pad.	
4	And at column 6 it reads, line 5, "A first	
5	wiring line 127 is drawn from the inter-connecting	11:43:28
6	pad 125a."	
7	And we discussed earlier that some of those	
8	lines are in dotted lines and some of the lines are	
9	continuous lines. So 127, as you pointed out, it	
10	refers to those lines as with an element which	11:43:50
11	characterizes 127, that wiring is characterized by	
12	one number, 127.	
13	And the process that we read on column 6,	
14	line 127, the moreover passage, it says that 127 can	
15	be made as a two-layer lines where the two layers	11:44:15
16	will be partially connected.	
17	I cannot see any support in the drawing or	
18	in the package to say that some lines are made with	
19	one material and some other lines made with another	
20	material.	11:44:36
21	BY MR. MANZO:	
22	Q When is a	
23	A Go ahead.	
24	Q May I ask a question?	
25	A Yes. Go ahead.	11:44:49
	1	

		Page 58
1	Q When is the pixel electrode 251 made in	
2	this sequence?	
3	A Whatever is formed after the formation of	
4	the gate dielectric, because we have information of	-
5	the storage capacitor element, which was described,	11:45:55
6	and I read that section earlier, and we have that	
7	Figure 4 if you are referring to the to other	
8	layers, can you be a little bit more specific as to	
9	what layers you're referring to?	
10	My answer is it is after the gate	11:46:34
11	dielectric.	
12	Q Can you	
13	A After the gate dielectric, it's not quite	
14	clear.	
15	Q Can you tell whether it's before or after	11:46:49
16	the lower layer of 127 is established?	
17	A It definitely is after the lower level of	
18	127, because we see, in Figure 4, the lower level of	
19	127 being which is made with the same material	
20	and the scanning lines Yj, which are already laid	11:47:18
21	down, above that layer, the scanning line, you have	
22	the gate dielectric element 211. And Figure 4 shows	
23	the pixel electrode to be above the 211.	
24	So, clearly, it is made after the lower	
25	level of 127 has already been formed.	11:47:44

	Page 59
O Con way tall whather it is before or often	rage 33
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the second level of 127 is formed?	
A I cannot find any passage in the	
specifications in the text to list the order. And	
if you do have such passages, please point it out to	11:49:15
me so I will not be wasting your time.	
Q I'll ask my colleagues to jump into that	
A Sure.	
Q if there is a citation in Shiba that	
they are aware of. I don't think that they are, but	11:49:34
we'll see.	
Meanwhile	
A While your colleague is looking for that,	
if I might make a comment in your previous question?	
Q Yes. Which previous question?	11:50:04
A You, hypothetically, discussed that the	
wirings can be formed as separate lines that run in	
parallel, and each line may be formed in different	
layers; is that what	
Q That is a hypothesis.	11:50:19
A Hypothesis.	
And I said the record does not support	
that.	
Q Okay.	
A And I want to point out another piece of	11:50:28
	specifications in the text to list the order. And if you do have such passages, please point it out to me so I will not be wasting your time.  Q I'll ask my colleagues to jump into that A Sure. Q if there is a citation in Shiba that they are aware of. I don't think that they are, but we'll see.  Meanwhile A While your colleague is looking for that, if I might make a comment in your previous question? Q Yes. Which previous question? A You, hypothetically, discussed that the wirings can be formed as separate lines that run in parallel, and each line may be formed in different layers; is that what Q That is a hypothesis.  A Hypothesis.  A Hypothesis.  And I said the record does not support that. Q Okay.

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		Page 60
1	evidence in the record that does not support it.	
2	Q Please.	
3 .	A Sure. So if we look at Figure 6, it is one	
4	we have not referred to so far.	
5	Q Yes, Figure 6.	11:50:43
6	A In Figure 6 you see on the right-hand side	
7	a set of parallel lines which are grouped as a	
8	bracket that says "127."	
9	Q I see several side-by-side lines 127.	
10	A Right. And some of them are under the	11:51:08
11	sealant and some are inside of the sealant, to the	
12	left of the sealant.	
13	And those lines are made a single-layer	
14	line made from the data line material, because those	
15	lines are above the insulating layer 211. And they	11:51:31
16	are all grouped together.	
17	If you were to have a structure where some	
18	of the lines will be made by the data line material	
19	and some of the structures will be made from the	
20	scan line material, then the mask step, it will have	11:51:54
21	some data line lines and it will be but not	
22	all of them.	
23	So if we count the lines of 1 to the 7 in	
24	Figure 3, and if we count the lines in Figure 6, I	
25	think they are the same.	11:52:28

		Page 61
1	So it shows that as many lines as you have	
2	in the planar section view, you have in the	
3	cross-sectional view, which means that the masking	
4	step for when you pattern the data line material, it	
5	includes all lines.	11:52:55
6	And, likewise, if you pattern the scanning	
7	line material, you pattern all lines, so then the	
8	wiring 127, as shown in Figure 3 and in Figure 6,	
9	they all constitute one cannot be referred with	
10	one numeral, 127.	11:53:15
11	Q Okay. So in Figure 6 you pointed out that	
12	all the lines 127 sit on top of the gate dielectric	
13	or the insulator 211; is that right?	
14	A That is what is shown.	
15	Q Okay. And you don't see a lower layer	11:53:39
16	there, do you, of conductor 127?	
17	MR. GIBSON: Objection; form.	
18	THE WITNESS: Well, Shiba talks about	
19	multiple embodiments and, particularly, this line	
20	127, it says line 30 column 6, line 31, "the	11:54:04
21	first wiring line 127 can be formed in the same step	
22	for forming the data lines Xi."	
23	So that particular embodiment in Figure 6	
24	depicts the embodiment that is made with first	
25	with the material of the data lines.	11:54:49

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		Page 62
1	But further down in column 6, in line 34 it	
2	says that, "further, depending on the kind of TFTs,	
3	the aforementioned wiring lines can be formed in the	
4	same step performing the scanning lines Yj." That's	
5	another embodiment.	11:55:16
6	And continuing the same column 6, line 37,	
7	we'll go into the passage that reads, "Moreover, the	
8	first wiring lines 127 may be formed in the step of	
9	forming the scanning lines and the data lines."	
10	So someone skilled in the art will see	11:55:36
11	there are three different embodiments that are	
12	disclosed by Shiba that you can make them with the	
13	scan line material, the data line material or as a	
14	two-layer structure that you use both materials.	
15	BY MR. MANZO:	11:55:49
16	Q The wirings 127 are part of the same layer	
17	or layers as 125a, I think you testified, and	
18	those that connects to lines 123-1 in Figure 3?	
19	And those, I think you said, are also a part of the	
20	same layer as 751 and 731 and 121-1.	11:56:59
21	Do you recall that testimony?	
22	A Well, I read you from Shiba. This is what	
23	Shiba is teaching	
24	Q Okay.	
25	A that all these elements are made from	11:57:22

		Page 63
1	the same metal layer.	
2	Q So pad 751 will be made at the same time in	
3	the same way as what you've described; is that	
4	correct?	
5	MR. GIBSON: Objection; form.	11:57:34
6	BY MR. MANZO:	
7	Q Or put it a different way, would pad 751 be	
8	made differently?	
9	A Well, the pad is we refer to the pad as	
10	a region. And in that region, like in 127 region,	11:57:56
11	you may have one layer or multiple layers; for	
12	example, we see 127 have multiple layers.	
13	Now, the 751 will include, as a minimum,	
14	the material made by the data lines as shown here in	
15	the preferred embodiment of Shiba.	11:58:31
16	Q Are you referring to a figure there or	1
17	when you say "here," what do you mean?	
18	A I'm looking at the passage that we read	
19	earlier.	
20	Q Okay. The column 6 "Moreover" passage?	11:58:48
21	A No. A little bit above that one in column	
22	6, line 27 reads, "The power supply pads 731 to 738,	
23	the common pad 751, the third wiring lines 121-1 to	
24	121-4, the inter-connecting pad 125a, and 125b the	
25	second wiring lines 123-1 to 123-4, and the first	11:59:26

		Page 64
1	wiring line 127 can be formed in the same step of	
2	forming the data lines Xi."	
3	So all those regions are formed in the same	
4	step as forming the data lines.	
5	Now, the "moreover" passage below in the	11:59:47
6	same column 6, it shows that the 127 can be made as	
7	a two-layer structure.	
8	So the thing in Shiba is that all these	`
9	regions are made from the data line materials, that	
10	some regions, some elements, can you made as a	12:00:19
11	two-layer structure.	
12	Q And in the paragraph from which you were	
13	reading in column 6, do you see a reference at line	
14	37 to "not increasing the number of manufacturing	
15	steps"?	12:01:11
16	A I see that.	
17	Q Is that important, in general, to	
18	semiconductor manufacturing?	
19	A Do not increase the number of manufacturing	
20	steps keeps cost down.	12:01:29
21	Q So more manufacturing steps means more	
22	cost; is that right?	
23	MR. GIBSON: Objection; form.	
24	THE WITNESS: In general, unless your	•
25	additional manufacturing steps improve yield or	12:01:48

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		Page 65
1	improve performance, and they may be worth the	
2	additional thing because you may make a better	
3	product which you sell it at the premium, so it	
4	doesn't mean that you ideally, if you can get	
5	advantages in yield and in performance without	12:02:06
6	increasing the manufacturing steps, you get the best	
7	of both worlds.	
8	So, for example, the wiring structure 127	
9	being a double-layer structure, it reduces the	
10	defects and also increases the resistance sorry,	12:02:51
11	decreases the resistance. Excuse me. Decreases the	
12	resistance.	
13	Shiba teaches giving the signal to the	
14	counter electrode is very critical, and those points	
15	are looked at very at distances far away from the	12:03:20
16	terminal parts.	
17	So having a wiring that will distribute the	
18	signal to the counter electrode in such a way that	
19	it will be able to modulate the potential of the	
20	counter electrode fast is essential for	12:03:40
21	high-performance displays.	
22	And he lists examples of different driving	
23	things that can be implemented because the wiring	
24	structure that he has is a low-resistance structure.	
25	BY MR. MANZO:	12:04:05

		Page 66
		rage oo
1	Q Just to be clear, does this two-layer	
2	structure for wiring lines, or line 127, according	
3	to Shiba, result in additional manufacturing steps	
4	or not?	
5	A It says, "If the layers are partially	12:04:19
6	connected to each other, the wiring defect can be	
7	prevented and the manufacturing yield can be	
8	improved" are you referring to that part?	
9	Q No.	
10	A "The structure of the present invention,	12:04:40
11	therefore, does not increase the number of	
12	manufacturing steps"?	
13	Q That's the part.	
14	That's at column 6, line 32, approximately.	
15	A I don't believe it increases the	12:05:03
16	manufacturing steps.	
17	Do you have evidence that it does?	
18	Q I just wanted to see if you have that	
19	understanding, Professor.	
20	A It is my understanding, based on the	12:05:19
21	teaching of Shiba, the drawings and the	
22	specifications and what I thought, was someone	
23	skilled in the art will also understood at the time	
24	of the invention that the double-layer structure can	
25	be implemented without increasing complexity,	12:05:36

	Page 67	
1	without adding additional manufacturing stems.	
2	I do not see evidence in the record that	
3	shows the opposite will be true.	
4	Q Very well.	
5	A little while ago you referred to 12:07:	01
6	Figure 6.	
7	A Yes.	
8	Q Does Figure 6 show that the protective	
9	overcoat 241 is above the lines 127?	
10	A This is what is depicted in Figure 6. And 12:07	27
11	241, column 4, line 29, it specify what it is and,	
12	quote, protective overcoat 241 made of silicon	
13	nitride is arranged in the TFT 221 and around the	
14	pixel electrode 251.	
15	And that's the second insulating film. 211 12:08	23
16	is the first insulating film in the sequence of	,
17	fabricating this structure.	
18	Q Professor, could you turn to paragraph 69	
19	of your declaration.	
20	Does paragraph 69 indicate that a person of 12:09	30
21	ordinary skill would modify Shiba in some way?	
22	A The paragraph 61 says the person with	
23	ordinary person with skill in the art can modify	
24	Shiba to form pads that will include a transparent	
25	conducting film, such as indium tin oxide, because 12:10	:39

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		rage oo
1	that material is well known to resist oxidation and,	
2	thus, will result in reliable connection.	
3	Two-layer structure has already been taught	
4	by Shiba	
5	Q Yes. Go ahead.	12:11:10
6	A within the context of forming the wiring	
7	lines 127.	
8	So two-layer structures have been shown in	
9	some portions of display to be advantageous.	
10	Extending the teaching of Shiba, one can view the	12:11:34
11	pad 751 as a two-layer pad and use different layers	
12	to gain the advantages of improved reliability.	
13	Q I just want to try to understand that	
14	better. Does the pad 751 have already the two	
15	layers formed from the scan line material used to	12:12:16
16	make the lines Y and the data line material used to	
17	make the lines X? And are you now changing one of	
18	those or putting something on top of that?	
19	MR. GIBSON: Objection; form.	
20	THE WITNESS: Well, Shiba is teaching that	12:12:39
21	751 is a single layer and that element 127 is a	
22	double layer.	
23	Shiba does not say that the 751 has a lower	
24	layer made of the scanning line and an upper layer	
25	made of the data lines. That's something that	12:13:06

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		Page 69
1	someone skilled in the art can implement based on	
2	the teaching, but Shiba does not disclose that part.	
3	BY MR. MANZO:	
4	Q Okay. So how would you form the two-layer	
. 5	pad structure, according to paragraph 69 of your	12:13:26
6	declaration? How would a person of ordinary skill	
7	in the art do that?	
8	A Someone ordinarily skilled in the art will	
9	have a two-layer structure in 751 where the lower	
10	layer is made still is made with the data line	12:13:54
11	material, but the upper layer is made by the indium	
12	tin oxide layer.	
13	Q Now, would there be dielectric on top of	
14	the data line material?	
15	A Yes, there is this dielectric of 241, I	12:14:15
16	believe yes, 241. So there will be openings in	
17	that layer 241. So the pad so the indium tin	
18	oxide can make a contact to the metal layer or, as	
19	we discussed several times, there is the data line	
20	material and then there is the insulating layer 241	12:15:13
21	and there is the indium tin oxide layer. So you can	
22	put the 751 down, you put the 241 down, you have	
23	openings, and then you put the ITO layer.	
24	Alternatively, you so that's one way to	
25	make the structure.	12:15:44

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		Page 70
1	Q Would this require an additional step of	
2	adding indium tin oxide at that location on pad 751?	
3	A There is already indium tin oxide used as	
4	the pixel electrodes.	
5	Q Are you saying no additional step would be	12:16:12
6	needed?	
7	A You asked me earlier about the order of the	
8	pixel electrode, and I did not find evidence in the	
9	specifications, and I asked for your help to point	
10	me in that direction, at least where the ITO is	12:16:40
11	relevant to to the data line material.	
12	There is a layer there is a pixel	
13	electrode layer and there is openings in 241 that	
14	are created and there is a data line material. And	
15	so you have all the steps there in making the	12:17:11
16	structure.	
17	Q I don't think that answers my question.	
18	Are you saying that this modification to	
19	Shiba to include ITO on top of the pad 751 can be	
20	done without adding additional steps?	12:17:53
21	A Someone skilled in the art will conclude	
22	that those steps can be made without adding steps.	
23	Shiba is not clear how the pixel electrode	
24	layer when the pixel electrode layer is formed.	
25	So depending upon where the pixel electrode is	12:18:22

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		Page 71
1	formed, you don't even you change nothing in the	
2	process, or depending on where the pixel electrode	
3	layer is formed, you change the ordering with some	
4	of the the order with which some of the steps are	
5	formed. But you not add in additional manufacturing	12:18:40
6	steps, you just simply change the order.	
7	And I did not see evidence in the	
8	specification. I asked for your help, and I did not	
9	see whether you have a clear answer where is the	
10	pixel electrode base in the teaching of Shiba in the	12:18:58
11	specifications.	•
12	Q Just so I understand your testimony of the	
13	declaration, in paragraph 48, you said that the	
14	wiring lines 127 can include a two-layer structure	
15	of an auxiliary bottom line that's conductive and	12:20:35
16	you've testified that there would be dielectric on	
17	top of it with openings, and then an upper	
18	conductive layer formed by the data line material	
19	that is partially connected through these openings	
20	to the scan line material forming the lines 127.	12:21:01
21	That's what I understand from your testimony so far.	
22	Is that correct is that understanding	
23	correct?	
24	A Well, the auxiliary lines at the bottom	
25	will be made by the scan line material. They will	12:21:54

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		Page 72
1	be insulating layer on top of that with openings.	
2	In the second upper conductive layer they	
3	will form the external connection lines that will be	
4	made by the data line material.	
5	Q Okay. So that's your testimony. And I	12:22:22
6	think that	
7	A This is what I described in paragraph 48.	·
8	Q Okay. And I think I understand your	
9	testimony.	
10	And then I asked you here a little while	12:22:32
11	ago whether that structure also applies to the pad	
12	751, and you indicated, I thought, that it does not,	
13	and you referred to the bit in column 6, I think,	
14	where the patent says that the pad is made of the	
15	same material in the data line material X; is that	12:23:03
16	correct?	
17	A Well, paragraph 48, and the portions we	
18	discussed earlier from the specifications, refers to	
19	wiring lines 127 being double-layer lines. So	
20	within that context there are two points I want to	12:23:33
21	make. One is that the patent is saying that it is	
22	double-layer structures, consisting of the scan line	
23	material and data line materials applies to 127, at	
24	least this is what is disclosed in the preferred	
25	embodiment of Shiba.	12:23:59

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		Page 73
1	And the other elements, 751, 121, 125, are	
2	referred to as a single-layer structure.	
3	The paragraph 38 and the context in the	
4	teaching of Shiba, there is something there is a	
5	general teaching that emerges out of the	12:24:26
6	construction of 127.	
7	And the general teaching of the	
8	construction of 127 is that two-layer structures are	
9	advantages. If you can form them without adding	
10	steps and if you gain advantages, that will improve	12:24:43
11	the manufacturing yield, will reduce defects.	
12	So there is a specific teaching for a	
13	specific wiring, but there is also a general	
14	teaching that someone skilled in the art will see	
15	that can apply to maybe other structures.	12:25:05
16	The general teaching is that the structure	
17	can be made, in general, with two layers. And the	
18	specific teaching for making the wiring 127 applies	
19	to wiring lines 127.	
20	One can do that exact sequence of layers to	12:25:29
21	other parts or it can change and use different	
22	layers to make other parts and use other materials	
23	which already used in the manufacturing process and	
24	mix and match the layers in order to gain	
25	advantages.	12:25:53

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		Page 74
1	After all, there are three layers that are	
2	reported in Shiba, the three conductive layers.	
3	There is the conductive layer made of the scanning	
4	line material, there is the conductive layer made of	
5	the data line material and there is a conductive	12:26:12
6	layer made with the transparent indium tin oxide.	
7	So you can mix and match which layers you	
8	want to use to make a two-layer structure in order	
9	to gain some advantages in that particular region	
10	that you want to implement that two-layer structure.	12:26:31
11	In forming a long wiring line, just as in	
12	127, to provide signals to these counter electrode	
13	pads, which are distributed along the periphery, far	
14	away from the common pad 751, it makes sense, as	
15	Shiba specifically disclosed, that that wiring has	12:26:55
16	been made with the data line and the scanning line	
17	materials. But the general teaching of making	
18	two-layer structure is there.	
19	So in the common pad region, someone	
20	skilled in the art will see that you can maintain	12:27:17
21	the data line material from Shiba and introduce the	
22	pixel electrode above it in order to gain advantages	
23	in corrosion protection.	
24	Q Would a person ordinarily skilled in the	
25	art want to consider and weigh the disadvantages, if	12:28:25

		Page 75
1	there are any, of a process sequence in which the	
2	indium tin oxide is added on top of pad 751 after	
3	the formation of the protective overcoat 241?	
4	MR. GIBSON: Objection; form.	
5	THE WITNESS: And what are these	12:29:29
6	disadvantages?	
7	BY MR. MANZO:	
8	Q Well, if there are any, would they have to	
9	be weighed?	
10	MR. GIBSON: Objection; form.	12:29:52
11	THE WITNESS: I did not see any	
12	disadvantages. If you have see something, if you	
13	have evidence of that there is a disadvantage,	
14	can you elaborate so I can answer your question?	
15	BY MR. MANZO:	12:30:02
16	Q Well, Professor, I'm not the expert here	
17	testifying. You're the expert at this, and I'm just	
18	trying to understand and explore the testimony	
19	you've given and the consequences of it.	
20	So if I ask some dumb questions, I'll hope	12:31:03
21	that you'll forgive my ignorance. Maybe my	
22	questions are not sensible and maybe they are, but	
23	together we are trying to arrive at the truth, so	
24	let me press on.	
25	A Let me comment that, first, your questions	12:31:44

		Page 76
1	are not, what you say, dumb questions, I do not find	
2	your questions to be dumb. And I agree with you	
3	that we need to derive to the truth of the matter	
4	and I understand that you may also have experts that	
5	help you in understanding those structures that may	12:32:10
6	have more knowledge and skill than you, as an	
7	attorney, may have on the subject matter. So I	
8	think some of your questions may be guided by	
9	information you receive by the experts, so I am	
10	in some of my questions towards you, I'm looking for	12:32:34
11	things that you or your colleagues or your experts	
12	see and read in the specifications so then I can	
13	respond and provide comments to those evidence that	
14	they maybe see.	
15	And it was my testimony earlier that I do	12:32:57
16	not see any disadvantages. If you have any	
17	information for such disadvantages that you have	
18	formulated yourself or your colleagues or your	
19	experts, please point it out to me so we can discuss	
20	them and then we'll derive to the truth, as you	12:33:13
21	said.	
22	Q Well, let me try.	
23	Looking at Figure 4 of Shiba, we have a	
24	gate dielectric layer 211 that you've pointed out,	
25	right?	12:33:49

	Page 77
A Yes.	
Q Now, I see number 211 at the right-hand	
side of Figure 4. Let me just ask one question for	
now before we break. Can you tell how far to the	
left in Figure 4 that layer 211 extends?	12:35:26
A From the drawing?	
Q Yes. Or, if you want to consult any other	
part of the specification, that's fine also.	
A Well, if you have evidence that will help	
me, we'll save time. If you want to direct me to	12:36:00
something, to some point you would like to make	
Q Well, frankly, you know, I almost need a	
microscope or magnifying glass to tell this.	
I just wonder if, in your expertise, maybe	
you know the answer and can help me with that.	12:36:18
A Well, Figure 4 is kind of crowded picture,	
and 211 is labeled on the right. And you asked me	
how far it extends to the left, if I understand your	
question?	
Q Right. Yes.	12:36:41
A In my analysis I use Figure 6 where it is a	
bit more clear where 211 is on with respect to	
the sealant. I presume you would like to find out	
where 211 is relevant to the sealant, because the	
sealant is towards the left. Is that what you're	12:37:13
	Q Now, I see number 211 at the right-hand side of Figure 4. Let me just ask one question for now before we break. Can you tell how far to the left in Figure 4 that layer 211 extends?  A From the drawing?  Q Yes. Or, if you want to consult any other part of the specification, that's fine also.  A Well, if you have evidence that will help me, we'll save time. If you want to direct me to something, to some point you would like to make  Q Well, frankly, you know, I almost need a microscope or magnifying glass to tell this.  I just wonder if, in your expertise, maybe you know the answer and can help me with that.  A Well, Figure 4 is kind of crowded picture, and 211 is labeled on the right. And you asked me how far it extends to the left, if I understand your question?  Q Right. Yes.  A In my analysis I use Figure 6 where it is a bit more clear where 211 is on with respect to the sealant. I presume you would like to find out where 211 is relevant to the sealant, because the

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		Page 78
1	trying to	
2	Q Yes.	
3	A And I can see it clearly in Figure 6. And	
4	I would assume that apply to the other side.	
5	Q In Figure 6, just to be clear, the display	12:37:32
6	region is on the left-hand side of Figure 6 and the	
7	terminal portion is on the right-hand side? Do I	
8	have that correct?	
9	A Figure 6 is one of those sides that does	
10	not have a terminal portion.	12:37:48
11	Q Oh.	
12	A As you see from Figure 1, the terminal	
13	portions are on two sides, and there are two other	
14	sides that did not have terminal portions. I	
15	believe that applies to the side that does not have	12:38:02
16	a terminal portion.	•
17	Q Okay. So to rephrase, in Figure 6 does the	
18	left-hand side of the sealant represent the	
19	display part of the display area and let's	
20	just end the question there.	12:38:26
21	Perhaps that's a little unartful. Let me	
22	rephrase it.	
23	In Figure 6, is the display area generally	
24	to the left of the sealant 113?	
25	A By "display area," I presume you are	12:38:42

		Page 79
1	referring to the array where you have the pixel and	
2	the electrodes, and the answer is, yes, it is to the	
3	left of 113, and you can see where the two	
4	substrates, 200 and 500, because the sealant 113	
5	that contains and seals the liquid crystal that is	12:39:06
6	the within the display area.	
7	Q Okay. So based on that, you've indicated	٠.
8	that Figure 6 shows the dielectric 211 extending	
9	from the array area of the display area beyond the	
10	sealant to a region outside the sealant, yes?	12:39:43
11	A That's correct.	
12	Q And would that tell you that in Figure 4 of	
13	the same orientation or configuration for that	
14	region 211, dielectric 211 persists or exists?	
15	A I think it is shown in Figure 4.	12:40:15
16	Q I'm sorry. Did you say, "I think it is	
17	shown in Figure 4" or did you say "I think it is so	
18	in Figure 4"?	
19	A "It is shown."	
20	Q Okay.	12:40:58
21	A Earlier in this questioning you said that	
22	you had trouble seeing 211 to the left, and then I	
23	refer you to the Figure 6, which I said would	
24	equally apply to Figure 4. And then we agree that	
25	it is shown clearly in Figure 6. And I'm looking at	12:41:16

		Page 80
	Figure 4. Without the help of magnifying lens, I	
1		
2	believe it is shown that it is there and extends all	
3	the way out to the edge.	
4	Q Okay. Well	
5	A And it is below element 751.	12:41:40
6	MR. MANZO: Maybe we can stop here for	
7	lunch.	
8	THE VIDEOGRAPHER: This is the end of tape	
9	1. We are off the record at 12:41 p.m.	
10	(Recess.)	01:29:32
11	THE VIDEOGRAPHER: This is the beginning of	
12	tape 2. We are back on the record at 1:30 p.m.	
13	BY MR. MANZO:	
14	Q Professor, did you discuss your testimony	,
15	with anybody over the lunch hour?	01:30:21
16	A No.	
17	Q Okay. We were talking, before the recess,	
18	about including a transparent conductive film, such	
19	as ITO, for the top layer of pad 751.	
20	Do you remember that?	01:31:00
21	A Yes.	
22	Q And I believe you rely on Sukegawa for that	
23	motivation; is that correct?	
24	A Yes.	
25	Q I just want to make clear that we're in	01:31:17

		Page 81
1	agreement that Shiba does not refer to using a	
2	transparent conductive layer for this specific	
3	purpose; is that right?	
4	A Shiba is teaching wirings that can be made	
5	or structures that can be made as two layers, where	01:31:42
6	each layer has been formed from a different	
7	material. The particular embodiment discussed in	
8	Shiba in the common pad region, 751, it depicts only	
9	a single layer, which is made of the material of the	4
10	data lines.	01:32:15
11	Q Are the data lines and the scanning lines	
12	made of different materials or are they the same	
13	material?	
14	A Made at two different times. It is two	
15	different deposition process, and they are separated	01:32:38
16	by a dielectric layer 211, as we discussed earlier.	
17	I do not recall, and please help me if you	
18	recall, where the materials of the data lines and	
19	scanning lines are referred explicitly what they are	
20	in Shiba.	01:33:43
21	Q Maybe I can rephrase, whether you recall	
22	any indication in the Shiba patent of whether the	
23	data lines and the scanning lines are different in	
24	material.	
25	A My recollection and a brief scan, they are	01:35:47

		Page 82
1	referring them as scanning line and a data line, but	
2	do not go into detail what material constitutes each	
3	line.	
4	If you are asking me in general	
5	Q Let me ask you in general.	01:36:04
6	A what material are used, they can be the	
7	same or they can be different.	-
8	For example, in Sukegawa that I'm using	
9	there is specific mentioning of what material they	
10	can be used, and they also provide a list of	01:36:36
11	additional materials of and the list includes	
12	chromium, molybdenum, aluminum, tantalum, to name a	
13	few.	
14	Q I understand your answer, but focusing	
15	specifically on Shiba, then you're not aware of any	01:37:21
16	specific teaching in Shiba, are you, of using	
17	different materials for the X lines and the Y lines?	
18	A My recollection, and I cannot find I	
19	cannot find any mentioning right now what are the	
20	materials for the scanning lines and data lines.	01:37:48
21	I see a reference to another component, the	
22	light shielded matrix layer, which is used to	
23	make which is made from chromium on the other	
24	substrate, the 500 substrate, but I do not see a	
25	reference on material for the scanning lines and	01:38:16

		Page 83
1	data lines.	
2	So I cannot tell you if they are the same	
3	or different or what is the material based on Shiba,	
4	unless you have portions that you want to guide me.	
5	Q I don't have an indication myself.	01:38:34
6	Is there any suggestion that the data lines	
7	or the scanning lines are made of ITO in Shiba?	
8	A No. They are not they are not referred	
9	as made of ITO. ITO, it refers specifically for	
10	the use of the pixel electrode and the counter	01:39:02
11	electrode.	
12	In general, you will not form scanning	
13	lines and data lines for a large display. And I	
14	believe this is a 14-inch diagonal display. You	2
15	will not form long lines, such as the scanning lines	01:39:35
16	or data lines, using ITO. Those long lines will be	
17	resisted.	
18	Q Okay.	
19	A In Shiba it mentions somewhere that ITO is	
20	a high-resistance material and only use it for the	01:40:06
21	counter electrode and the pixel electrode.	
22	So you not form long lines with ITO, but	
23	you can certainly form a second layer, because in	
24	that case you only have to transverse the thickness	
25	of the layer. You do not actually, the length	01:40:37
	1	

		Page 84
1	that you're transversing through that layer is very,	
2	very small.	
3	And the cross section of that area will be	
4	large, because it will be the cross section of the	
5	sum of all the openings in that area.	01:40:58
6	Q All right. So do you agree that the only	
7	mention of transparent conductive layer in Shiba is	
8	as pixel electrodes 251?	
9	A And as a counter electrode 541, column 4,	
10	line 42.	01:41:47
11	Q Anything else?	
12	A Not in this preferred embodiment, not in	
13	this embodiment.	
14	Q Well, I don't mean just in this embodiment.	
15	I'm talking about in the whole patent.	01:42:14
16	A I mean, in the embodiment described in the	
17	specifications. The patent provides a description	
18	of certain embodiments. And out of that examples,	
19	there is a teaching that is conveyed to the persons	
20	of ordinary skill in the art, and that teaching go	01:42:45
21	beyond the specific details and specific structures	
22	used in the specific in the particular	
23	embodiments used to illustrate.	
24	Q Okay. So you don't find any such teaching	
25	in the description of embodiments or anywhere else	01:43:12

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		Page 85
1	in this patent?	
2	A What teaching?	
3	Q Of using a transparent conductive layer for	
4	anything other than a pixel electrode or the	
5	electrode 541, the counter electrode; is that right?	01:43:37
6	A Well, what I found is that in there are	
7	three conductive layers. One conductive layer is	
8	the transparent pixel electrode that is mentioning	
9	to be used as the pixel electrodes and the counter	
10	electrodes. But I also found find, and someone	01:44:19
11	skilled in the art also finds, the teaching that you	
12	can use multiple layers out of these three layers,	
13	and there is a specific example between two such	
14	layers that you can make a double-layer structure	
15	using the existing conductive layers.	01:44:50
16	So though a specific embodiment there is	
17	no specific embodiment that utilizes the pixel	
18	electrode sorry utilizes the transparent	
19	conductive layer specifically for other purposes,	
20	the general teaching of combining existing layers to	01:45:12
21	form conductive structures is there.	•
22	And in light of other prior art, such a	
23	combination of layers will be obvious.	
24	Q Excuse me for a second.	
25	There is so much that we covered yesterday	01:47:59
	1	

		Page 86
1	about Sukegawa, that it would be just covering the	
2	same ground over and over again, which I don't think	
3	we want to do.	
4	If we can use yesterday's testimony on this	
5	topic, it will shorten this deposition a lot.	01:48:18
6	MR. GIBSON: I don't think you need to ask	
7	the same questions again. I don't have any issue	
8	with that. I assume that we'll have the same	
9	ability when we take your experts on these too.	
10	MR. MANZO: Right.	01:48:36
11	MR. GIBSON: With that understanding, I	
12	think you can use any of the questions from	
13	yesterday relating to Sukegawa in this petition as	
14	well, with the understanding we'll have the same	
15	ability to do that.	01:48:48
16	MR. SCHLITTER: We'll be accommodating.	
17	MR. MANZO: Okay. Thank you.	
18	Q Now, do you remember Sukegawa, Professor?	•
19	A I do.	
20	Q And just to refresh, Sukegawa, in Figure	01:49:24
21	1B, for example, shows that the transparent	
22	conductive layer 8 is directly on top of the upper	
23	metal layer wiring 7, right?	
24	A Yes. May I have Sukegawa reference?	
25	Q There is the figure.	01:49:56

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		Page 87
1	A Are you planning to cover that	
2	Q Here is a copy of Sukegawa patent that was	
3	marked as Exhibit 1005 in this proceeding.	
4	MR. GIBSON: Thank you.	
5	BY MR. MANZO:	01:50:12
6	Q And my question was just to refresh your	
7	recollection, I think, that 8 is directly on top of	
8	7, as shown in Figure 1B.	
9	A 8 is laid over 7.	
10	Q Okay. Now, if a person of ordinary skill	01:50:47
11	in the art were to modify Shiba, in light of	•
12	Sukegawa, to provide a transparent conductive layer	
13	over the common pad 751 of Shiba, would the ITO be	
14	provided or would the transparent conductive	
15	layer be provided directly on the common pad 751?	01:51:19
16	A Well, as we discussed quite extensively	
17	yesterday, on top of the layer 7 there are two	
18	layers, 8 and 9. One is the transparent conductive	
19	layer and the other is the insulating layer 9.	
20	There are two and only two possible ways	01:51:49
21	that one can lay down those films on top of 7.	
22	You can put 8 to touch 7 and 9 over 8 or	
23	you can put 9 above 7 and 8 above 9.	
24	So if one wants to take those two	
25	processing options and import those in Shiba to	01:52:22

		Page 88
1	modify Shiba terminal pads, common pads, 751, they	
2	can be modified in any one of those two ways. I	
3	don't think it's been limited to one or the other	
4	because the goal is to have the ITO layer be the	
5	layer that will be in between the flexible printed	01:52:57
6	circuit board and the external connection line.	
7	Whether you put 8 on top of the external	
8	connection line and then you have the passivation	
9	layer and then form the opening or vice versa, you	
10	have the passivation layer, form the opening and	01:53:24
11	then put the transparent conductive layer, to	
12	someone skilled in the art, both options will be	
13	obvious and whichever they choose to do, they will	
14	be equally valid for the particular embodiments that	
15	they wish to particular products that they wish	01:53:42
16	to make.	
17	Maybe there are some specific reasons for	
18	that particular product to choose one or the other,	
19	but they are equally valid they are equally sound	
20	approaches, and they are both obvious.	01:54:01
21	Q Okay. I understand. Is there anything in	
22	Sukegawa that suggests flipping the order of layers	
23	as between 8 and 9 with respect to layer 7?	
24	MR. GIBSON: Objection; form.	
25	BY MR. MANZO:	01:54:38
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		Page 89
1	Q I'll rephrase the question.	
2	Is there anything in Sukegawa that suggests	
3	that the person ordinarily skilled in the art could	
4	deposit layer 9 directly on top of layer 7 and then	
5	put transparent conductive film 8 on top of 9,	01:55:16
6	presumably after 9 were etched? Is that actually	
7	taught in Sukegawa?	
8	A Well, you see that teaching when you have	
9	to connect 2 to 7 and you have 3, which is	
10	insulating layer in between.	01:55:46
11	So in connecting 2 and 7, and you have an	
12	insulating layer in between, you opening the contact	
13	holes and you establish regions where the upper	
14	layer will touch the lower layer.	
15	So if you have, again, two layers, an	01:56:20
16	insulating and a conducting, that you want to lay	
17	over another conducting layer, isn't that obvious in	
18	light of the underlying structure, namely, between	
19	2, 3 and 7, isn't that structure similar to 7, 8, 9,	
20	flipping the sequence of the orders and putting the	01:56:49
21	opening and doing the etching to form the opening	
22	first versus last?	
23	They are teaching in Sukegawa how to	
24	connect lines, two conducting lines, with an	
25	insulating layer in between is within Sukegawa, and	01:57:13

		Page 90
1	that's no different connecting 7, 8 combining 7,	
2	8 and 9.	
3	I did not recall if the claims in Sukegawa	
4	are broad enough that it can cover both embodiments.	
5	Q In Sukegawa, are layers 2 and 7 made of the	01:59:16
6	same material?	
7	A In the detailed description of the	
8	preferred embodiment, it reads on column 4, line	
9	10, it reads, "a chromium film is an opaque	
10	conductive film that is deposited to a film	01:59:59
11	thickness of 149 nanometer on the entire subsurface	
12	of the glass substrate 1 by spattering process	
13	followed by being patterned to form a lower layer	
14	metal wiring 2."	
15	So there is a specific disclosure that the	02:00:18
16	lower wiring is chrome. And then further down in	
17	column 4 on line 46 it reads that another chromium	
18	film is opaque conductive film, is deposited to a	
19	film thickness of 140 nanometers so as to cover the	:
20	inter-layer insulation film 3.	02:00:51
21	So in this preferred embodiment, they are	
22	both described to be as chromium. That is another	
23	section on column 8, at line 55 that it reads,	
24	"While the present invention has been explained with	
25	reference to the preferred embodiment, the invention	02:01:25

		Page 91
1	is not restricted only to such embodiments, but	
2	various modifications are possible within a range	
3	not departing the purpose of the present invention.	
4	For instance, the lower layer metal wiring and the	
5	upper layer metal wiring are made of chrome, but,	02:01:52
6	instead, a single layer or a composite layer, such	
7	as made of aluminum, tantalum, molybdenum and	
8	tungsten may be used. Further, the materials for	
9	the lower layer metal wiring and the upper layer	
10	wiring may not always be identical."	02:02:18
11	So this paragraph in this section in the	
12	first sentence it says that multiple that	
13	variations and modifications can be implemented, and	
14	switching the order with which 8 and 9 falls within	
15	the scope of Sukegawa will result in the same	02:02:57
16	benefits.	
17	And the second sentence gives an example	
18	that is referring to switching the materials used	
19	for the wirings 2 and 7, or changing the materials	
20	or referring to the selection of materials that	02:03:31
21	could be used to form wirings 2 and 7.	
22	Q Thank you. Just to make sure that I	
23	understand you, it is your testimony that switching	
24	8 and 9 relative to 7 is suggested by the first	
25	sentence in column 8 beginning at line 55? Is that	02:04:05

	Page 92
what you said?	
A I said the invention is not restricted only	
to such embodiments.	
So there are several embodiments described	
in Sukegawa, and this sentence say that the	02:04:25
invention is not restricted only to such	
embodiments, but various modifications are possible.	
So it is my testimony that someone skilled	
in the art, switching the order of 8 and 9, it will	
have been obvious, particularly because making a	02:04:49
connection between 8 and 7 with the layer 9 in	
between 8 and 7, you simply replicate the process	
sequence that is used in connecting 2 and 7 where 3	
is in between.	
Q Forgive me if I'm taking a bit of time to	02:06:50
formulate my next question, but I'm skipping a lot	
in light of yesterday's testimony.	
MR. GIBSON: That's always appreciated.	
THE WITNESS: Let me take a one-minute	
break.	02:07:09
MR. MANZO: Sure.	
THE VIDEOGRAPHER: Off the record at 2:07	
p.m.	
(Recess.)	
THE VIDEOGRAPHER: Back on the record at	02:14:36
	A I said the invention is not restricted only to such embodiments.  So there are several embodiments described in Sukegawa, and this sentence say that the invention is not restricted only to such embodiments, but various modifications are possible.  So it is my testimony that someone skilled in the art, switching the order of 8 and 9, it will have been obvious, particularly because making a connection between 8 and 7 with the layer 9 in between 8 and 7, you simply replicate the process sequence that is used in connecting 2 and 7 where 3 is in between.  Q Forgive me if I'm taking a bit of time to formulate my next question, but I'm skipping a lot in light of yesterday's testimony.  MR. GIBSON: That's always appreciated.  THE WITNESS: Let me take a one-minute break.  MR. MANZO: Sure.  THE VIDEOGRAPHER: Off the record at 2:07 p.m.

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		Page 93
1	2:14 p.m.	
2	BY MR. MANZO:	
3	Q Professor, I invite your attention to the	
4	'204 patent.	
5	I may have asked you similar questions	02:15:11
6	yesterday, but if you look at Figures 4A and 4B, do	
7	you see that the ITO 114 is not covered anywhere by	
8	the resin inter-layer film 113?	
9	A I see that in Figure 4A and 4B.	
10	Q Is there any advantage to that that you	02:15:56
11	recognize?	
12	A Such as?	
13	Q Well, I'm just trying to understand what	,
14	you recognize.	
15	A As I said, there are two layers that need	02:16:38
16	to be placed over 403. One way is to put the resin	
17	inter-layer film first, then create form the	
18	openings, and then put 114, as shown here.	
19	Q Would a person ordinarily skilled in the	
20	art in 1997 have recognized an advantage to doing it	02:17:19
21	one way or the other way?	
22	A Figure 4A shows us one way and Figure 1B in	
23	Sukegawa they are referring shows a different way.	
24	These are the two possible ways to implement them.	
25	It is my opinion, and reading the	02:17:51

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		Page 94
1	specifications of both patents, neither inventor	
2	wanted to limit to one or the other. They described	
3	a particular embodiment.	
4	Now, what someone skilled in the art	
5	chooses to do will depend upon a lot of details that	02:18:13
6	will be applicable to the selection, to the flow in	·
7	manufacturing lines that they have.	
8	As I said a number of times, there are only	
9	two options, and inventors of one do it one way in	
10	their preferred embodiments, and perhaps inventors	02:18:41
11	of a later patent choose to do different from the	
12	earlier patent, maybe perhaps to show it is	
13	different, but I tried to explain that someone	
14	skilled in the art, both ways are obvious.	
15	So even before the '204, both ways were	02:19:08
16	obvious. And the '204, as I said within the	
17	context of '413 yesterday, the '204 does not claim	
18	any advantages for that part. '204 is not	
19	describing the advantages of having the ITO on top	
20	of the inter-layer resin film, it doesn't try to	02:19:36
21	make that as a claim invention.	
22	If you read the specifications, and I'm	
23	sure you have read it a number of times, over many,	
24	many columns, the inventors discuss these adjustment	
25	layers, what materials, what shape, what to extend,	02:20:00

	Page 95
how to connect them, or to form one continuous, so	
there are a lot of discussion about the adjustment	
layers. So and as far as this terminal	
connection, there is only a very brief reference to	
it, and that is in example 3 in column 8 in line 53	02:20:24
to 55 that reads, "Referring to Figure 4A, the	
external connection lines 403 are electrically	
connected to an FPC (flexible printed circuit) 107	
through contact holes provided in the resin	
inter-layer film 113 through ITO (indium tin oxide)	02:21:00
film 114," period. That's the only reference I have	
found. And guide me if there are others that refer	
to this connection.	
And in these four brief sentences here	
four brief lines, in this entire sentence, I do not	02:21:25
see any claim advantages. And even the level of	
details is of how to form it is not explained.	
There is no order with what the layers are put down,	
it doesn't list that you have to list put down	
the resin inter-layer film first and the indium tin	02:21:53
oxide on top of it.	
That description, that language, as I said	
yesterday and I'm saying it again now, it is broad	
enough that covers both implementations of	
connecting ITO and the layer 403.	02:22:19
	there are a lot of discussion about the adjustment layers. So and as far as this terminal connection, there is only a very brief reference to it, and that is in example 3 in column 8 in line 53 to 55 that reads, "Referring to Figure 4A, the external connection lines 403 are electrically connected to an FPC (flexible printed circuit) 107 through contact holes provided in the resin inter-layer film 113 through ITO (indium tin oxide) film 114," period. That's the only reference I have found. And guide me if there are others that refer to this connection.  And in these four brief sentences here four brief lines, in this entire sentence, I do not see any claim advantages. And even the level of details is of how to form it is not explained. There is no order with what the layers are put down, it doesn't list that you have to list put down the resin inter-layer film first and the indium tin oxide on top of it.  That description, that language, as I said yesterday and I'm saying it again now, it is broad enough that covers both implementations of

		Page 96
1	Perhaps the inventors wanted to have it	
2	broad enough so it would cover both embodiments, but	
3	to someone skilled in the art, that is broad and	
4	covers many embodiments.	
5	Similar to Sukegawa does not disclose the	02:22:52
6	other embodiments, that the inventors of '204 didn't	
7	choose to create an embodiment like the one in	
8	Figure like the one in Figure 1B of Sukegawa.	
9	Choose to have one, but then have enough have a	
10	language which is broad enough that cover both.	02:23:17
11	I'm not expert in writing patents. I don't	
12	know what the advantage or disadvantage of broad	
13	language, but that's the only reference I found for	
14	that particular connection.	
15	Q Thank you.	02:23:36
16	Looking a little more at Sukegawa and the	
17	Figure 3 pages, Figures 3A through E, in Figure 3D	
18	would you agree that the I think you agreed	
19	yesterday that the sealant is between structure 100	
20	and structure 200.	02:24:38
21	A The edge of 200.	
22	Q Okay. And that would be to the left of the	
23	left edge of the tape carrier package 300, correct?	
24	A Correct.	
25	Q Would you agree that the tape carrier	02:25:02

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		Page 97
1.	package 300 could not extend between structures 100	
2	and 200, for at least the reason that it's much too	
3	thick?	
4	A I do not know what you mean by the reason	
5	at least it is too much thick.	02:25:29
6	Q That the tape carrier package 300 is much	
7	too thick and it would introduce a very large gap	
8	between 100 and 200.	
9	A The tape carrier package 300 comes down	
10	onto 100 after 200 already has been formed. So the	02:25:54
11	sequence of which comes first is very specific. You	
12	fabricate the wirings and the transistors on 100,	
13	you bring 200, you seal it, you fill it with liquid	
14	crystal, and then you build 300.	;
15	By the time 300 comes to and applied	02:26:22
16	onto 100, 200 has already been formed.	
17	Q Okay. Now, looking at Figure 3E, I invite	
18	your attention to the space marked 14 between the	
19	tape carrier package parts 31 on the right side of	
20	14 and the structure on the left side of 14.	02:26:55
21	And do you see that the upper layer wiring	
22	7 has been removed in the region 14?	
23	A That is another embodiment in Sukegawa, and	
24	I did not rely on that embodiment for my analysis.	
25	But I see in this embodiment that you are referring	02:27:20
	ł	

		Page 98
1	in Figure 3 what you just described.	
2	Q Would that normally be done by an etching	
3	step before layer 8 is deposited?	
4	A You're asking me the details of fabricating	
5	the embodiment shown in Figure 3?	02:27:57
6	Q Yes. Just with regard to removing that bit	
7	of the upper metal layer 7 in that particular spot.	
8	A For building that particular embodiment, it	
9	is obvious that if you want to add a layer and	
10	remove portions of it, you do that before you cover	02:28:39
11	it by another layer, so the answer is yes.	
12	Q Okay. Now, as between Figure 2C, about	
13	which you did testify in your declaration, comparing	
14	Figure 2C and Figure 3E, assuming that all the	
15	dimensions are the same and all the materials are	02:29:09
16	the same, please make those assumptions, does	
17	Figure 3E present the higher resistance between the	
18	copper foil 31B and the left side of layer 2?	
19	MR. GIBSON: Objection; form.	
20	THE WITNESS: What are you referring to?	02:29:50
21	Left side of layer 2?	
22	BY MR. MANZO:	
23	Q The part on the left side of the drawing,	
24	the left side of 2.	
25	A Okay, the end the far left of the	02:30:03

		Page 99
1	drawing?	
2	Q Yes.	
3	A At the end.	
4	Q At the metallic layer or the wiring layer	
5	2.	02:30:13
6	MR. GIBSON: Objection; form.	
7	BY MR. MANZO:	
8	Q Well, Professor, do you understand what I'm	
9	asking?	
10	A No, I think I understand. Don't worry, I	02:30:41
11	think I understand what you're asking.	
12	Q Okay.	
13	A I just want to make sure I didn't	
14	misunderstood anything.	
15	If one compares Figure 2C and Figure 3E,	02:30:55
16	and assuming all the dimensions are identical, as	
17	you highlighted, and we measure the resistance	
18	between the flexible printed circuit board and the	
19	far left of wiring 2 in Figure 3 and compare that to	•
20	the far left of Figure 2C, Figure 2C will have a	02:31:43
21	lower resistance.	
22	Q Is that because part of the upper metal	
23	wiring has been removed?	
24	MR. GIBSON: Objection; form.	
25	THE WITNESS: The two wirings in Figure 2C	02:32:00

	Page 100
are forming a parallel path for the signal to go, so	
you have element 7, what is continuing to the left	
without any gaps, and that will have a lower	
resistance compared to the Figure 3E where a portion	
of the 7 has been removed and you only have 8, which	02:32:37
is known to have a lower resistance than 7.	
BY MR. MANZO:	
Q Thank you.	
A But, as I said, I did not use Figure 3 in	
my analysis, I used Figure 2C in my analysis.	02:33:05
Q Yes. Thank you.	2
A And what you just described, as you	
admitted, a double layer structure to the left of	
the flexible printed circuit board, it creates a	:
lower resistance structure, because I think there	02:33:33
was a discussion in one of the responses that said	
that that part does not create any does not have	:
any effect from the resistance.	
And these discussions that we have, it is	
good to clarify that we both agree that the	02:33:50
resistance to the left of the flexible printed	
circuit board is lower by having the two wires	
running parallel.	
Q Well, thank you. I thought that my	
questions were just questions rather than testimony	02:34:48
	are forming a parallel path for the signal to go, so you have element 7, what is continuing to the left without any gaps, and that will have a lower resistance compared to the Figure 3E where a portion of the 7 has been removed and you only have 8, which is known to have a lower resistance than 7.  BY MR. MANZO:  Q Thank you.  A But, as I said, I did not use Figure 3 in my analysis, I used Figure 2C in my analysis.  Q Yes. Thank you.  A And what you just described, as you admitted, a double layer structure to the left of the flexible printed circuit board, it creates a lower resistance structure, because I think there was a discussion in one of the responses that said that that part does not create any does not have any effect from the resistance.  And these discussions that we have, it is good to clarify that we both agree that the resistance to the left of the flexible printed circuit board is lower by having the two wires running parallel.  Q Well, thank you. I thought that my

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	Page 101	
1	on my part.	
2	But that's something that counsel for the	
3	petitioner can talk to you about, after the	
4	deposition.	
5	MR. GIBSON: Not during the deposition. 02:35:08	3
6	THE WITNESS: I think we are all here to	
7	clarify the records.	
8	BY MR. MANZO:	
9	Q Professor, in making a liquid crystal	
10	display device, are there technical reasons why a 02:36:34	1
11	person ordinarily skilled in the art would want to	
12	avoid covering indium tin oxide with a protective	
13	layer?	
14	A What do you mean by "technical reasons"? I	
15	mean process compatibility reasons or performance of 02:37:39	5
16	the system reasons, or you group them all into one	
17	broad category?	
18	Q I don't want to restrain your answer in any	
19	way by narrowing the scope of your answer.	
20	A I'm trying to understand the scope of your 02:38:03	1
21	questions.	
22	I'm not aware of technical reasons of	
23	covering ITO with an insulating layer, will that	
24	create problems as far as compatibility with the	
25	processing steps. 02:38:2	5

		Page 102
1	You can coat an ITO layer with a	
2	passivation layer.	
3	In the pixel electrode region, that what	
4	used to be the case, it used to be that the ITO	
5	electrode was covered by a passivation layer.	02:38:43
6	I believe in some of the earlier	
7	implementation of the active matrix liquid crystal	
8	displays, that in those displays, the voltage you	
9	had to apply on the pixel electrode to create a	
10	certain twist of the liquid crystal was high because	02:39:16
11	the twist of the liquid crystal was a function of	
12	the strength of the electric field. And by having	
13	the pixel electrode lower than the passivation	
14	layer, increase the increase the layers between	
15	the ITO and the counter electrode. You have the gap	02:39:46
16	and then you also have the passivation layer, so the	
17	electric field was lower.	
18	To reduce the voltage and, hence, to	
19	increase the electric field people either start	
20	removing the passivation layer by creating an	02:40:11
21	opening on the pixel electrode or by flipping the	
22	order and putting the pixel electrode on top of the	
23	passivation layer.	
24	That what comes in my mind at the end of	
25	these two long days is technical reasons and	02:40:32

	Page	103
1	advantage/disadvantage, putting them on top of each	
	other, it refers to the electric field on top of a	
2	·	
3	pixel electrode in the array and how that's related	
4	to the voltage of the that it is the electric	·
5	field, how the electric field is related to the	02:40:58
6	voltage you put to the pixel electrode, and I	
7	believe that is mentioned in one of the patents that	
8	are used in some of the other inter partes that have	
9	used. I don't recall exact which one now, but I	
10	think that's what it was referred to.	02:41:20
11	Q · Okay.	:
12	A If you have something else in particularly	
13	that you want to do, please relate more specific and	
14	I will remember.	
15	Q I think you already answered this, but you	02:41:39
16	said that the protective layer can be placed over	
17	the ITO?	
18	A In the pixel electrode region, there could	
19	be a protective layer on top of the ITO. That	
20	protective layer will reduce the electric field that	02:42:01
21	will go. So either one has if you want to	
22	operate the lower voltages, lower power, you either	
23	have to create an opening on top of the pixel	
24	electrode and remove that dielectric or put the ITO	
25	on top of the dielectric.	02:42:28

	Page 104
1	Q What about the strength of the composite in
2	terms of one material adhering sufficiently well to
3	the material below it?
4	MR. GIBSON: Objection; form.
5	THE WITNESS: Are you talking about the 02:42:56
6	passivation layer, like passivation layer, say, 9 in
7	Sukegawa?
8	BY MR. MANZO:
9	Q Yes.
10	A Well, like depending on like 9 adhering 02:43:09
11	better to ITO or ITO adhering better to 9? I don't
12	quite understand your question.
13	Q That's fine. We'll just move on.
14	In Sukegawa is it true that to avoid a risk
15	of corrosion, Sukegawa uses double coverage over the 02:43:38
16	upper level wiring 7, where the double coverage is
17	the transparent conductive film 8, together with the
18	protective insulation film 9?
19	A In the parts of 7 that are not exposed in
20	order to make a contact to the flexible printed 02:44:25
21	circuit or be used for testing, are you referring to
22	that portion, the portion which is to the left of
23	the left opening or the vertical edge of the left
24	opening?
25	Q Yes. 02:44:47

	Dame 105
	Page 105
1	A Yeah, both layers are used for protection.
2	Q Yes, okay. Thank you.
3	A But reversing the order will equally
4	provide both layers in that region.
5	Q Well, do you agree that Sukegawa discloses 02:45:51
6	that the transparent conductive film 8 needs to be
7	formed directly on top of the upper metal layer
8	wiring 7 in order to avoid corrosion?
9	MR. GIBSON: Objection; form.
10	THE WITNESS: 8 needs to be formed above 7, 02:46:25
11	but whether 8 is directly above 7 or you have 9 in
12	between 7, I do not find any evidence in Sukegawa to
13	say that one is better than the other one and that
14	one should be avoided in favor of the other.
15	If you do have that, please guide me to 02:46:50
16	that part and I will remember it.
17	BY MR. MANZO:
18	Q I'm not aware of Sukegawa discussing the
19	option of putting 9 directly on top of 7, and that
20	the only disclosure I'm aware of in Sukegawa is that 02:47:18
21	the ITO 8 is on top of the wiring 7.
22	Are you aware of any other specific
23	affirmative teaching in Sukegawa otherwise?
24	A I think I read the part in column 8, line
25	starting at 55, that "while the present invention 02:47:57

	Page 106	
1	has been explained with reference to the preferred	
2	embodiments, the invention is not restricted to such	
3	embodiments, but various modifications are possible	
4	within the range not departing the purpose of the	
5	present invention." 02:	48:17
6	So that would have been one set of choice	
7	that one could form.	_
8	Q Where does that sentence say that you can	
9	put 9 on top of 7?	
10	A It says, "various modifications are 02:	48:49
11	possible."	
12	Q But it doesn't say what they are.	
13	A It lists one example as far as the	
14	visualization, so rely on what someone skilled in	
15	the art will consider whether certain modifications 02:	49:12
16	will become obvious in light of the teachings in	
17	Sukegawa.	
18	For example, Sukegawa teaches that layers 8	
19	may have pinholes and layers and pinholes in	
20	layer 8 may lead into corrosion of the underlying 02:	49:40
21	layer 7.	
22	So if we put 9 directly above 7, and 8	
23	above 9 and create the openings, that will be one	
24	way to remove the effect of the pinholes in 8 from	
25	the effect of 7.	50:17

	Dama 107
	Page 107
1	Q Well, that may be one possibility, but
2	Sukegawa actually does something different, doesn't
3	he or they, if you know?
4	A Again, in the particular embodiments in
5	the particular embodiments that he lists, but to 02:50:34
6	someone skilled in the art, other modifications may
7	also be obvious.
8	And I discussed earlier that having 9 above
9	7 and 8 above 9, it will simply duplicate the
10	structure that is listed below, namely the 02:51:03
11	connections between 2 and 7.
12	Q Okay. But just to make perfectly clear, do
13	you agree with me that there is no figure that shows
14	that in Sukegawa?
15	A The figures list specific embodiments in 02:51:18
16	the paragraph I read
17	Q We'll get to the text next. I just want to
18	ask you about the picture first.
19	So do you agree that there is no picture
20	showing this change in order? 02:51:39
21	A No, the pictures list specific embodiments,
22	and that's a different embodiment.
23	Q All right. Now, turning to the text of
24	Sukegawa, do you find a specific suggestion beyond
25	column 8 saying you can make modifications where it 02:52:01

	Page	e 108
1	says one of the modifications you can make is to put	
2	9 on top of sorry, you can put 8 on top of 9? Is	
3	that anywhere in here specifically?	
4	A It has been two long days, so if you give	
5	me time, I will read it again to find out supporting	02:52:29
6	evidence. And I have to reframe my mind again to	
7	the claims if they suggest that part.	
8	If you want me to look at it again, I will	
9	take the time to look at it again. I	
10	Q Sure. That's fine. How long would you	02:52:57
11	like?	
12	A I don't know what is my mental capability	
13	right now, but, I don't know.	
14	Q Should we take a recess while you do this	
15	review and come back in a few minutes?	02:53:27
16	A Sure.	
17	Q Okay.	
18	THE VIDEOGRAPHER: Off the record at 2:53	
19	p.m.	
20	(Recess.)	02:53:34
21	THE VIDEOGRAPHER: Back on the record at	
22	3:13 p.m.	
23	BY MR. MANZO:	
24	Q So, Professor, have you found that specific	
25	citation, if there is one?	03:13:33

	Page 109
1	A I found several sections in which Sukegawa
2	is teaching the advantages of placing ITO directly
3	on insulating layer. And by doing the placement of
4	the ITO directly on the insulating layer, pinholes
5	within the indium tin oxide layers will not affect 03:14:44
6	the reliability of any underlying metal layer,
7	because below the indium tin oxide layer there will
8	be an insulating layer.
9	Those portions are discussed with reference
10	to the different embodiments of Sukegawa, so the 03:15:14
11	teaching in Sukegawa so within Sukegawa there is
12	a teaching that it is advantageous to place the
13	transparent conductive film 8 that will be exposed
14	directly on the surface of an insulating layer.
15	So it will take someone with ordinary skill 03:15:46
16	in the art to place 8 on top of 9, given the
17	teachings within Sukegawa that such a placement will
18	create a more robust structure because it will solve
19	the problem of the pinholes.
20	Q Okay. So where are those citations, 03:16:15
21	please?
22	A So in column 6, lines 23 to 38, starting
23	from the part that reads, "Accordingly, even if the
24	transparent conductive film 8 in the terminal
25	portion contains a defect or even if it is used in a 03:18:57

	Page 110
1	high humidity atmosphere, the upper layer metal
2	wirings, 7-1 and 7-2 are not exposed to the external
3	air and protected against corrosion, " and continues,
4	"in that embodiment, the transparent conductive film
5	8 is shown to be formed directly on insulating 03:19:24
6	layer."
7	That insulating layer is not subject to any
8	corrosion because it does not react. It is already
9	an oxide or nitride.
10	So it shows that if I place the transparent 03:19:46
11	conducting film on an insulating film, and even if
12	there are pinholes, those pinholes will not cause
13	any corrosion.
14	Q Professor, isn't that discussing Figure 3E,
15	and they are talking about the part of layer 8 where 03:20:16
16	there is no layer 7 underneath it to be corroded by
17	pinholes?
18	A I think that's the essence of the teaching,
19	you place the transparent conductive layer that will
20	be exposed not directly on top of a metal, so if 03:20:39
21	there are pinholes, you will not expose the
22	underlying metal into it.
23	So you it will take ordinary skill in the
24	art that if you reverse the order with which 8 and 9
25	are laid down, you will gain that advantage by 03:21:06

	Page 111
1	having the 8 on top of an insulating layer
2	throughout the surface. And the same thing has also
3	been taught in another embodiment where, again, the
4	placement of the transparent conductive layer
5	directly on top of an insulating layer and not above 03:21:32
6	the metal, it results in this improved reliability.
7	Q Okay. Is there a further citation that you
8	located?
9	A Yes, let me so it is column 7, line 48,
10	and I'm quoting, "Also in this embodiment, the 03:22:44
11	transparent conductive film 8 exposed through the
12	aperture portion of the protective insulating film 9
13	is formed directly on the surface of the inter-layer
14	insulating film 3 under which the upper layer metal
15	wire 7 is not present in the same manner as in the 03:23:05
16	first embodiment. Accordingly, if the transparent
17	conductive film 8 in the terminal portion contains
18	defect or it is used in a high humidity atmosphere,
19	the metal wiring 7 is not exposed to the external
20	atmospheric and can be protected against corrosion." 03:23:23
21	So in both these embodiments, Sukegawa
22	teaches the advantages of placing the transparent
23	conductive layer directly onto an insulating layer,
24	which, by itself, would be resistant to corrosion
25	and will not be affected if there are any pinholes 03:23:46

		Page 112
1	in the transparent conductive layer.	
2	So it will take somebody with ordinary	
3	skill in the art to see that reversing the order 8	
4	and 9, that will result in such advantages because 8	
5	will be on top of an insulating layer and any	03:24:13
6	pinholes will not affect the underlying layers.	
7	Q Anything else? Are those all the ones you	
8	found?	
9	A And there is another embodiment it is	
10	placing the ITO on top of the glass where it is an	03:24:34
11	insulating layer. So there are three embodiments	
12	within Sukegawa that discuss the advantages of	
13	placing the ITO on an insulating layer. It gives	
14	two examples of what are those insulating layers.	
15	One is the insulating layer 3, another is the glass	03:25:00
16	substrate.	
17	So I will take ordinary skill in the art to	
18	reverse the order of 8 and 9 and place 8 above 9 to	
19	get the benefits of placing the transparent	
20	conductive layer on to an insulating substrate.	03:25:23
21	Q Thank you.	
22	A If I may, outside of Sukegawa, that has	
23	also been disclosed in other prior art references	
24	that I have reviewed as part of this proceeding. It	
25	was well known at the time of the invention that the	03:25:48

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	Page 113
1	placement of the ITO on the upper surface of the
2	passivation film would result in some advantages as
3	far as the corrosion concerns.
4	Placing the IT on top of 9, particularly
5	the time that the '204 was filed was certainly not 03:26:23
6	an innovation. I'm not sure what was the priority
7	day, but even by Sukegawa is better than the '204
8	and Sukegawa is listed as a reference in '204 so I'm
9	sure that the artist have to put a reference
10	sorry, a drawing that will not be exactly the same 03:27:09
11	as the one depicted by Sukegawa.
12	But, as we discussed and I testified a
13	number of times already, the language in '204 is
14	broad that covers both structures.
15	Q If there are advantages to putting the ITO 03:27:40
16	layer 8 above layer 9, can you think of any reasons
17	why Sukegawa did not illustrate that in Figure 3E,
18	for example, or even in Figure 2C?
19	A Perhaps Sukegawa wanted to avoid having an
20	embodiment that would look like other prior art 03:28:14
21	pieces that had ITO above the layer 9.
22	Q You don't have those references here today,
23	do you?
24	A No. You may have it.
25	If you look at the prior art, Moriyama, you 03:28:55

	·	
		Page 114
1	will see that the ITO layer is the upper layer and	,
2	it has been placed on top of an insulating film.	
3	Do you happen to have Moriyama with you?	
4	Q No.	
5	A I'm sure my counsel can try to generate it.	03:30:04
6	Q Is it correct that in the column 7 citation	
7	you gave us, Sukegawa	
8	A Which line?	
9	Q At line well, I think you cited 48 to	
10	57.	03:31:37
11	Is the transparent conductive layer on top	
12	of the oxide layer 3 or, rather, the nitride layer	
13	9?	
14	A Well, in the particular embodiment	
15	discussed in this section, it is form the	03:32:18
16	inter-layer insulating film 3, but the benefit of	
17	placing at here on top of an insulating film will	
18	apply independently of what is that insulating film,	
19	whether that will be the 3 or the 9. Because they	
20	are both 3 and 9 will not be affected if there	03:32:56
21	will be a pinhole in the transparent conductive	
22	layer, none of them will be reacting and corroding.	
23	Q I understand.	
24	In your citation at column 6, lines 23 to	
25	38, is that also talking about placing the ITO film	03:33:23

Page	115
on top of the oxide 3, rather than on top of the	
nitride 9?	
A Inter-layer insulating film 3 is a	
composite film that includes an oxide, silicon oxide	
film and silicon nitride film. It is column 4, line	03:34:12
22. It reads, "a composite film of a silicon oxide	
film and a silicon nitride film to form an	
inter-layer insulating film 3 is formed."	
So both 3 and 9 contain silicon nitride, so	
it's not that one is better than the other one.	03:34:38
Q But is it the case that in the Figure	
sorry in the column 6 passage you mentioned,	
Sukegawa is talking about the transparent conductive	·
layer over the insulator 3?	
A In this particular embodiment. But the	03:35:42
advantages of having 8 on top of 9 will be the same	
as having on top of 3.	
Q I understand what you're saying.	
Do you agree that there is no embodiment in	
Sukegawa where 8 is over or on top of 9,	03:36:19
specifically?	
A There is no drawing, but the teaching for	
creating and modifying the embodiment discussed, the	
preferred embodiment we discussed, is there. And	
Sukegawa teaches that various modifications are	03:36:56
	on top of the oxide 3, rather than on top of the nitride 9?  A Inter-layer insulating film 3 is a composite film that includes an oxide, silicon oxide film and silicon nitride film. It is column 4, line 22. It reads, "a composite film of a silicon oxide film and a silicon nitride film to form an inter-layer insulating film 3 is formed."  So both 3 and 9 contain silicon nitride, so it's not that one is better than the other one.  Q But is it the case that in the Figuresorry in the column 6 passage you mentioned, Sukegawa is talking about the transparent conductive layer over the insulator 3?  A In this particular embodiment. But the advantages of having 8 on top of 9 will be the same as having on top of 3.  Q I understand what you're saying.  Do you agree that there is no embodiment in Sukegawa where 8 is over or on top of 9, specifically?  A There is no drawing, but the teaching for creating and modifying the embodiment discussed, the preferred embodiment we discussed, is there. And

	Pa	ge 116
1	possible. And placing 8 above 9 is one such obvious	
2	modification or possible modification.	
3	It would be better to say obvious and	
4	possible. Even better, an obvious possible	
5	modification.	03:38:18
6	Q The passage that you're citing does not	
7	say, specifically, to have an embodiment with layer	
8	8 over layer 9, does it?	
9	A The passages I described teach the benefits	
10	of having ITO on top of an insulating layer. They	03:39:04
11	use an example. They have two examples, two	
12	different insulating layers, and those are two	
13	different insulating layers. 9 would have been an	
14	obvious modification.	
15	Q I understand what you're saying. That	03:39:35
16	doesn't mean I agree with you, but I understand what	
17	you're saying.	
18	I want to ask you some questions about the	
19	'204 patent. I believe you have a copy in front of	
20	you.	03:40:01
21	I think that the first claim that is	
22	challenged is claim 31; is that right?	
23	A I have to refer to my declaration.	
24	That's correct.	
25	Q Okay. Well, let's look at claim 31, then.	03:41:31

		Page 117
1	That appears at column 17, 1	lines 35 through
2	63, or 62.	
3	Do you have that before you?	?
4	A I do.	
5	Q Good.	03:41:56
6	Actually, let's, instead, to	urn to claim 54.
7	That starts at column 19, line 62 and	d extends down
8	through column 20, line 31.	
. 9	Do you have that before you	. now?
10	A I do.	03:43:10
11	Q Okay. Good.	
12	And if you read through thi	s claim, I think
13	that you'll find the use of the word	l "over," for
14	example, at column 20, line 3	
15	A Okay.	03:43:37
16	Q which calls for a first	conductive line
17	over the substrate.	
18	And these questions are muc	ch like the ones
19	I asked you yesterday about the sequ	lence.
20	So does the word "over" imp	oly a sequence 03:43:46
21	here as between the substrate and th	ne first
22	conductive line?	
23	A It means that the first cor	nductive line is
24	formed over the substrate, that the	substrate
25	5 preexists the first conductive line.	. 03:44:08

	P.	age 118
1	Q Okay.	
2	A You cannot form the substrate over the	
3	first conductive line.	
4	Q Does the next claim element, a first	
5	insulating film over the conductive line, call for	03:44:25
6	the first conductive line to be in existence first	
7	with respect to the first insulating film?	
8	A That's correct.	
9	Q And, correspondingly, or similarly, does	
10	the next claim element at column 20, line 5, which	03:44:46
11	recites, "a second conductive line over the first	
12	insulating film," does that require the existence of	
13	the first insulating film to be in existence prior	
14	to the second conductive line?	
15	A Yes.	03:45:09
16	Q Does the next paragraph of the claim	
17	similarly have a sequence implicit in it? That one	
18	calls for a, quote, transparent conductive layer	
19	over a first region of the second conductive line,	
20	closed quote.	03:45:37
21	Does that mean that the transparent	
22	conductive layer is placed after the second	
23	conductive layer sorry, the second conductive	
24	line is established?	
25	A The transparent conductive layer is formed	03:45:58

,	Page 119	
1	after the first region of the second conductive line	
2	is formed.	
3	Q And the next claim element, must the	
4	flexible printed circuit be placed over the first	
5	region of the second conductive line, does that mean 03:46:18	3
6	that the second conductive line must be formed first	
7	before the flexible printed circuit is put there?	
8	A That's correct.	
9	Q And next, where it says that a sealant	
10	strike that. 03:46:38	3
11	It says, quote, a sealant over a second	
12	region of the second conductive line, closed quote.	
13	Does that language imply that the second	
14	conductive line must have a second region which is	
15	established before the sealant is placed there? 03:46:5	5
16	A That is correct.	
17	In all these cases, we relay the sequence	
18	of two elements. And the only thing we can say from	
19	each individual sentence is which comes first and	
20	which comes second. 03:47:2	4
21	If we take certain of these sentences as a	
22	group to relay the sequence of more than two	
23	elements, then in some sections an explicit order of	
24	which comes first, second and third also appears.	
25	The fact that there is a sequence between 03:47:5	8

		Page 120
1	two different elements, it does not imply inherently	
2	the sequence of more than one more than two	
3	elements. I don't know if I'm making myself clear.	
4	Q Okay.	
5	A But please continue.	03:48:24
6	Q Okay. The next one is on line 14 and that	
7	calls for a conductive layer over the substrate.	
8	Does that mean the substrate must exist	
9	before the conductive layer is placed over it?	
10	A Yes. But, for example, in this particular	03:48:38
11	sentence, a conductive layer over the substrate, we	
12	can only say the relationship, the order, between	
13	those two elements; for example, that sentence by	
14	itself, it does not, for example, relate the	
15	conductive layer with any of the previous elements	03:49:05
16	that were discussed, for example, to know how the	
17	conductive layer maybe start again.	
18	That sentence, "a conductive layer over the	
19	substrate," it only implies an order that the	
20	conductive layer is formed after you have the	03:49:41
21	substrate. But that sentence alone does not relay	
22	the conductive layer, it does not relay the order	
23	with which the conductive layer was placed over the	
24	substrate in relationship to any of the other	
25	elements that we are already discussed. And we	03:50:05

		Page 121
1	discussed a number of elements that are over the	
2	substrate. For example, we had the first conductive	
3	line over the substrate, we had the first insulating	
4	film over the first conductive line, hence the first	
5	insulating film is also over the substrate. The	03:50:24
6	second conductive line over the first insulating	
7	film also, which means that the second conductive	
8	line is also over the substrate.	
9	So this is three other elements, first	
10	conductive line, first insulating film, second	03:50:38
11	conductive line, that are also over the substrate.	
12	And here we have, in this line 14, another element	
13	over the substrate.	
14	So I'm just trying to clarify that the	
15	order, in using the word "over," is implied only	03:50:56
16	between those two elements that are used in that	
17	sentence.	
18	If we want to derive the relationship or	
19	the order among multiple layers, we have to look in	
20	more than one of those statements and see if that	03:51:17
21	such relationship can be established in a unique way	,
22	or there are multiple ways that can be established.	
23	Q Looking at the subparagraph beginning at	
24	line 21, which reads, "Wherein the second conductive	:
25	line and the flexible printed circuit are in	03:52:13

		Page	122
1	electrical contact through the transparent		
2	conductive layer."		
3	Is there an implied sequence there that		
4	tells us that the contact that tells us anything		
5	about the contact?		03:52:38
6	MR. GIBSON: Objection; form.		
7	THE WITNESS: What do you mean?		
8	MR. MANZO: Strike the question.		
9	Q Let's move on to the next claim element.		
10	The next claim element says, "Wherein the		03:53:04
11	second conductive line and the transparent		
12	conductive layer are in direct contact through an		
13	opening in the second insulating film."		
14	Does the opening in the second insulating		
15	film provide the ability for the second conductive		03:53:27
16	line and the transparent conductive layer to have		
17	direct contact?		
18	MR. GIBSON: Objection; form.		
19	THE WITNESS: Whether it provides the		
20	ability for the second conductive line and the		03:54:00
21	transparent conductive layer to have direct contact		
22	or, alternatively, between the vertical walls of the	Э	
23	opening, the second conductive line and the		
24	transparent conductive layer are in direct contact		
25	depends upon how you construe the word "through."		03:54:24

Γ		
		Page 124
1	MR. MANZO: I think we have to change the	
2	video disk.	
3	THE VIDEOGRAPHER: This is the end of tape	
4	2. We are off the record at 3:57 p.m.	
5	(Recess.)	03:57:18
6	THE VIDEOGRAPHER: This is the beginning of	
7	tape 3. We're back on the record at 4:11 p.m.	
8	BY MR. MANZO:	
9	Q Professor, I think you were in the middle	
10	of analyzing claim 54.	04:11:32
11	Had you finished your answer about whether	
12	the opening referred to at column 20, line 25 is the	
13	structure that permits the second conductive line to	
14	have direct contact with the transparent conductive	
15	layer?	04:12:17
16	A What I wanted to say and is not quite	
17	captured in my last statement, in the last sentence	
18	of my previous statement, is that based on the	
19	different limitations listed above for the different	
20	elements, one cannot see an order between the	04:13:36
21	transparent conductive layer and the second	
22	insulating film.	
23	Both are over the second conductive line,	
24	but the language is broad and does not express or	
25	implies a specific order. In that in the context	04:14:11

Γ	_	105
		age 125
1	of such a broad language, the word "through" cannot	
2	be construed in a unique way.	
3	Q Looking at Figure 4A, would you agree that	
4	the second conductive line shown in this figure is	
5	in direct contact with the transparent conductive	04:15:01
6	layer, which is 114, only because of the existence	
7	of the opening in the resin inter-layer film 113?	
8	MR. GIBSON: Object to the form.	
9	THE WITNESS: In the particular Figure 4A	
10	in this particular embodiment in this particular way	04:16:22
11	of fabricating and constructing the structure, this	
12	is true, but the language used in the claim and,	
13	if I may add, the language that is in the text of	
14	the specifications within '204, and, in particular,	
15	the section that I read to you earlier within the	04:16:55
16	example 3, column 8, lines 53 to 55, the language is	
17	broad and is not limited to just one embodiment.	
18	BY MR. MANZO:	
19	Q That language says, "Referring to	
20	Figure 4A, the external connection lines 403 are	04:17:31
21	electrically connected to an FPC (flexible printed	
22	circuit) 107, through contact holes provided in the	
23	resin inter-layer film 113 through ITO (indium tin	
24	oxide) film 114."	
25	Is that what you're citing?	04:17:58

		Page 126
1	A Exactly.	
2	Q And you agree let me ask.	
3	Do you agree from the very first words of	
4	that sentence that it is referring to Figure 4A,	
5	where it says, quote, Referring to Figure 4A, closed	04:18:20
6	quote?	
7	A Right. But that language, if I may ask,	
8	how is it different from the Figure 1B of Sukegawa?	
9	Isn't that language here that the "external	
10	connection lines 403 are electrically connected to	04:18:39
11	an FPC (flexible printed circuit) 107 through	
12	contact holes providing the resin inter-layer film	
13	113 through an ITO (indium tin oxide) film 114"?	
14	Doesn't that language describe exactly what	
15	it is in Sukegawa?	04:18:59
16	Q I don't think so, no, but that's what we're	
17	going to let the board decide.	
18	A No, I understand.	
19	But if we we agree that Figure 4A shows	
20	something.	04:19:15
21	Q Yes.	
22	A I don't think we disagree with that part.	
23	Q Good.	
24	A Okay. Now, referring to the text that	
25	describes the "external connection lines 403 are	04:19:23

	Page 127
1	electrically connected to an FPC (flexible printed
2	circuit) 107 through contact holes providing the
3	resin inter-layer film 113 through an ITO (indium
4	tin oxide) film 114," and I say that that sentence,
5	someone with ordinary skill in the art will see 04:19:50
6	exactly that part. That's how someone ordinarily
7	skilled in the art will describe the connection
8	between the external connection lines and the
9	flexible printed circuit board by looking at the
10	Sukegawa, and that's the only reference I find in 04:20:07
11	the specifications that is relevant to construe the
12	word "through," Figure 4A and that sentence.
13	And I said that sentence is exactly what is
14	described in Sukegawa if we just change the numerals
15	to the ones used by Sukegawa. 04:20:42
16	I understand that the words should be
17	could be construed in light of specifications, but
18	in this case, the specifications themselves contain
19	a very broad language.
20	And if there is any other section in the 04:21:29
21	specifications that you read something else in
22	relationship to the order of the different layers,
23	in particular the second insulating film and the
24	transparent conductive layer, please bring out to me
25	so I can review that section in case I have already 04:21:47

ı		
		Page 128
1	miss it.	
2	Q Give me a moment to consider what you said.	
3	A moment ago you said we could substitute	
4	the we could change the numerals to refer to	
5	Sukegawa and change the sentence that's in column 8.	04:24:27
6	Can you go ahead and do that for us so that	
7	this sentence would describe Sukegawa?	
8	A The external connection lines 31 are	
9	electrically connected strike that.	
10	The external connection lines 7 are	04:25:36
11	electrically connected to a FPC (flexible printed	
12	circuit) 31 through contact holes provided in the	
13	resin inter-layer film 9 through ITO (indium tin	
14	oxide) film 8.	
15	If I may add, that sentence even captures	04:26:16
16	better Figure 2C of Sukegawa than Figure 4A that it	
17	is referring to, because at least the flexible	
18	printed circuit through contact holes.	
19	And that's what we see in Figure 2C of	
20	Sukegawa, we see the openings in 9.	04:26:38
21	And then says "through an ITO," and we see	
22	that in Sukegawa.	,
23	Q In the '204 patent, looking and the Figure	
24	4A or 4B, is there a sealant on only the second	
25	insulator and not on the ITO? Do you see that?	04:29:13

		I	Page 129
1	A This is what	t is depicted in Figure 4A.	
2	Q And do you	agree that the ITO is formed	
3	after the second inst	ulator?	
4	A Based on Fig	gure 4A?	
5	Q Yes, based	on Figure 4A.	04:29:45
6	A Okay.		
7	Q Do you agre	e?	
8	A This is wha	t is shown.	
9	Q Okay. And	I think, as you just discussed	
10	or we've just discus	sed for a while, Figure 4A shows	04:30:01
11	an opening in that f	ilm 113 so that the external	
12	connection lines 403	can contact the conductive	
13	layer 114; is that r	ight?	
14	A Looking at	Figure 4A in the way that the	
15	this particular embo	diment is drawn, this is right.	04:31:07
16	Q And you agr	ee that in Figure 4A the sealant	
17	105 is not on top of	the ITO 114, but, instead, it	
18	is separated from it	; is that correct?	
19	MR. GIBSON:	Objection; form.	
20	THE WITNESS	: In this particular embodiment	04:31:49
21	shown in Figure 4A,	this is what it shows, but the	
22	claims do not provid	e any limitation on the	
23	placement of the sea	lant with respect to the ITO, or	
24	I do not recall such	a limitation.	
25	BY MR. MANZO:		04:32:11

	Т	age 130
		age 130
1	Q Do you agree that the '204 patent reduces	
2	the height difference that is caused by the	
3	formation of the auxiliary line, which is the first	
4	conductive line 401 and the external connection	
5	line, which is the second conductive line 403?	04:32:31
6	MR. GIBSON: Objection; form.	
7	THE WITNESS: The objective of '204 is to	
8	address the issue of unevenness around the seal area	
9	and provide certain several embodiments of how to	
10	do so, depending upon the construction of the wiring	04:33:28
11	used in making the external connections.	
12	BY MR. MANZO:	
13	Q Okay. Do you agree that the '204 patent	
14	uses adjustment layers to keep a relatively constant	
15	seal height strike that.	04:33:49
16	Do you agree that the '204 patent uses	·
17	adjustment layers to preserve a relatively constant	
18	spacing between the substrate and the counter	
19	substrate even though an external connection line	
20	and an auxiliary line, one above the other, pass	04:34:11
21	underneath the sealant between those two substrates?	
22	A Well, the '204 patent alleges, as	
23	innovations, the use of the adjustment layers to	
24	preserve a relatively constant spacing between the	
25	substrate and the counter substrate, even though an	04:35:13

	D 121
	Page 131
1	external connection line, auxiliary line, one above
2	the other pass underneath the sealant between those
3	two substrates.
4	And it's my understanding it's the purpose
5	of these proceedings to determine if these are, 04:35:30
6	indeed, new innovations worthy of issuing a patent
7	or not.
8	In my analysis, I found prior art that meet
9	exactly the same provide similar structures
10	actually, same structures to solve that problem. 04:36:00
11	Q Is it true that you did not find any
12	adjustment layer in the Shiba patent for attacking
13	that problem?
14	A Shiba does not address the issue of the
15	spacing between the two substrates, but the prior 04:36:27
16	art reference which I utilized, namely, the Watanabe
17	patent, address that issue.
18	So Shiba, in view of Watanabe, teach or
19	render obvious these substrate gap adjustment
20	layers. 04:37:32
21	Q In Shiba, you have spoken about a
22	transparent conductive layer, right?
23	A (No audible response.)
24	Q Do you recall that we talked about a
25	transparent conductive layer in Shiba? 04:38:07

	Page	e 132
1	A There is a transparent conductive layer	
2	disclosed in Shiba.	
3	Q Is there any disclosure or teaching or	
4	suggestion in Shiba that the transparent conductive	
5	layer should not extend to the second region that is	04:38:22
6	defined by the claims at issue here?	
7	MR. GIBSON: Objection; form.	
8	BY MR. MANZO:	
9	Q Well, Professor, I think your counsel wants	
10	me to point you to the particular claim, so let me	04:39:44
11	modify my question.	
12	Do you see that claim 54 refers to a first	
13	region and a second region of the second conductive	a marina
14	line?	
15	MR. GIBSON: Which claim?	04:40:05
16	MR. MANZO: 54.	
17	MR. GIBSON: Thank you.	
18	THE WITNESS: Yes.	
19	BY MR. MANZO:	
20	Q Okay. So I'm asking whether there's any	04:40:44
21	teaching, suggestion or other specific motivation in	
22	Shiba that the transparent conductive layer should	
23	not extend to the second region defined in claim 54	
24	of the '204 patent?	;
25	MR. GIBSON: Objection; form.	04:41:23

	]	Page 134
1	the slit 243. That part will be modified in view of	
2	Sukegawa.	
3	Someone with ordinary skill in the art will	
4	not be motivated to protect wirings that have	
5	already been protected by the second insulating film	04:45:08
6	by adding additional piece of metals, such as ITO,	
7	the same way that they will do it in the terminal	
8	pad region.	
9	And in view of Sukegawa sorry. Strike	
10	that.	04:45:26
11	In view of Watanabe, which addresses the	i
12	issue of the gap spacing, the person with ordinary	
13	skill in the art will also understand that adding	
14	additional metal will create a bigger step in the	
15	gap region in the sealant region.	04:46:10
·16	So you asked me whether there is it any	
17	teaching, suggestion or other specific motivation in	•
18	Shiba that the transparent conductive layer should	
19	not extend to the second region defined in claim 54	
20	of the '204 patent. And in my answer I said that	04:46:44
21	someone with ordinary skill in the art based on the	
22	teaching of Shiba, in view of Watanabe and Sukegawa,	
23	will place the transparent conductive layer only in	
24	the terminal region in the common pad 751 and will	
25	not extend it into the second region under the	04:47:18

	D 525
	Page 135
1	sealant, because you will not result in any
2	additional advantages and may, rather, create a
3	bigger step.
4	BY MR. MANZO:
5	Q Thank you. 04:47:50
6	If you were to look at claim 54 again, at
7	line 23, I think, the claim talks about electrical
8	contact. And at line 25 it talks about direct
9	contact.
10	Do you see that? 04:50:24
11	A I see that.
12	Q Are those two the same to a person of
13	ordinary skill in the art?
14	A A direct contact implies a direct physical
15	contact, that the two layers touch each other. 04:51:53
16	An electrical contact can occur between two
17	elements, even if they're not touching each other,
18	in other words, even if they are not in direct
19	contact. But if each touch a third element, third
20	object, they can be in electrical contact without 04:52:18
21	being in direct contact by through this other
22	material or object.
23	So in the case it says line 22, the second
24	conductive line and the flexible printed circuit are
25	in electrical contact through the transparent 04:52:46

Г	Addition		
		Page	136
1	conductive layer, that implies that the flexible		
2	printed circuit is not touching directly the second		
3	conductive line, but rather, in between there is		
4	another material and that other material is the		1
5	transparent conductive layer.		04:53:07
6	So an electrical connection, which means	•	
7	that the layers have to be in the same potential and		
8	current can flow through them, can be established		
9	through the transparent conductive layer.		
10	In the other section, the direct contact,		04:53:27
11	it means that the two layers physically touching		-
12	each other.		
13	And we had a lengthy discussion yesterday		
14	about an electrical contact is established when you		
15	have the passage of current, and that will require	•	04:54:09
16	some sort of an excitation source, some sort of a		
17	voltage difference, such as those provided by an		
18	external board through the flexible printed circuit.		
19	Q Professor, if you have a television set,		
20	the kind that runs off of AC power rather than a		04:56:52
21	battery, and it's plugged into the wall socket,		
22	would you say that the television set is not in		
23	electrical contact with the wall socket until the		
24	on/off switch is turned on on the TV set?		
25	MR. GIBSON: Objection; form.		04:57:20

	Page	137
1	THE WITNESS: You have a television set and	
2	you have a wire, a cable, that is plugged to the	
3	socket on the wall	
4	BY MR. MANZO:	
5	Q The power supply cord.	04:58:23
6	A The power supply cord.	and the second s
7	Before you turn on the power switch, the	
8	television set will not consume any power. There	
9	will be no current flowing into the television set,	
10	no current will flow out of the wall to power any	04:58:43
11	electronics because the switch will prevent the flow	
12	of the current.	
13	Q So does that mean that until the switch is	
14	turned on, the TV set is not in electrical contact	
15	with the wall socket?	04:59:13
16	MR. GIBSON: Objection; form.	
17	THE WITNESS: The cable, which is plugged	
18	in, is physically attaching the external cable. A	
19	potential along the cable line is established to be	
20	the same as that of the of the outlet. And	04:59:49
21	somewhere there is a switch and that switch is off.	
22	Now, from the streets and into the circuit	
23	of the television there is no electricity that has	
24	been sent. The cable is at the same voltage as the	
25	outlet, like 110, 20 volts, and then there is a	05:00:31

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		Page 138
1	switch that is opened. And the circuit within the	
2	television apparatus is isolated.	
3	And when you turn on the switch,	
4	electricity flows. And when you turn off and	
5	power is consumed. And when you turn off the	05:01:08
6	switch, current stops to flow.	
7	BY MR. MANZO:	
8	Q So	
9	A But the means to flow the current in and	
10	out of the television set exists.	05:01:24
11	Q And	
12	A Whether it is controlled by the switch.	
13	You have the ability to flow current in and out of	
14	the television through the switch.	
15	Yesterday we were discussing in the context	05:02:18
16	of Sukegawa when you have a continuous passivation	
17	film, and I have explained that before the opening	
18	in the passivation film there is no ability to flow	
19	currents in or out because the whole system was	
20	coated with insulations, there was no, in this	05:02:45
21	context, a magical switch that you can turn on or	
22	off, so I don't know what you're trying to lead	
23	where you're trying to lead with your line of	
24	questioning.	
25	I am trying to be as explicit as possible	05:03:07

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	Pa	ige 139
1	in my answers.	
2	Q And we appreciate that, Professor.	
3	Just going back to the TV set, you've	
4	talked about the circuits in the TV set.	
5	Is the power cord of the TV set in	05:03:21
6	electrical contact with the wall socket once it's	
7	plugged into the wall socket without regard to the	
8	state of the on/off switch in the TV set?	
9	MR. GIBSON: Objection; form.	
10	THE WITNESS: So if I understand your	05:04:13
11	question well, you're asking me, the switch is	,
12	off you asked me without regard to the state of	·
13	the on/off switch. So if the switch is on,	
14	electricity flows to the TV set through the cable,	
15	so in that case everything is electrically	05:04:40
16	connected, electricity flows. If the switch is off,	
17	that's what needs to be addressed.	
18	If the switch is off, no electricity flows	!
19	inside the TV set; however, at the moment you	
20	starting from the moment where the switch where	05:05:16
21	the cable is not plugged. Okay? We now have a	
22	cable which is not plugged in the wall. Let's	
23	assume that the TV is also in the off position. And	
24	now we're plugging in the cable we're going to	
25	plug the cable.	05:05:41

	Pag	e 141
1	isolated and no electricity can flow, no potential	
2	difference still exist inside the circuit, and that	
3	will continue until you press the on switch in the	
	on state.	3
4		05:07:40
5	BY MR. MANZO:	05:07:40
6	Q Professor	
7	A Thank you.	
8	In the morning we were discussing a	
9	hypothetical structure in Shiba.	
10	Do you recall that? You were saying about	05:08:14
11	the presence of the dielectric layer 211 in between	
12	the two wirings the two layers of wiring 127? Do	
13	you recall that?	
14	Q Yes. We were discussing dielectric and the	
15	wiring in 127, and we had a discussion about whether	05:08:39
16	there's dielectric there or not.	
17	A Right.	
18	And you allege that there may be a means	
19	that the two structure, the two layers will be	
20	formed that will physically attach each other	05:09:05
21	because the dielectric layer will not be there.	
22	Do you recall that?	
23	Q Yes.	
24	A And I have asked you whether there is	
25	evidence, and you did not I did not recall you	05:09:20

	Page 142
1	showing me such evidence other than the allegations
2	that that somehow it is possible. If I recall, I
3	did not see evidence to point out that this
4	dielectric layer somehow is removed.
5	Q Did you want to amend your answer? 05:09:43
6	A No. What I would like to do is provide you
7	evidence that this layer is not removed, and I want
8	to back that evidence from the statements within
9	Shiba why that is not occurring. If you're
10	interesting (sic) to obtain that evidence from 05:10:09
11	Shiba, I will tell you that this hypothetical
12	structure has not been taught on Shiba. In
13	contrast, Shiba teaches away from that hypothetical
14	structure that you allege that may exist.
15	Q Where is that teaching? 05:10:3
16	A If we go to Shiba, column 7, line 45, it
17	reads, "Since the wiring line 127 is constituted by
18	a plurality of narrow lines as shown in Figure 6,
19	the protective overcoat 241 and the gate dielectric
20	211 are directly connected to each other through the 05:11:1
21	gap between the narrow lines. As a result, there is
22	a removal of the first wiring line 127 together with
23	the sealing agent 113 is reduced. Accordingly, the
24	degree of freedom of selecting material of the
25	sealing agent 113, depending on the adhesion 05:11:3

	D 142
	Page 143
1	capacity, can be increased."
2	In other words, this section
3	Q That's the end of the quote?
4	A Yes.
5	Q Okay. Go ahead. 05:11:53
6	A I read from line 46 to line 54.
7	Q Thank you.
8	A In that section Shiba is teaching the value
9	of maintaining the gate dielectric 211 in this
10	region, and the benefit of that one, it provides 05:12:17
11	better adhesion, protection for the first wiring 127
12	and the overcoat 241.
13	So here Shiba says the gate dielectric 211
14	serves a function. And the hypothetical structures
15	that you allege may exist will weaken the adhesion 05:12:54
16	of the overcoat and of the first wiring line 127
17	and, thus, will compromise the selection of the
18	materials that could be used for the sealing agent
19	113.
20	Q Where does this paragraph talk about 05:13:42
21	adhesion? Oh, are you referring to line 34,
22	adhesion capacity?
23	A Are we in the same column 7?
24	MR. MURPHY: 54.
25	BY MR. MANZO: 05:14:01

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		Page 144
1	Q Oh, a 54. Sorry.	
2	The last line of that paragraph, you	
3	mentioned adhesion capacity. Is that the part	
4	you're	
5	A Well, that's the adhesion capacity of the	05:14:11
6	sealing agent, but let me explain.	
7	You have some structure on the glass and	
8	now you're going to bond that glass to another piece	
9	of glass and you're going to be using this sealing	
10	agent 113.	05:14:34
11	Q Yes.	
12	A But that process you may come down or you	
13	may move it or you may not place in the right	
14	location and then you have to remove it and reapply	
15	it again.	05:14:53
16	And as it reads in line 54, the risk or	
17	removal of the first wiring line 127 together with a	
18	sealing agent 113 is recused. This means that when	
19	you remove the sealing agent 113 to reposition the	
20	second glass in order to align the color filters	05:15:24
21	with the right pixels, you're running the risk of	
22	removing from the substrate the wiring 127 and the	
23	overcoat 241.	
24	If the sealant adheres very well to 241 and	L
25	241 adheres to 127, if you remove the sealant, you	05:15:47

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	Page	145
1	will tear those lines apart and you're causing a	
2	manufacturing defect.	
3	241 is anchored to the substrate through	
4	the gate dielectric 111. If you are removing the	
5	dielectric 211, you create a bigger step for the	05:16:13
6	overcoat 241 to go all the way down to the	
7	substrate.	
8	Furthermore, you may undercut the wirings	
9	127, and then will you start compromising how the	
10	wiring 127 adheres onto 211, because even portions	05:16:35
11	around the edges of 127 may be exposed because the	
12	211 may be undercut when you try to remove it.	
13	So removal of 211 will create a lot of	
14	risks in weakening the way that the 241 and 127	
15	adhere into the substrate and will make the	05:17:07
16	placement of the counter substrate a very dangerous	
17	operation.	
18	But if you leave it intact, and the process	
19	I said that someone with ordinary skill in the art	
20	will opt to do to create contacts only on top of	05:17:36
21	the in selected areas on top of the underlying	
22	layer, you will not remove the layer 211 from that	
23	entire region and, yes, it will be the structure	
24	will not be compromised.	
25	Q Thank you for that clarification. I think	05:18:02

		Page 146
1	we'll take a recess now, please.	
2	THE VIDEOGRAPHER: Off the record at	
3	5:18 p.m.	
4	(Recess.)	
5	THE VIDEOGRAPHER: Back on the record at	05:18:17
6	5:31 p.m.	
7	BY MR. MANZO:	
8	Q Professor, just to conclude, a little while	
9	ago you were talking about what happens if you need	
10	to move the counter substrate relative to the	05:31:52
11	substrate.	
12	Do you remember that?	
13	A I was just explaining the passage from	
14	column 7.	
15	Q Okay. And the question we want to ask you	05:32:02
16	is with regard to the wiring that's under the	
17	sealant	
18	A You mean 127?	
19	Q Yes.	
20	if that wiring strike that.	05:32:23
21	With regard to that wiring 127, is that	
22	wiring more likely or less likely to be ripped out,	1
23	as you said, if it is indium tin oxide or metal?	
24	MR. GIBSON: Object to the form.	
25	THE WITNESS: As I explained earlier, there	05:32:56

	Page 147
1	is no reason why to form that wiring with 127. The
2	indium tin oxide will result in a high resistance,
3	so your question is a little bit not relevant
4	because you will not form that wiring with indium
5	tin oxide, it will be very resistive, it will serve 05:33:30
6	no purpose.
7	BY MR. MANZO:
8	Q Well, hypothetically, if let's modify
9	the question, then. So make it hypothetical so that
10	in one case it is metal that is not covered with ITO 05:33:45
11	and in the other case it is metal that is covered
12	with ITO.
13	Is one more likely to be ripped out than
14	the other when the counter substrate has to be moved
15	relative to the substrate? 05:34:05
16	MR. GIBSON: Object to the form.
17	THE WITNESS: Even if, hypothetically, one
18	includes ITO, based on your statement, that will
19	still be under the protective overcoat 241.
20	The issue is not so much if you have or not 05:34:36
21	have ITO, which I explained I see no reason to have
22	ITO there, but the issue is whether you have or not
23	have the gate dielectric 211.
24	As I explained, Shiba teaches away from
25	removing the gate dielectric 211 from that region 05:35:01

		Page 148
1	because that will weaken the structure. And if you	
2	weaken the structure by removing the gate dielectric	
3	211, whether you have or do not have the ITO, the	
4	structure has already been compromised and it will	
5	be very weak based on the approach you discussed the	05:35:24
6	morning.	
7	So I'm trying to say Shiba teaches a way of	
8	removing the gate dielectric 211 and Shiba	
9	acknowledges that ITO is a highly resistive material	
10	so you would not use ITO to create a wiring line	05:35:56
11	that runs long distance. And Shiba acknowledges	
12	that the length of the first wiring line is very	
13	long because it has to distribute the power to the	
14	pads that in the opposing long side to the side	
15	where the power is supplied since the wiring line	05:36:20
16	has to be low resistance.	
17	In my testimony I have only described	
18	modifications in Shiba that will result in current	
19	flow transverse to the thickness of the Shiba	
20	sorry transverse to the thickness of indium tin	05:37:34
21	oxide, hence the presence of the layer will not add	
22	resistance, because that layer is so thin.	
23	But you're referring to a line that is very	
24	long. And I did not find the rationale anywhere in	
25	Shiba to add that line to the transparent	05:38:15

Pa	ge 149
conductive layer in the first wiring line.	
BY MR. MANZO:	
Q Okay. Well, let me ask you hypothetically,	
assuming that the dielectric 211 is present in the	
lines and one wire is ITO, hypothetically, and	05:38:39
another wire is another metal.	
Is the sealant adhesion different for the	
two wires?	
MR. GIBSON: Object to the form.	
THE WITNESS: I don't think I can answer	05:39:29
this hypothetical question because it all depend	
upon how you put the ITO, what stresses you have in	
the ITO layer and what is the passivation this	
protective overcoat 241, what is the gate dielectric	
on top of which the ITO will be deposited and what	05:39:55
is the sticking coefficient or the adhesion between	
the ITO and these layers.	
And then the same analysis has to be done	
for the other wire, which is made by another metal	
to convert them.	05:40:21
For example, you can put a wire down that	
it a metal down to form a wire, and if you have a	
lot of stresses built in that material during the	
deposition process or if the thermal expansion	
coefficient of that metal is such that when you are	05:40:42
	conductive layer in the first wiring line.  BY MR. MANZO:  Q Okay. Well, let me ask you hypothetically, assuming that the dielectric 211 is present in the lines and one wire is ITO, hypothetically, and another wire is another metal.  Is the sealant adhesion different for the two wires?  MR. GIBSON: Object to the form.  THE WITNESS: I don't think I can answer this hypothetical question because it all depend upon how you put the ITO, what stresses you have in the ITO layer and what is the passivation this protective overcoat 241, what is the gate dielectric on top of which the ITO will be deposited and what is the sticking coefficient or the adhesion between the ITO and these layers.  And then the same analysis has to be done for the other wire, which is made by another metal to convert them.  For example, you can put a wire down that it a metal down to form a wire, and if you have a lot of stresses built in that material during the

		Page 150
1	putting all these other layers and you're hitting	_
2	the structures, you build a lot of stresses in the	
3	structures, you may compromise the adhesion.	
4	So it's not a straightforward answer. You	
5	can have each the same material that as you go	05:41:03
6	through different deposition conditions and	
7	different annealing conditions, you can modify the	
8	stresses in the metal layer, and those ones may	
9	affect the way that layer interacts with the	
10	overcoat and the underlying coating, so there are a	05:41:36
11	lot of unknowns in your hypothetical example.	
12	Some layers, for example, aluminum and	
13	chromium, adhere very well. So there are layers	
14	that are known to have good adhesion. But even	
15	chromium, if you make it too thick, it will have a	05:42:03
16	lot of stresses and the adhesion will compromise.	
17	So a lot of factors affecting that.	
18	BY MR. MANZO:	
19	Q Thank you.	•
20	What is the adhesive property of ITO	05:42:57
21	relative to chromium and aluminum, which you did	
22	mention?	
23	A You mean the sticking coefficient of I	
24	think it is called sticking coefficient of I	
25	Q ITO?	05:43:32

	Page 151
1	A Yeah. I have to work from memory, I don't
2	know exact numbers, but I know, for example,
3	chromium is a very good adhesion to silicon dioxide
4	layer, and most likely it will be better than ITO.
5	I just don't remember exact numbers or relative 05:43:55
6	rating of the two. I know some are better than
7	others.
8	MR. MANZO: Thank you, Professor. We have
9	no further questions.
10	MR. GIBSON: I do not have any questions. 05:44:19
11	THE VIDEOGRAPHER: We are off the record at
12	5:44 p.m. This concludes today's testimony given by
13	Dr. Hatalis. The total number of media was 3 and
14	will be retained by Veritext LLC.
15	(TIME NOTED: 5:44 p.m.)
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	Page 152
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4	I, MILTIADIS HATALIS, PH.D., do hereby
5	declare under penalty of perjury that I have read
6	the foregoing transcript; that I have made any
7	corrections as appear noted, in ink, initialed by
8	me, or attached hereto; that my testimony as
9	contained herein, as corrected, is true and correct.
10	
11	
12	EXECUTED thisday of,
13	2013, at
	(City) (State)
14	
15	
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	MILTIADIS HATALIS, PH.D.
17	Volume I
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Page 153 I, the undersigned, a Certified Shorthand 1 Reporter of the State of California, do hereby 2 certify: 3 That the foregoing proceedings were taken 4 before me at the time and place herein set forth; 5 that any witnesses in the foregoing proceedings, 6 prior to testifying, were placed under oath; that a 7 true and correct record of the proceedings was made 8 by me using machine shorthand which was thereafter 9 transcribed under my direction; further, that the 10 11 foregoing is an accurate transcription thereof. I further certify that I am neither 12 financially interested in the action nor a relative 13 or employee of any attorney of any of the parties. 14 15

IN WITNESS WHEREOF, I have this date subscribed my name.

CSR No. 11241

Dated: 8 July 2013

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BARDSLEY

&	<b>01:53:24</b> 88:10	<b>02:25:29</b> 97:5	<b>02:47:57</b> 105:25
<b>&amp;</b> 3:4,13 7:11	<b>01:53:42</b> 88:15	<b>02:25:54</b> 97:10	<b>02:48:17</b> 106:5
	01:54:01 88:20	<b>02:26:22</b> 97:15	<b>02:48:49</b> 106:10
0	<b>01:54:38</b> 88:25	<b>02:26:55</b> 97:20	<b>02:49:12</b> 106:15
<b>01:29:32</b> 80:10	<b>01:55:16</b> 89:5	<b>02:27:20</b> 97:25	<b>02:49:40</b> 106:20
<b>01:30:21</b> 80:15	<b>01:55:46</b> 89:10	<b>02:27:57</b> 98:5	<b>02:50:17</b> 106:25
<b>01:31:00</b> 80:20	<b>01:56:20</b> 89:15	<b>02:28:39</b> 98:10	<b>02:50:34</b> 107:5
<b>01:31:17</b> 80:25	<b>01:56:49</b> 89:20	<b>02:29:09</b> 98:15	<b>02:51:03</b> 107:10
<b>01:31:42</b> 81:5	01:57:13 89:25	<b>02:29:50</b> 98:20	<b>02:51:18</b> 107:15
<b>01:32:15</b> 81:10	<b>01:59:16</b> 90:5	<b>02:30:03</b> 98:25	<b>02:51:39</b> 107:20
<b>01:32:38</b> 81:15	<b>01:59:59</b> 90:10	<b>02:30:13</b> 99:5	<b>02:52:01</b> 107:25
01:33:43 81:20	<b>02:00:18</b> 90:15	<b>02:30:41</b> 99:10	<b>02:52:29</b> 108:5
<b>01:35:47</b> 81:25	<b>02:00:51</b> 90:20	<b>02:30:55</b> 99:15	<b>02:52:57</b> 108:10
01:36:04 82:5	<b>02:01:25</b> 90:25	<b>02:31:43</b> 99:20	<b>02:53:27</b> 108:15
<b>01:36:36</b> 82:10	<b>02:01:52</b> 91:5	<b>02:32:00</b> 99:25	<b>02:53:34</b> 108:20
01:37:21 82:15	<b>02:02:18</b> 91:10	<b>02:32:37</b> 100:5	<b>03:13:33</b> 108:25
01:37:48 82:20	<b>02:02:57</b> 91:15	<b>02:33:05</b> 100:10	<b>03:14:44</b> 109:5
01:38:16 82:25	<b>02:03:31</b> 91:20	<b>02:33:33</b> 100:15	<b>03:15:14</b> 109:10
01:38:34 83:5	<b>02:04:05</b> 91:25	<b>02:33:50</b> 100:20	<b>03:15:46</b> 109:15
01:39:02 83:10	<b>02:04:25</b> 92:5	<b>02:34:48</b> 100:25	<b>03:16:15</b> 109:20
01:39:35 83:15	<b>02:04:49</b> 92:10	<b>02:35:08</b> 101:5	<b>03:18:57</b> 109:25
01:40:06 83:20	<b>02:06:50</b> 92:15	<b>02:36:34</b> 101:10	<b>03:19:24</b> 110:5
01:40:37 83:25	<b>02:07:09</b> 92:20	<b>02:37:35</b> 101:15	<b>03:19:46</b> 110:10
<b>01:40:58</b> 84:5	<b>02:14:36</b> 92:25	<b>02:38:01</b> 101:20	<b>03:20:16</b> 110:15
01:41:47 84:10	<b>02:15:11</b> 93:5	<b>02:38:25</b> 101:25	<b>03:20:39</b> 110:20
01:42:14 84:15	<b>02:15:56</b> 93:10	<b>02:38:43</b> 102:5	<b>03:21:06</b> 110:25
01:42:45 84:20	<b>02:16:38</b> 93:15	<b>02:39:16</b> 102:10	<b>03:21:32</b> 111:5
01:43:12 84:25	<b>02:17:19</b> 93:20	<b>02:39:46</b> 102:15	03:22:44 111:10
01:43:37 85:5	<b>02:17:51</b> 93:25	<b>02:40:11</b> 102:20	03:23:05 111:15
01:44:19 85:10	02:18:13 94:5	<b>02:40:32</b> 102:25	03:23:21 111:20
01:44:50 85:15	<b>02:18:41</b> 94:10	<b>02:40:58</b> 103:5	03:23:46 111:25
01:45:12 85:20	<b>02:19:08</b> 94:15	<b>02:41:20</b> 103:10	03:24:13 112:5
01:47:59 85:25	<b>02:19:36</b> 94:20	<b>02:41:39</b> 103:15	03:24:34 112:10
01:48:18 86:5	<b>02:20:00</b> 94:25	<b>02:42:01</b> 103:20	03:25:00 112:15
<b>01:48:36</b> 86:10 <b>01:48:48</b> 86:15	02:20:24 95:5	<b>02:42:28</b> 103:25	<b>03:25:23</b> 112:20
· ·	<b>02:21:00</b> 95:10	<b>02:42:56</b> 104:5	03:25:48 112:25
01:49:24 86:20	02:21:25 95:15	02:43:09 104:10	03:26:23 113:5
<b>01:49:56</b> 86:25 <b>01:50:12</b> 87:5	<b>02:21:53</b> 95:20	<b>02:43:38</b> 104:15	03:27:09 113:10
	02:22:19 95:25	<b>02:44:25</b> 104:20	03:27:40 113:15
<b>01:50:47</b> 87:10	02:22:52 96:5	<b>02:44:47</b> 104:25	<b>03:28:14</b> 113:20
<b>01:51:19</b> 87:15 <b>01:51:49</b> 87:20	02:23:17 96:10	<b>02:45:51</b> 105:5	03:28:55 113:25
01:51:49 87:20 01:52:22 87:25	02:23:36 96:15	<b>02:46:25</b> 105:10	03:30:04 114:5
01:52:22 87:23 01:52:57 88:5	<b>02:24:38</b> 96:20	<b>02:46:50</b> 105:15	03:31:37 114:10
U1.32.37 00.3	<b>02:25:02</b> 96:25	<b>02:47:18</b> 105:20	<b>03:32:18</b> 114:15

03:32:56         114:20         03:56:03         123:15         04:39:44         132:10         05:07:40         141:5           03:33:23         114:25         03:56:41         123:20         04:40:05         132:15         05:08:14         141:10           03:34:12         115:5         03:56:59         123:25         04:40:44         132:20         05:08:39         141:15           03:34:38         115:10         03:57:18         124:5         04:41:23         132:25         05:09:05         141:20           03:35:42         115:15         04:11:32         124:10         04:42:08         133:5         05:09:20         141:25           03:36:50         115:20         04:13:36         124:20         04:43:18         133:10         05:09:43         142:5           03:38:18         116:5         04:14:11         124:25         04:44:05         133:20         05:10:30         142:15           03:39:35         116:15         04:16:22         125:10         04:45:08         134:5         05:11:36         142:25           03:40:01         116:20         04:16:55         125:15         04:45:26         134:10         05:11:53         143:5
03:34:12         115:5         03:56:59         123:25         04:40:44         132:20         05:08:39         141:15           03:34:38         115:10         03:57:18         124:5         04:41:23         132:25         05:09:05         141:20           03:35:42         115:15         04:11:32         124:10         04:42:08         133:5         05:09:20         141:25           03:36:19         115:20         04:12:17         124:15         04:42:45         133:10         05:09:43         142:5           03:36:56         115:25         04:13:36         124:20         04:43:18         133:15         05:10:09         142:10           03:38:18         116:5         04:14:11         124:25         04:44:05         133:20         05:10:30         142:15           03:39:04         116:10         04:15:01         125:5         04:44:34         133:25         05:11:11         142:20           03:39:35         116:15         04:16:22         125:10         04:45:08         134:5         05:11:36         142:25
03:34:38         115:10         03:57:18         124:5         04:41:23         132:25         05:09:05         141:20           03:35:42         115:15         04:11:32         124:10         04:42:08         133:5         05:09:20         141:25           03:36:19         115:20         04:12:17         124:15         04:42:45         133:10         05:09:43         142:5           03:36:56         115:25         04:13:36         124:20         04:43:18         133:15         05:10:09         142:10           03:38:18         116:5         04:14:11         124:25         04:44:05         133:20         05:10:30         142:15           03:39:04         116:10         04:15:01         125:5         04:44:34         133:25         05:11:11         142:20           03:39:35         116:15         04:16:22         125:10         04:45:08         134:5         05:11:36         142:25
03:35:42         115:15         04:11:32         124:10         04:42:08         133:5         05:09:20         141:25           03:36:19         115:20         04:12:17         124:15         04:42:45         133:10         05:09:43         142:5           03:36:56         115:25         04:13:36         124:20         04:43:18         133:15         05:10:09         142:10           03:38:18         116:5         04:14:11         124:25         04:44:05         133:20         05:10:30         142:15           03:39:04         116:10         04:15:01         125:5         04:44:34         133:25         05:11:11         142:20           03:39:35         116:15         04:16:22         125:10         04:45:08         134:5         05:11:36         142:25
03:36:19         115:20         04:12:17         124:15         04:42:45         133:10         05:09:43         142:5           03:36:56         115:25         04:13:36         124:20         04:43:18         133:15         05:10:09         142:10           03:38:18         116:5         04:14:11         124:25         04:44:05         133:20         05:10:30         142:15           03:39:04         116:10         04:15:01         125:5         04:44:34         133:25         05:11:11         142:20           03:39:35         116:15         04:16:22         125:10         04:45:08         134:5         05:11:36         142:25
03:36:56       115:25       04:13:36       124:20       04:43:18       133:15       05:10:09       142:10         03:38:18       116:5       04:14:11       124:25       04:44:05       133:20       05:10:30       142:15         03:39:04       116:10       04:15:01       125:5       04:44:34       133:25       05:11:11       142:20         03:39:35       116:15       04:16:22       125:10       04:45:08       134:5       05:11:36       142:25
03:38:18       116:5       04:14:11       124:25       04:44:05       133:20       05:10:30       142:15         03:39:04       116:10       04:15:01       125:5       04:44:34       133:25       05:11:11       142:20         03:39:35       116:15       04:16:22       125:10       04:45:08       134:5       05:11:36       142:25
03:39:04       116:10       04:15:01       125:5       04:44:34       133:25       05:11:11       142:20         03:39:35       116:15       04:16:22       125:10       04:45:08       134:5       05:11:36       142:25
<b>03:39:35</b> 116:15 <b>04:16:22</b> 125:10 <b>04:45:08</b> 134:5 <b>05:11:36</b> 142:25
<b>03:40:01</b>   116:20   <b>04:16:55</b>   125:15   <b>04:45:26</b>   134:10   <b>05:11:53</b>   143:5
<b>03:41:31</b> 116:25 <b>04:17:31</b> 125:20 <b>04:46:10</b> 134:15 <b>05:12:17</b> 143:10
<b>03:41:56</b> 117:5
<b>03:43:10</b> 117:10 <b>04:18:20</b> 126:5 <b>04:47:18</b> 134:25 <b>05:13:42</b> 143:20
<b>03:43:37</b> 117:15 <b>04:18:39</b> 126:10 <b>04:47:50</b> 135:5 <b>05:14:01</b> 143:25
<b>03:43:46</b> 117:20 <b>04:18:59</b> 126:15 <b>04:50:24</b> 135:10 <b>05:14:11</b> 144:5
<b>03:44:08</b> 117:25 <b>04:19:15</b> 126:20 <b>04:51:53</b> 135:15 <b>05:14:34</b> 144:10
<b>03:44:25</b> 118:5 <b>04:19:23</b> 126:25 <b>04:52:18</b> 135:20 <b>05:14:53</b> 144:15
<b>03:44:46</b> 118:10 <b>04:19:50</b> 127:5 <b>04:52:46</b> 135:25 <b>05:15:24</b> 144:20
<b>03:45:09</b> 118:15 <b>04:20:07</b> 127:10 <b>04:53:07</b> 136:5 <b>05:15:47</b> 144:25
<b>03:45:37</b> 118:20 <b>04:20:42</b> 127:15 <b>04:53:27</b> 136:10 <b>05:16:13</b> 145:5
<b>03:45:58</b> 118:25 <b>04:21:29</b> 127:20 <b>04:54:09</b> 136:15 <b>05:16:35</b> 145:10
<b>03:46:18</b> 119:5 <b>04:21:47</b> 127:25 <b>04:56:52</b> 136:20 <b>05:17:07</b> 145:15
<b>03:46:38</b> 119:10 <b>04:24:27</b> 128:5 <b>04:57:20</b> 136:25 <b>05:17:36</b> 145:20
<b>03:46:55</b> 119:15 <b>04:25:36</b> 128:10 <b>04:58:23</b> 137:5 <b>05:18:02</b> 145:25
<b>03:47:24</b> 119:20 <b>04:26:16</b> 128:15 <b>04:58:43</b> 137:10 <b>05:18:17</b> 146:5
<b>03:47:58</b> 119:25 <b>04:26:38</b> 128:20 <b>04:59:13</b> 137:15 <b>05:31:52</b> 146:10
<b>03:48:24</b> 120:5 <b>04:29:13</b> 128:25 <b>04:59:49</b> 137:20 <b>05:32:02</b> 146:15
<b>03:48:38</b> 120:10 <b>04:29:45</b> 129:5 <b>05:00:31</b> 137:25 <b>05:32:23</b> 146:20
<b>03:49:05</b> 120:15 <b>04:30:01</b> 129:10 <b>05:01:08</b> 138:5 <b>05:32:56</b> 146:25
<b>03:49:41</b> 120:20 <b>04:31:07</b> 129:15 <b>05:01:24</b> 138:10 <b>05:33:30</b> 147:5
<b>03:50:05</b> 120:25 <b>04:31:49</b> 129:20 <b>05:02:18</b> 138:15 <b>05:33:45</b> 147:10
<b>03:50:24</b> 121:5 <b>04:32:11</b> 129:25 <b>05:02:45</b> 138:20 <b>05:34:05</b> 147:15
<b>03:50:38</b>   121:10   <b>04:32:31</b>   130:5   <b>05:03:07</b>   138:25   <b>05:34:36</b>   147:20
<b>03:50:56</b> 121:15
<b>03:51:17</b> 121:20 <b>04:33:49</b> 130:15 <b>05:04:13</b> 139:10 <b>05:35:24</b> 148:5
<b>03:52:13</b>   121:25   <b>04:34:11</b>   130:20   <b>05:04:40</b>   139:15   <b>05:35:56</b>   148:10
<b>03:52:38</b> 122:5 <b>04:35:13</b> 130:25 <b>05:05:16</b> 139:20 <b>05:36:20</b> 148:15
<b>03:53:04</b> 122:10 <b>04:35:30</b> 131:5 <b>05:05:41</b> 139:25 <b>05:37:34</b> 148:20
<b>03:53:27</b> 122:15 <b>04:36:00</b> 131:10 <b>05:05:59</b> 140:5 <b>05:38:15</b> 148:25
<b>03:54:00</b> 122:20 <b>04:36:27</b> 131:15 <b>05:06:19</b> 140:10 <b>05:38:39</b> 149:5
<b>03:54:24</b> 122:25 <b>04:37:32</b> 131:20 <b>05:06:34</b> 140:15 <b>05:39:29</b> 149:10
<b>03:55:02</b> 123:5 <b>04:38:07</b> 131:25 <b>05:06:52</b> 140:20 <b>05:39:55</b> 149:15
<b>03:55:44</b> 123:10 <b>04:38:22</b> 132:5 <b>05:07:15</b> 140:25 <b>05:40:21</b> 149:20

05:40:42	149:25	09:27:27	13:15	<b>09:47:46</b> 22:10	<b>107</b> 95:8 125:22
05:41:03	150:5	09:27:27	13:20	<b>09:48:31</b> 22:15	126:11 127:2
05:41:36	150:10	09:28:49	13:25	<b>09:48:54</b> 22:20	<b>10:00:00</b> 27:25
05:42:03	150:15	09:29:20	14:5	<b>09:49:16</b> 22:25	10:00:30 28:5
05:42:57	150:15	09:29:52	14:10	<b>09:49:10</b> 22:23 <b>09:49:29</b> 23:5	10:00:30 28:3 10:00:44 28:10
05:42:37	150:25	09:29:32	14:15	09:49:29 23:3 09:50:07 23:10	10:01:02 28:15
05:43:55	150.25	09:30:46	14:13	<b>09:50:43</b> 23:15	10:01:32 28:20
05:44:19	151:3	09:30:40	14:25	<b>09:51:14</b> 23:20	10:01:48 28:25
09:14:45	6:5	09:31:28	15:5	<b>09:51:38</b> 23:25	10:01:48 28:23 10:02:11 29:5
09:15:07		09:31:50	15:10	<b>09:52:05</b> 24:5	<b>10:02:35</b> 29:10
09:15:34	6:15	09:32:24	15:15	<b>09:52:24</b> 24:10	<b>10:03:04</b> 29:15
09:15:57	6:20	09:32:40	15:20	<b>09:52:53</b> 24:15	10:03:30 29:20
09:16:08	6:25		15:25	<b>09:53:29</b> 24:20	10:03:43 29:25
09:16:17	7:5	09:33:36	16:5	<b>09:53:52</b> 24:25	<b>10:04:03</b> 30:5
09:16:38	7:10	09:33:51	16:10	<b>09:54:30</b> 25:5	<b>10:04:17</b> 30:10
09:16:47	7:15	09:34:14	16:15	<b>09:54:54</b> 25:10	<b>10:04:35</b> 30:15
09:17:11	7:25	09:34:42	16:20	<b>09:55:08</b> 25:15	<b>10:05:07</b> 30:20
09:17:40	8:5	09:35:19	16:25	<b>09:55:43</b> 25:20	<b>10:05:35</b> 30:25
09:18:29	8:10	09:35:38	17:5	<b>09:55:59</b> 25:25	10:06:03 31:5
09:18:44	8:15	09:36:35	17:10	<b>09:56:30</b> 26:5	10:06:55 31:10
09:19:04	8:20	09:36:55	17:15	<b>09:57:12</b> 26:10	<b>10:07:25</b> 31:15
09:19:20	8:25	09:37:16	17:20	<b>09:57:34</b> 26:15	<b>10:07:51</b> 31:20
09:19:32	9:5	09:37:56	17:25	<b>09:57:52</b> 26:20	<b>10:08:23</b> 31:25
09:19:41	9:10	09:38:23	18:5	<b>09:58:19</b> 26:25	<b>10:08:46</b> 32:5
09:19:59	9:15	09:38:41	18:10	<b>09:58:40</b> 27:5	<b>10:09:04</b> 32:10
09:20:16	9:20	09:39:25	18:15	<b>09:58:49</b> 27:10	<b>10:09:36</b> 32:15
09:20:27	9:25	09:39:50	18:20	<b>09:59:04</b> 27:15	<b>10:10:09</b> 32:20
09:20:40	10:5	09:40:20	18:25	<b>09:59:32</b> 27:20	<b>10:10:50</b> 32:25
09:21:03	10:10	09:40:57	19:5	1	<b>10:11:16</b> 33:5
09:21:24	10:15	09:41:20	19:10	1 1:25 19:13,15,17	<b>10:11:50</b> 33:10
09:21:42	10:20	09:41:32	19:15	21:24 22:16,18	<b>10:12:24</b> 33:15
	10:25		19:20	25:14,20 26:3,15	<b>10:13:08</b> 33:20
09:22:26		09:42:14		28:11,22 31:11	<b>10:13:33</b> 33:25
09:22:49		09:42:34		41:13 42:19 60:23	<b>10:13:57</b> 34:5
09:23:41	11:15	ł	20:10	78:12 80:9 90:12	<b>10:14:25</b> 34:10
09:24:11		09:43:55		10 90:9	<b>10:14:42</b> 34:15
09:24:27		09:44:34		<b>100</b> 96:19 97:1,8,10	<b>10:15:09</b> 34:20
09:24:53		09:44:55		97:12,16	<b>10:15:42</b> 34:25
09:25:16		1	21:5	1001 11:5 12:3	<b>10:16:23</b> 35:5
09:25:51		09:46:07		1003 8:1 47:4	<b>10:17:01</b> 35:10
09:26:21		1	21:15	<b>1005</b> 87:3	<b>10:17:18</b> 35:15
09:26:48	12:25	09:46:34	21:20	1007 11:11,13 41:24	<b>10:17:43</b> 35:20
09:26:59	13:5	09:46:58		<b>105</b> 129:17	10:18:35 35:25
09:27:15	13:10	09:47:16	22:5		<b>10:19:01</b> 36:5

<b>10:19:34</b> 36:10	115 3:16 22:3	<b>11:30:07</b> 50:5	11:47:44 58:25
<b>10:19:56</b> 36:15	<b>11:13</b> 41:21	<b>11:30:29</b> 50:10	<b>11:49:15</b> 59:5
<b>10:20:36</b> 36:20	11:13:38 41:20	<b>11:30:46</b> 50:15	<b>11:49:34</b> 59:10
<b>10:21:12</b> 36:25	<b>11:14:39</b> 41:25	<b>11:31:08</b> 50:20	<b>11:50:04</b> 59:15
<b>10:21:58</b> 37:5	11:15:06 42:5	<b>11:31:27</b> 50:25	<b>11:50:19</b> 59:20
<b>10:22:26</b> 37:10	11:15:35 42:10	<b>11:31:49</b> 51:5	<b>11:50:28</b> 59:25
<b>10:23:20</b> 37:15	<b>11:16:22</b> 42:15	<b>11:32:37</b> 51:10	<b>11:50:43</b> 60:5
<b>10:24:31</b> 37:20	<b>11:17:07</b> 42:20	<b>11:32:57</b> 51:15	<b>11:51:08</b> 60:10
<b>10:25:08</b> 37:25	<b>11:17:31</b> 42:25	<b>11:33:18</b> 51:20	<b>11:51:31</b> 60:15
<b>10:25:37</b> 38:5	<b>11:17:55</b> 43:5	<b>11:33:44</b> 51:25	11:51:54 60:20
<b>10:26:07</b> 38:10	<b>11:18:19</b> 43:10	<b>11:34:05</b> 52:5	<b>11:52:28</b> 60:25
<b>10:26:31</b> 38:15	<b>11:18:42</b> 43:15	<b>11:34:30</b> 52:10	<b>11:52:55</b> 61:5
<b>10:26:57</b> 38:20	<b>11:19:02</b> 43:20	<b>11:34:50</b> 52:15	<b>11:53:15</b> 61:10
<b>10:27:57</b> 38:25	<b>11:19:36</b> 43:25	<b>11:35:14</b> 52:20	<b>11:53:39</b> 61:15
10:28:23 39:5	<b>11:20:09</b> 44:5	<b>11:35:34</b> 52:25	<b>11:54:04</b> 61:20
<b>10:28:38</b> 39:10	<b>11:20:31</b> 44:10	<b>11:36:07</b> 53:5	<b>11:54:49</b> 61:25
<b>10:29:01</b> 39:15	<b>11:20:58</b> 44:15	<b>11:36:28</b> 53:10	<b>11:55:16</b> 62:5
<b>10:29:28</b> 39:20	<b>11:21:29</b> 44:20	<b>11:36:52</b> 53:15	<b>11:55:36</b> 62:10
<b>10:29:43</b> 39:25	<b>11:21:37</b> 44:25	<b>11:37:06</b> 53:20	<b>11:55:49</b> 62:15
<b>10:30:25</b> 40:5	<b>11:21:51</b> 45:5	<b>11:37:24</b> 53:25	<b>11:56:59</b> 62:20
<b>10:30:44</b> 40:10	<b>11:22:07</b> 45:10	<b>11:37:38</b> 54:5	<b>11:57:22</b> 62:25
<b>10:31:05</b> 40:15	<b>11:22:30</b> 45:15	<b>11:37:57</b> 54:10	<b>11:57:34</b> 63:5
<b>10:31:25</b> 40:20	<b>11:22:49</b> 45:20	<b>11:38:17</b> 54:15	<b>11:57:56</b> 63:10
<b>10:31:45</b> 40:25	<b>11:23:06</b> 45:25	<b>11:38:33</b> 54:20	<b>11:58:31</b> 63:15
<b>10:32</b> 41:12	<b>11:23:32</b> 46:5	<b>11:38:47</b> 54:25	<b>11:58:48</b> 63:20
<b>10:32:12</b> 41:5	<b>11:24:02</b> 46:10	<b>11:39:18</b> 55:5	<b>11:59:26</b> 63:25
<b>10:32:36</b> 41:10	<b>11:24:23</b> 46:15	<b>11:39:39</b> 55:10	<b>11:59:47</b> 64:5
<b>10:32:52</b> 41:15	<b>11:24:53</b> 46:20	<b>11:39:58</b> 55:15	<b>12</b> 42:23
<b>110</b> 137:25	<b>11:25:12</b> 46:25	<b>11:40:12</b> 55:20	<b>120</b> 4:8
<b>1100</b> 2:16 3:7 6:12	<b>11:25:26</b> 47:5	<b>11:40:39</b> 55:25	<b>121</b> 22:16,25 23:20
111 17:23,23,25	<b>11:25:52</b> 47:10	<b>11:41:02</b> 56:5	24:18 30:17 43:2
18:2,5,8 29:13,16	<b>11:26:17</b> 47:15	<b>11:41:24</b> 56:10	73:1 133:18,19
29:17,19,21 145:4	<b>11:26:34</b> 47:20	<b>11:41:46</b> 56:15	<b>121-1</b> 23:14 25:7
<b>11241</b> 1:22 2:19	<b>11:26:57</b> 47:25	<b>11:42:02</b> 56:20	37:2 62:20 63:23
153:20	<b>11:27:16</b> 48:5	<b>11:42:42</b> 56:25	<b>121-4</b> 23:14 63:24
113 29:3,8,12,16	<b>11:27:40</b> 48:10	<b>11:43:28</b> 57:5	<b>123</b> 24:18 30:17
78:24 79:3,4 93:8	<b>11:27:54</b> 48:15	<b>11:43:50</b> 57:10	35:8 37:8
95:10 125:7,23	<b>11:28:08</b> 48:20	<b>11:44:15</b> 57:15	<b>123-1</b> 23:15 25:8
126:13 127:3	<b>11:28:23</b> 48:25	<b>11:44:36</b> 57:20	62:18 63:25
129:11 142:23,25	<b>11:28:44</b> 49:5	<b>11:44:49</b> 57:25	<b>123-4</b> 23:15 63:25
143:19 144:10,18	<b>11:29:03</b> 49:10	<b>11:45:55</b> 58:5	<b>125</b> 24:19 25:8
144:19	<b>11:29:11</b> 49:15	<b>11:46:34</b> 58:10	30:18 57:3 73:1
<b>114</b> 93:7,18 95:11	<b>11:29:22</b> 49:20	<b>11:46:49</b> 58:15	<b>125a</b> 19:9 23:14
125:6,24 126:13	<b>11:29:42</b> 49:25	<b>11:47:18</b> 58:20	25:9,9,10 31:10
127:4 129:13,17			57:2,2,6 62:17

	40 44 40 60 5	10.00.40.56.05	200 70 4 06 20 21
63:24	12:11:10 68:5	<b>12:33:49</b> 76:25	<b>200</b> 79:4 96:20,21
<b>125b</b> 23:15 63:24	12:11:34 68:10	<b>12:35:26</b> 77:5	97:2,8,10,13,16
<b>127</b> 23:16 24:19	<b>12:12:16</b> 68:15	<b>12:36:00</b> 77:10	<b>201</b> 20:19 42:25
25:8,25 26:16,23	<b>12:12:39</b> 68:20	<b>12:36:18</b> 77:15	<b>2013</b> 1:16 2:18 6:1,6
29:22 30:18 31:8,8	<b>12:13:06</b> 68:25	<b>12:36:41</b> 77:20	41:12 152:13
31:9,12,15,18,18,21	<b>12:13:26</b> 69:5	<b>12:37:13</b> 77:25	<b>201a</b> 31:17
32:3,17,19 37:9	<b>12:13:54</b> 69:10	<b>12:37:32</b> 78:5	<b>201b</b> 31:13
42:14 43:11 44:9	<b>12:14:15</b> 69:15	<b>12:37:48</b> 78:10	<b>201c</b> 19:2 31:16
45:25 46:7,7 47:7	<b>12:15:13</b> 69:20	<b>12:38:02</b> 78:15	<b>201d</b> 18:25 31:12
48:4 49:2 50:3 51:7	<b>12:15:44</b> 69:25	<b>12:38:26</b> 78:20	<b>204</b> 8:20 12:4,5,6,6
55:4,7,7,8 56:19	<b>12:16:12</b> 70:5	<b>12:38:42</b> 78:25	12:7,21 31:6 93:4
57:1,5,9,11,12,14,14	<b>12:16:40</b> 70:10	<b>12:39:06</b> 79:5	94:15,16,17,18 96:6
58:16,18,19,25 59:2	<b>12:17:11</b> 70:15	<b>12:39:43</b> 79:10	113:5,7,8,13 116:19
60:8,9 61:8,10,12	<b>12:17:53</b> 70:20	<b>12:40:15</b> 79:15	125:14 128:23
61:16,20,21 62:8,16	<b>12:18:22</b> 70:25	<b>12:40:58</b> 79:20	130:1,7,13,16,22
63:10,12 64:1,6	<b>12:18:40</b> 71:5	<b>12:41</b> 80:9	132:24 134:20
65:8 66:2 67:9 68:7	<b>12:18:58</b> 71:10	<b>12:41:16</b> 79:25	<b>21</b> 121:24
68:21 71:14,20	<b>12:20:35</b> 71:15	<b>12:41:40</b> 80:5	<b>211</b> 37:16 38:10,16
72:19,23 73:6,8,18	<b>12:21:01</b> 71:20	<b>14</b> 83:14 97:18,20	43:2 44:12,17 58:22
73:19 74:12 141:12	<b>12:21:54</b> 71:25	97:20,22 120:6	58:23 60:15 61:13
141:15 142:17,22	12:22:22 72:5	121:12	67:15 76:24 77:2,5
143:11,16 144:17	12:22:32 72:10	<b>140</b> 90:19	77:17,22,24 79:8,14
144:22,25 145:9,10	<b>12:23:03</b> 72:15	<b>149</b> 90:11	79:14,22 81:16
145:11,14 146:18	<b>12:23:33</b> 72:20	<b>15</b> 29:11 43:16	141:11 142:20
146:21 147:1	<b>12:23:59</b> 72:25	<b>153</b> 1:25	143:9,13 145:5,10
<b>12:00:19</b> 64:10	<b>12:24:26</b> 73:5	<b>16</b> 44:18	145:12,13,22
<b>12:01:11</b> 64:15	<b>12:24:43</b> 73:10	<b>1683385</b> 1:23	147:23,25 148:3,8
<b>12:01:29</b> 64:20	<b>12:25:05</b> 73:15	<b>17</b> 52:13 117:1	149:4
<b>12:01:48</b> 64:25	<b>12:25:29</b> 73:20	<b>19</b> 117:7	<b>22</b> 115:6 135:23
<b>12:02:06</b> 65:5	<b>12:25:53</b> 73:25	<b>1997</b> 35:15 39:16	<b>2200</b> 4:8
<b>12:02:51</b> 65:10	12:26:12 74:5	93:20	<b>22033</b> 4:16
<b>12:03:20</b> 65:15	<b>12:26:31</b> 74:10	1:30 80:12	<b>221</b> 42:21 67:13
<b>12:03:40</b> 65:20	<b>12:26:55</b> 74:15	<b>1b</b> 86:21 87:8 93:22	<b>23</b> 109:22 114:24
<b>12:04:05</b> 65:25	<b>12:27:17</b> 74:20	96:8 126:8	135:7
<b>12:04:19</b> 66:5	<b>12:28:25</b> 74:25	2	<b>241</b> 67:9,11,12
<b>12:04:40</b> 66:10	<b>12:29:29</b> 75:5		69:15,16,17,20,22
<b>12:05:03</b> 66:15	<b>12:29:52</b> 75:10	2 1:16 2:18 6:1	70:13 75:3 142:19
<b>12:05:19</b> 66:20	<b>12:30:02</b> 75:15	27:14 28:12 80:12	143:12 144:23,24
<b>12:05:36</b> 66:25	<b>12:30:02</b> 75:19 <b>12:31:03</b> 75:20	89:9,11,19 90:5,14	144:25 145:3,6,14
<b>12:07:01</b> 67:5	<b>12:31:44</b> 75:25	91:19,21 92:13	147:19 149:14
<b>12:07:27</b> 67:10	<b>12:31:44</b> 75:25 <b>12:32:10</b> 76:5	98:18,21,24 99:5,19	<b>243</b> 134:1
<b>12:08:23</b> 67:15	<b>12:32:34</b> 76:10	107:11 124:4	<b>25</b> 23:10 124:12
<b>12:09:30</b> 67:20	<b>12:32:57</b> 76:15	<b>20</b> 117:8,14 118:10	135:8
<b>12:10:39</b> 67:25	<b>12:32:37</b> 76:13 <b>12:33:13</b> 76:20	124:12 137:25	<b>251</b> 37:17,19 58:1
12.10.37 01.43	12.55.15 /0.20		67:14 84:8
			07.17 07.0

27 62:22	29 25.25 72.2	4h 02.60 120.24	(22 7200 2.0
<b>27</b> 63:22	38 25:25 73:3	<b>4b</b> 93:6,9 128:24	<b>623-7200</b> 3:9
<b>29</b> 67:11	109:22 114:25	5	63 117:2
<b>2:07</b> 92:22	<b>39</b> 51:5	<b>5</b> 20:8 25:3,3,5,6,7	640 18:19,19
<b>2:14</b> 93:1	<b>3975</b> 4:15	25:14 27:17,18 28:9	655-1500 4:10
<b>2:53</b> 108:18	<b>3:13</b> 108:22	28:10,11 31:8 57:4	<b>69</b> 67:18,20 69:5
2c 98:12,14 99:15,20	<b>3:57</b> 124:4	118:10	7
99:20,25 100:10	<b>3a</b> 96:17	<b>5,684,555</b> 8:2	7 5:6 37:18 52:18
113:18 128:16,19	3d 96:17	<b>500</b> 22:11 79:4	60:23 86:23 87:8,9
<b>2nd</b> 6:6 41:12	3e 97:17 98:14,17	82:24	87:17,21,22,23
3	99:15 100:4 110:14	<b>501</b> 22:9,11	88:23 89:4,9,11,19
<b>3</b> 2:16 3:7 6:11	113:17	<b>51</b> 20:9	89:19 90:1,1,5
16:21,22 17:2,21	4	<b>53</b> 95:5 125:16	91:19,21,24 92:11
19:14,15,18,21 20:1	4 21:3,23 27:15,21	<b>54</b> 117:6 124:10	92:12,13 97:22 98:7
21:4 22:14 25:13	28:12 29:4 32:22	132:12,16,23	100:2,5,6 104:16,19
26:25 27:14,22	33:1,3,4,10,16	134:19 135:6 143:6	105:8,10,11,12,19
28:12 29:14,17	34:24 36:19 37:9,13	143:24 144:1,16	105:21 106:9,21,22
30:15 31:4 32:13,22	37:18,22 42:19,22	<b>541</b> 84:9 85:5	106:25 107:9,11
33:1,5,14 37:10	42:23 43:15 44:18	<b>55</b> 90:23 91:25 95:6	110:16 111:9,15,19
56:24,25 60:24 61:8	58:7,18,22 67:11	105:25 125:16	114:6 128:10
62:18 89:9,19 90:20	76:23 77:3,5,16	57 114:10	142:16 143:23
92:13 95:5 96:17	79:12,15,17,18,24	<b>571</b> 4:17	146:14
98:1,5 99:19 100:9	80:1 84:9 90:8,17	<b>577-1250</b> 3:18	<b>7-1</b> 110:2
111:14 112:15	115:5	<b>5:18</b> 146:3	<b>7-2</b> 110:2
114:12,16,19,20	40 51:5 55:12	<b>5:31</b> 146:6	711 20:13 27:20
115:1,3,8,9,14,17	401 130:4	<b>5:44</b> 2:17 151:12,15	<b>727</b> 26:7
117:14 124:7	<b>403</b> 93:16 95:7,25	6	<b>731</b> 20:1,9,15,17,23
125:16 133:18	125:20 126:10,25		21:22 22:15,24
151:13	129:12 130:5	6 23:10 31:8,20 42:1	23:12,20 24:18 25:7
<b>30</b> 61:20	413 8:20 39:9,12	42:12 43:25 45:13	25:16,24 27:14,18
<b>300</b> 96:23 97:1,6,9	94:17	46:10 48:15 51:4	30:17 33:14 35:5
97:14,15	<b>42</b> 52:8,18 84:10	52:7 55:11 56:15	36:25 37:3 62:20
<b>31</b> 43:25 61:20	<b>434-6789</b> 4:17	57:4,13 60:3,5,6,24	63:22
97:19 116:22,25	45 142:16	61:8,11,20,23 62:1	<b>732</b> 25:15,15,19,21
117:8 128:8,12	<b>46</b> 90:17 143:6	62:6 63:20,22 64:6	<b>734</b> 26:1,2
<b>3100</b> 3:16	48 29:10 41:23	64:13 66:14 67:6,8	<b>735</b> 25:16,25 31:14
<b>312</b> 3:18 4:10	71:13 72:7,17 111:9	67:10 72:13 77:21	<b>736</b> 25:16
<b>31b</b> 98:18	114:9	78:3,5,6,9,17,23	<b>737</b> 25:16
<b>32</b> 66:14	<b>4:11</b> 124:7	79:8,23,25 109:22	<b>738</b> 23:13 25:16
<b>34</b> 62:1 143:21	4a 93:6,9,22 95:6	114:24 115:12	31:14 63:22
<b>35</b> 56:15 117:1	125:3,9,20 126:4,5	142:18	<b>751</b> 20:11,11 23:13
<b>36</b> 25:25 42:12 52:7	126:19 127:12	60603 3:17	24:18,25 25:2,3,6
52:18	128:16,24 129:1,4,5	60606 4:9	25:16,22,24 26:9
<b>37</b> 25:25 31:21	129:10,14,16,21	61 67:22	27:16,17,23 28:5,11
56:15 62:6 64:14		<b>62</b> 117:2,7	28:15,21 30:17,21

30:21 35:8 36:25       114:19,20 115:2,9       adheres 144:24,25       116:16 125:3 12         37:1,5,11 62:20       115:16,20 116:1,8       145:10       126:3,19 129:2         63:2,7,13,23 68:11       116:13 128:13,20       adhering 104:2,10       130:1,13,16         68:14,21,23 69:9,22       92614 3:8 6:12       104:11       agreeable 8:12         70:2,19 72:12 73:1       949 3:9       adhesion 16:11,16       agreed 7:1 96:1         81:8 87:13,15 88:1       133:17,19,25       143:21,22 144:3,5       agreement 6:24         133:17,19,25       134:24       150:16 151:3       adhesive 150:20       aim 16:9	7,16
63:2,7,13,23 68:11 68:14,21,23 69:9,22 70:2,19 72:12 73:1 74:14 75:2 80:5,19 81:8 87:13,15 88:1 133:17,19,25 134:24  116:13 128:13,20 92614 3:8 6:12 949 3:9 9:14 2:17 6:2,5 142:25 143:11,15 143:21,22 144:3,5 149:7,16 150:3,14 150:16 151:3  130:1,13,16 agreeable 8:12 agreeing 53:22 agreement 6:24 ahead 9:22 57:2 68:5 128:6 143	
68:14,21,23 69:9,22 70:2,19 72:12 73:1 74:14 75:2 80:5,19 81:8 87:13,15 88:1 133:17,19,25 134:24  92614 3:8 6:12 949 3:9 9:14 2:17 6:2,5 a  104:11  adhesion 16:11,16  142:25 143:11,15  143:21,22 144:3,5  149:7,16 150:3,14  ahead 9:22 57:2  68:5 128:6 143	8
70:2,19 72:12 73:1 74:14 75:2 80:5,19 81:8 87:13,15 88:1 133:17,19,25 134:24  949 3:9 9:14 2:17 6:2,5  a dhesion 16:11,16 142:25 143:11,15 143:21,22 144:3,5 149:7,16 150:3,14 150:16 151:3  agreed 7:1 96:1 agreeing 53:22 agreement 6:24 ahead 9:22 57:2 68:5 128:6 143	8
74:14 75:2 80:5,19 81:8 87:13,15 88:1 133:17,19,25 134:24  9:14 2:17 6:2,5 a 142:25 143:11,15 143:21,22 144:3,5 149:7,16 150:3,14 150:16 151:3 agreeing 53:22 agreement 6:24 ahead 9:22 57:2 68:5 128:6 143	8
81:8 87:13,15 88:1  133:17,19,25  134:24  a.m. 2:17 6:2,5 41:12.21  143:21,22 144:3,5 149:7,16 150:3,14 150:16 151:3  agreement 6:24 ahead 9:22 57:2 68:5 128:6 143	
133:17,19,25 134:24   a.m. 2:17 6:2,5   149:7,16 150:3,14   ahead 9:22 57:2 41:12.21   150:16 151:3   68:5 128:6 143	
134:24   a.m. 2:17 6:2,5   150:16 151:3   68:5 128:6 143	
134:24   41:12.21   130:16 131:3   68:3 128:6 143	
764 04 14 06 05 11.12,21 1 albanina 150.00 1 oim 16.0	5
ability 86.9 15	
27:14 28:11,16   122:15 20 123:9   adjustment 94:24   air 110:3	
<b>762</b>   26:25 28:11	
763 26:25   able 28:22 30:6 9 11   131:12,19   allegations 142	
764 26:25 28:16 35:17 18 19 20 21 administer 6:20 allege 141:18 19	12:14
8 36:17.41:7.50:24 7:15 143:15	
8 86:22 87:70 18 22 65:19 administered 7:18 alleges 130:22	
87.22.23.88.7.23   ac. 136.20   admitted 100:13   alternative 51:2	
80.5 10 00.1 2 23 accepted 12.22 advantage 56.5 alternatively 69	:24
91:14 24 25 92:9 11   access 15:20 140:17   93:10,20 96:12   122:22	
02:12:05:5:08:3   accommodating   103:1:110:25   aluminum 82:1	2
100.5 104.17 105.6 86.16 advantageous 68:9 91:7 150:12,21	
105:10 11 21 24   accurate 153:11   109:12   amend 142:5	
106:18 20 22 24   achieving 14:2   advantages 65:5   analysis //:21 !	
107:0 25 108:2   acknowledges 148:0   68:12 /3:9,10,25   100:10,10 123:	14
107.5,25 108.2 <b>acknowledges</b> 148.5 74:9,22 94:18,19 131:8 149:18 109:13,16,24 110:5 148:11	
110.15.24.111.1.11   action 6.21.153.13   95:16.109:2.111:22   analyzing 124:	
111:17 112:3 4 18   active 102:7   112:4,12 113:2,15   anchored 145:3	
112:18 113:16 add 42:5 71:5 98:9 115:16 133:9 135:2 anisotropic 17:	
115:16:20 116:1 9   125:13 128:15   advise 48:25   annealing 150:	
125:16 128:5 14 148:21 25 affect 109:5 112:6 answer 39:5,24	
8 068 204 1:6 2:6 added 75:2 150:9 40:17 51:1 58:.	
6:15 11:7 adding 67:1.70:2.20 affirmative 105:23 /1:9/5:14 //:	
27.10 27.10 22 24 70.22 73.9 134.6 13 aforementioned 79:2 82:14 98:	
additional 56:5 62:3 101:18,19 124:	11
64:25 65:2 66:3 agent 142:23,25 134:20 142:5	
9 87:18,19,22,23,23 67:1 70:1 5 20 71:5 143:18 144:6,10,18 149:10 150:4	
88:23 89:4,5,6,19 82:11 134:6 14 144:19 answered 103:	
90:2 91:14,24 92:9 ago 21:23 28:25 answers 40:16	51:14
92:11 104:6,10,11   address 130:8   32:8 67:5 72:11   70:17 139:1	
104:18 105:11,19   131.14.17   128:3 146:9   anybody 80:15	
106:9,22,23 107:8,9   addressed 130:17   agree 6:17 /:2 53:20   apart 145:1	
108:2,2 109:16 addresses 134:11 54:9 76:2 79:24 aperture 111:1	2
110:24 111:12   adhere 145:15   84:6 96:18,25   apparatus 138:	2
112:4,18,18 113:4   150:13   100:20 105:5   appeal 1:2 2:2	
113:16,21 114:13 107:13,19 115:19	

[appear biter]			1.181
appear 28:10 152:7	69:8 70:21 73:14	attorney 3:6,15 76:7	behalf 2:15 7:12
appearance 4:18	74:20,25 84:20	153:14	10:17
appearances 3:1 4:1	85:11,22 87:11	attorneys 4:7	believe 8:14 33:7
appears 117:1	88:12 89:3 92:9	audible 131:23	36:15 66:15 69:16
119:24	93:20 94:4,14 96:3	audio 6:16	78:15 80:2,22 83:14
applicable 94:6	101:11 106:15	author 34:4	102:6 103:7 116:19
applied 11:19 29:15	107:6 109:16	authorized 6:20	benefit 114:16
97:15	110:24 112:3,17,23	auxiliary 31:5 32:6	143:10
applies 72:11,23	113:20,25 123:16	32:10,12,16,17	<b>benefits</b> 15:24 91:16
73:18 78:15	127:5,7 131:8,16	36:20,22 71:15,24	112:19 116:9
apply 73:15 78:4	133:4,6 134:3,13,21	130:3,20 131:1	best 65:6
79:24 102:9 114:18	135:13 145:19	avoid 34:10 101:12	better 65:2 68:14
appreciate 54:5	artist 30:7 33:19	104:14 105:8	104:11,11 105:13
139:2	37:23 38:4 113:9	113:19	113:7 115:10 116:3
appreciated 92:18	arts 35:22	avoided 105:14	116:4 128:16
approach 14:23	artwork 34:21	aware 40:6 59:10	143:11 151:4,6
15:13 148:5	asked 8:7 9:13,18,19	82:15 101:22	<b>beyond</b> 18:8 79:9
approaches 14:21	10:4,6 36:21 40:16	105:18,20,22	84:21 107:24
88:20	40:17 70:7,9 71:8	b	big 22:23 23:19
appropriate 8:21	72:10 77:17 93:5	back 22:14 26:9	34:22 41:8
approximately	117:19 134:16	29:17 41:21 80:12	<b>bigger</b> 134:14 135:3
66:14	139:12 141:24	92:25 108:15,21	145:5
arbitrary 33:24,25	asking 9:11 40:9	124:7 139:3 142:8	<b>bit</b> 16:4 47:17 58:8
area 13:23 14:6,6,7	41:8 51:12,15 52:15	146:5	63:21 72:13 77:22
14:10,11,17 15:4,6	82:4 98:4 99:9,11	background 8:8,10	92:15 98:6 147:3
15:17 16:14 17:12	132:20 139:11	8:13,17 9:12 10:2	blackwell 4:4 7:7
17:22,23,24,25	aspect 52:20	34:17 36:5,16	<b>blowup</b> 19:16
18:10 19:20 20:1	assembles 14:5	backgrounds 36:3	<b>board</b> 1:2 2:2 12:17
32:15 46:3,4 78:19	assume 78:4 86:8	band 18:3,3,5	12:20,23 15:23 88:6
78:23,25 79:6,9,9	139:23	bardsley 1:21 2:18	99:18 100:14,22
84:3,5 130:8	assuming 98:14	6:8 153:20	126:17 127:9
areas 9:19 15:21,22	99:16 140:13 149:4	base 71:10	136:18
145:21	assumptions 98:16	based 12:11 43:24	<b>bond</b> 144:8
arranged 67:13	atmosphere 110:1	66:20 69:1 79:7	<b>bonded</b> 20:18,20
array 17:18 18:13	111:18	83:3 124:18 129:4,5	bonding 15:17
79:1,9 103:3	atmospheric 111:20	134:21 147:18	<b>bottom</b> 19:15 21:24
arrive 75:23	attach 141:20	148:5	23:11 26:16 30:10
art 11:19,25 12:8	attached 152:8	basis 12:23	48:22 71:15,24
15:4,15 34:16,19,22	attaches 17:4	<b>battery</b> 136:21	box 20:2
35:12,23 36:17	attaching 56:1	bears 8:2	bracket 29:22 60:8
37:11 39:10,16	137:18	beginning 2:17	branch 31:14
40:14 45:15 47:14	attacking 131:12	56:15 80:11 91:25	break 77:4 92:20
47:19,23 48:1 51:21	attempted 15:9	121:23 124:6	brief 81:25 95:4,14
55:18,20 62:10	attention 16:21 93:3		95:15
66:23 67:23 69:1,7	97:18		

briefly 14:2	carrying 56:10	chosen 48:10	77:22 78:5 80:25
bring 97:13 127:24	case 6:13,14 10:5,15	chrome 90:16 91:5	107:12 120:3
broad 8:25 27:10	10:16,22,22 11:12	chromium 82:12,23	clearly 58:24 78:3
90:4 95:23 96:2,3	23:10 31:24 83:24	90:9,17,22 150:13	79:25
96:10,12 101:17	102:4 115:11	150:15,21 151:3	clients 10:19,20,23
113:14 123:18	127:18,25 135:23	circuit 15:23 28:14	closed 118:20
124:24 125:1,17	139:15 140:11	36:1,14 88:6 95:8	119:12 126:5
127:19	147:10,11	99:18 100:14,22	coat 102:1
broken 29:25 30:2,7	cases 10:13,17	104:21 119:4,7	coated 138:20
build 45:16 97:14	119:17	121:25 123:20	coating 150:10
150:2	category 101:17	125:22 126:11	coefficient 149:16
building 98:8	cause 110:12	127:2,9 128:12,18	149:25 150:23,24
built 149:23	caused 130:2	135:24 136:2,18	colleague 59:13
butler 3:4	causing 38:24 39:19	137:22 138:1	colleagues 59:7
bypass 56:10	145:1	140:25 141:2	76:11,18
c	cellular 6:19	circuits 139:4	color 18:20 144:20
	certain 14:8 34:17	circumscribed	column 20:8 23:10
c 37:16	34:18 84:18 102:10	17:25	31:8,20 37:18 42:1
cable 137:2,17,18,19	106:15 119:21	citation 59:9 108:25	42:12,19,22 43:15
137:24 139:14,21	130:9	111:7 114:6,24	43:25 44:18 45:13
139:22,24,25 140:1	certainly 8:16 83:23	citations 109:20	46:10 48:15 51:4
140:3,4,12,21 cables 140:5,18,24	113:5	cite 11:22	52:7 55:11 56:14
california 1:15 2:16	<b>certified</b> 2:19 153:1	cited 12:22 52:6	57:4,13 61:20 62:1
3:8 6:1,12 153:2	certify 153:3,12	56:16 114:9	62:6 63:20,21 64:6
call 21:17 118:5	challenged 116:22	citing 48:14 116:6	64:13 66:14 67:11
called 17:18 19:18	change 71:1,3,6	125:25	72:13 84:9 90:8,17
22:4 39:24 150:24	73:21 107:20 124:1	city 152:13	90:23 91:25 95:5
calling 17:16	127:14 128:4,5	<b>cj</b> 37:16,20 42:24	105:24 107:25
calls 117:16 118:18	changed 8:14	claim 94:17,21	109:22 111:9 114:6
120:7	changing 68:17	95:16 116:21,22,25	114:24 115:5,12
capability 108:12	91:19	117:6,12 118:4,10	117:1,7,8,14 118:10
capacitance 38:12	characterization	118:16 119:3 122:9	124:12 125:16
38:19,19	16:24	122:10 124:10	128:5 142:16
capacitor 37:13,19	characterized 57:11	125:12 132:10,12	143:23 146:14
37:20,21 38:3,5,7	characterizes 57:11	132:15,23 134:19	columns 94:24
38:11 42:24 58:5	<b>chemical</b> 36:8 54:21	135:6,7	combination 85:23
capacity 43:10	54:21,22	claims 12:7,11 31:6	combine 35:21
143:1,22 144:3,5	chemistry 36:5	90:3 108:7 129:22	combining 15:1
caption 6:12	chicago 3:17 4:9	132:6	85:20 90:1
captured 124:17	choice 106:6	clarification 145:25	come 11:22 40:19
captures 128:15	<b>choices</b> 48:6,7	clarify 9:25 10:15	46:7 49:7 108:15
carrier 96:23,25	choose 88:13,18	29:16 100:20 101:7	144:12
97:6,9,19	94:11 96:7,9	121:14	comes 17:4 48:19
	chooses 94:5	clear 34:19,20 58:14	97:9,11,15 102:24
		66:1 70:23 71:9	119:19,20,24

. 262	05 15 10 01 06 00	00 1 00 10 05 05	47.00.40.4.50.0
coming 36:3	85:15,19,21 86:22	90:1 92:13 95:25	47:22 49:4 50:3
comment 9:12 10:9	87:12,14,18 88:11	connection 20:24	51:24,24 55:23
10:18 14:25 59:14	89:5 90:10,18	30:16 31:3 32:7,12	69:18 89:12 95:9
75:25	104:17 105:6	32:18 35:5 36:19,22	104:20 122:1,4,5,12
comments 76:13	109:13,24 110:4,19	36:23,24 45:19 46:9	122:17,21,24 123:9
common 20:11,11	111:4,11,17,23	47:9,18 50:8,25	123:11 124:14
22:16,22 23:13 24:1	112:1,20 114:11,21	51:3,6,17,23 55:5,6	125:5,22 126:12
26:24 27:4,16,23	115:13 117:16,22	55:9,22 68:2 72:3	127:2 128:12,18
63:23 74:14,19 81:8	117:23,25 118:3,5,6	88:6,8 92:11 95:4,7	129:12 135:8,9,14
87:13,15 88:1 133:7	118:11,14,18,19,22	95:13 96:14 125:20	135:15,16,19,20,21
133:16 134:24	118:23,23,25 119:1	126:10,25 127:7,8	135:25 136:10,14
compare 99:19	119:5,6,12,14 120:7	128:8,10 129:12	136:23 137:14
compared 10:9	120:9,11,15,17,18	130:4,19 131:1	139:6
100:4	120:20,22,23 121:2	136:6	contacts 145:20
compares 99:15	121:4,6,7,10,11,24	connections 107:11	contain 35:22 54:19
comparing 98:13	122:2,11,12,15,16	130:11	115:9 127:18
compatibility	122:20,21,23,24	connects 62:18	contained 21:21
101:15,24	123:19,23,24	conrad 4:22 41:16	152:9
complete 51:6	124:13,14,21,23	41:20	contains 17:12
completely 49:5,10	125:4,5 127:24	consequences 75:19	20:16 79:5 109:25
50:15	129:12 130:4,5	consider 74:25	111:17
complexity 66:25	131:22,25 132:1,4	106:15 128:2 133:3	<b>content</b> 12:1 19:19
component 82:21	132:13,22 133:8,11	considered 30:3	context 39:6,9,25
composite 91:6	133:15 134:18,23	consisting 72:22	40:18 51:14 68:6
104:1 115:4,6	135:24 136:1,3,5,9	constant 130:14,17	72:20 73:3 94:17
compromise 143:17	149:1	130:24	124:25 138:15,21
150:3,16	conductor 61:16	constantly 31:25	continually 37:1
compromised	conductors 48:16	45:15 50:6,11,20	continuation 41:18
145:24 148:4	configuration 79:13	constitute 61:9	continue 24:14
compromising	confirm 22:15 29:11	constituted 37:19	56:10 120:5 141:3
145:9	confused 46:19	142:17	continued 4:1
concept 22:11	connect 51:22 89:9	constitutes 82:2	continues 22:25
concerns 113:3	89:24 95:1	constituting 31:24	25:18 26:1 30:19,21
conclude 42:11	connected 24:3,21	constructing 125:11	110:3
70:21 146:8	26:1 27:8,16 28:1	construction 73:6,8	continuing 15:12
concludes 151:12	31:13,25 36:25 42:7	123:2,6 130:10	24:20 41:13 62:6
conditions 150:6,7	44:1 45:14,16 50:1	construe 122:25	100:2
conducting 17:6	50:6,11,12,20,21	127:11	continuous 24:13,19
67:25 89:16,17,24	55:8,12 57:16 66:6	<b>construed</b> 123:7,10	30:13,25 31:3 33:22
110:11	71:19 95:8 125:21	125:2 127:17	57:9 95:1 138:16
conductive 20:15,16	126:10 127:1 128:9	consult 77:7	continuously 37:8
20:21,23 22:5 51:6	128:11 139:16	consume 137:8	contrast 142:13
52:17 71:15,18 72:2	140:21 142:20	consumed 138:5	control 44:19,24
74:2,3,4,5 80:18	connecting 23:14	contact 16:13 17:5	controlled 27:18
81:2 84:7 85:3,7,7	57:3,5 63:24 89:11	20:17 44:3 46:17	138:12
		1	150.12

	130:25 145:16	current 136:8,15	145:2
conversations 6:19		137:9,10,12 138:6,9	defects 16:1 65:10
convert 149:20	146:10 147:14	137:9,10,12 138:0,9	73:11
conveyed 84:19	course 12:4,20		defendants 10:21
copper 98:18 140:11	17:14	148:18	1
copy 7:25 11:4,10	courses 36:9	currents 138:19	define 46:21 47:3
87:2 116:19	court 6:8 7:15	d	defined 132:6,23
<b>cord</b> 137:5,6 139:5	cover 8:17 13:11	d 4:5	134:19
corner 18:4 19:15	24:9 55:2 87:1 90:4	damage 38:24 39:19	definitely 58:17
19:22 20:2 26:5	90:19 96:2,10 98:10	dangerous 145:16	degree 142:24
corporation 1:4 2:4	coverage 104:15,16	dash 22:16	degrees 35:25
6:13	<b>covered</b> 13:15 23:23	data 18:11,12,13,22	denise 1:21 2:18 6:8
correct 12:13 13:10	30:4 85:25 93:7	18:24 19:4 23:2,6	153:20
13:20,21 16:12,20	102:5 147:10,11	23:17 26:24 27:14	departing 91:3
21:8,14,15,19 22:6	covering 86:1	28:2,5 31:23 32:4	106:4
22:9 25:9 26:19,23	101:12,23	42:15 44:6,8,13	<b>depend</b> 94:5 149:11
28:17 29:23 33:12	covers 20:24 43:23	53:5 60:14,18,21	depending 18:17
46:22,24 48:23	45:9 95:24 96:4	61:4,22,25 62:9,13	62:2 70:25 71:2
49:21 63:4 71:22,23	113:14	63:14 64:2,4,9	104:10 130:10
72:16 78:8 79:11	<b>cpt</b> 43:23	68:16,25 69:10,14	142:25
80:23 96:23,24	create 35:22 45:12	69:19 70:11,14	depends 38:12
114:6 116:24 118:8	45:18 46:9 49:23	71:18 72:4,15,23	122:25
119:8,16 129:18	50:5,8 55:5 93:17	74:5,16,21 81:10,11	<b>depict</b> 33:19 37:24
152:9 153:8	96:7 100:17 101:24	81:18,23 82:1,20	depicted 17:21
corrected 152:9	102:9 103:23	83:1,6,13,16	47:23 67:10 113:11
corrections 152:7	106:23 109:18	date 153:15	129:1
correspondingly	134:14 135:2 145:5	dated 153:17	<b>depicts</b> 61:24 81:8
118:9	145:13,20 148:10	day 113:7 152:12	deposit 89:4
corroded 110:16	created 42:9 70:14	days 13:7,8,9,11,12	deposited 43:8,22
corroding 114:22	creates 56:7,8	13:13,13,16 102:25	46:16 90:10,18 98:3
corrosion 74:23	100:14	108:4	149:15
104:15 105:8	creating 16:12	deal 40:12,19	<b>deposition</b> 1:14 2:14
106:20 110:3,8,13	44:16 102:20	decide 126:17	6:6,11 12:14 39:14
111:20,24 113:3	115:23	decision 12:17,19	41:14,19 45:2,3,7,8
133:10,11,16,24	<b>critical</b> 37:24,25	declaration 11:11	45:11 81:15 86:5
cost 64:20,22	38:1 65:14	11:14,17,20 12:15	101:4,5 149:24
counsel 6:10 7:4	cross 21:6,7 33:4,21	12:22 13:4 29:9	150:6
8:12 11:24 13:6,7	34:2,5,10 35:2,7	41:24 67:19 69:6	depositions 13:9
101:2 114:5 132:9	61:3 84:3,4	71:13 98:13 116:23	derive 76:3,20
count 60:23,24	crossing 56:21	133:6	121:18 123:4
<b>counter</b> 14:5 17:13	crowded 77:16	declare 16:17 152:5	<b>describe</b> 35:15,24
20:8,17,25 21:1	crystal 14:12 21:21	decrease 38:19,20	42:8 123:18 126:14
22:8,9,12,13 65:14	79:5 97:14 101:9	decreases 65:11,11	127:7 128:7
65:18,20 74:12	102:7,10,11	defect 32:1 56:6,11	described 28:23
83:10,21 84:9 85:5	csr 1:22 153:20	66:6 109:25 111:18	58:5 63:3 72:7
85:9 102:15 130:18		00.0 109.23 111.18	84:16 90:22 92:4
	1	L	

[described - effect]

94:2 98:1 100:12	differences 34:21	disclosed 62:12	doing 34:9 46:8
116:9 123:17	different 9:3 23:21	72:24 74:15 112:23	55:24 89:21 93:20
127:14 148:17	24:11,20 34:6 35:9	132:2	109:3 123:15
describes 126:25	35:21,23 36:3,4,16	discloses 105:5	dotted 57:8
describing 94:19	37:1 41:5 42:16	disclosure 90:15	double 45:17 65:9
description 84:17	43:9,20 45:6 56:9	105:20 132:3	66:24 68:22 72:19
84:25 90:7 95:22	56:19 59:18 62:11	discontinuity 24:24	72:22 85:14 100:13
design 36:1	63:7 65:22 68:11	56:8	104:15,16
designer 36:1	73:21 81:6,12,14,15	discuss 40:14 47:18	downward 26:18
designers 36:14	81:23 82:7,17 83:3	76:19 80:14 94:24	1
desire 13:22	·	112:12	dr 6:7 41:14 151:13
	90:1 93:23 94:11,13		draftsmen 34:8
detail 16:6 82:2	107:2,22 109:10	discussed 30:17	draw 34:4
detailed 90:7	116:12,13 120:1	36:12 37:6,23 57:7	drawing 50:17,18
details 17:15 84:21	124:19,19 126:8	59:16 69:19 72:18	50:19 57:17 77:6
94:5 95:17 98:4	127:22 140:9 149:7	81:7,16 87:16 107:8	98:23 99:1 113:10
determine 131:5	150:6,7	109:9 113:12	115:22
determined 18:16	differently 30:23	114:15 115:23,24	drawings 35:13
28:14	53:8 63:8	120:16,25 121:1	56:25 66:21
developing 54:15	dig 8:6	129:9,10 148:5	drawn 19:17 31:9
device 101:10	dimensions 13:24	discussing 47:15	57:5 129:15
devices 23:11	14:24 98:15 99:16	51:9 105:18 110:14	drive 4:15
diagonal 83:14	dioxide 151:3	138:15 141:8,14	driving 65:22
dielectric 38:13,16	direct 46:17 50:3	<b>discussion</b> 52:7 95:2	dry 54:21
42:20 43:1,2,4,16	77:10 122:12,17,21	100:16 136:13	<b>dumb</b> 75:20 76:1,2
43:18,21,22 44:2,12	122:24 123:9	141:15	duplicate 107:9
44:13,16,17 45:18	124:14 125:5 135:8	discussions 100:19	e
45:21 46:3,12 48:3	135:14,14,18,21	disk 124:2	e 96:17
48:7 50:9,10,14,24	136:10	<b>display</b> 13:23 14:7	earlier 27:17 37:6
51:2,16 53:9,10,12	<b>direction</b> 14:8 70:10	15:5,10,11,14 17:12	37:23 42:2 43:11
53:21 55:2,21,25	153:10	17:20,22,24 18:4,5	57:7 58:6 63:19
58:4,11,13,22 61:12	directly 86:22 87:7	18:10,18,25 19:1,20	70:7 72:18 76:15
69:13,15 71:16	87:15 89:4 105:7,11	21:4,18 24:15 26:8	79:21 81:16 94:12
76:24 79:8,14 81:16	105:19 106:22	35:1,4 68:9 78:5,19	102:6 107:8 123:3
103:24,25 141:11	109:2,4,14 110:5,20	78:19,23,25 79:6,9	125:15 146:25
141:14,16,21 142:4	111:5,13,23 136:2	83:13,14 101:10	edge 80:3 96:21,23
142:19 143:9,13	142:20	displays 65:21 102:8	104:23
145:4,5 147:23,25	disadvantage 75:13	102:8	edges 123:12 145:11
148:2,8 149:4,14	96:12 103:1	<b>distance</b> 14:9 31:19	educated 34:17
dielectrics 44:20	disadvantages	50:18 148:11	education 35:24
46:24	74:25 75:6,12 76:16	distances 65:15	36:10
difference 130:2	76:17	distribute 65:17	edward 4:5 7:6
136:17 140:2,3,14	disagree 8:23	148:13	edward.manzo 4:11
140:15,17,19,24	126:22	distributed 74:13	effect 100:18 106:24
141:2	<b>disclose</b> 69:2 96:5	documents 34:14	106:25
		35:18	100.23
1	1	1	

effective 14:16	120:15,25 121:1,9	essential 33:19	excitation 136:16
effects 140:7	120:15,25 121:1,9	65:20	excuse 65:11 85:24
either 19:5 48:8	135:17	establish 55:22,24	executed 152:12
		89:13	exhibit 8:1 11:5,11
102:19 103:21,22	eliminating 16:1	· ·	11:13 12:3 41:24
elaborate 75:14	embodiment 23:6	established 58:16	
electric 102:12,17	23:12 61:23,24 62:5	118:24 119:15	47:4 87:3
102:19 103:2,4,5,20	63:15 72:25 81:7	121:21,22 136:8,14	exhibits 5:10 8:22
electrical 22:7 36:7	84:12,13,14,16	137:19 140:23	exist 120:8 141:2
122:1 135:7,16,20	85:16,17 90:8,21,25	establishing 51:24	142:14 143:15
135:25 136:6,14,23	94:3 96:7 97:23,24	etch 39:18 40:24,25	existed 123:11
137:14 139:6	97:25 98:5,8 107:22	46:24 47:1,3,5 49:4	existence 118:6,12
electrically 22:15,22	110:4 111:3,10,16	49:18,19	118:13 125:6
24:1 26:24 27:3,8	112:9 113:20	etched 45:22 89:6	existing 85:15,20
95:7 125:21 126:10	114:14 115:15,19	etching 24:10 38:23	exists 79:14 138:10
127:1 128:9,11	115:23,24 116:7	40:21 41:1 45:10	expansion 149:24
139:15 140:21	125:10,17 129:15	46:11 48:5 49:1	experience 9:17
electricity 137:23	129:20	52:10,11 53:17,23	10:5,24 34:8,18
138:4 139:14,16,18	embodiments 61:19	54:24 89:21 98:2	35:17,25
140:23 141:1	62:11 84:18,23,25	evidence 60:1 66:17	experienced 40:21
<b>electrode</b> 21:1 37:16	88:14 90:4 91:1	67:2 70:8 71:7	<b>expert</b> 10:10 75:16
37:17,19 58:1,23	92:3,4,7 94:10 96:2	75:13 76:13 77:9	75:17 96:11 123:15
65:14,18,20 67:14	96:4,6 106:2,3	105:12 108:6	123:15
70:8,13,23,24,25	107:4,5,15,21	141:25 142:1,3,7,8	expertise 77:14
71:2,10 74:12,22	109:10 111:21	142:10	experts 10:9,22,25
83:10,11,21,21 84:9	112:11 130:9	exact 13:1 17:15	76:4,9,11,19 86:9
85:4,5,5,8,18 102:3	emerges 73:5	73:20 103:9 151:2,5	<b>explain</b> 14:1 42:10
102:5,9,13,15,21,22	employee 153:14	exactly 113:10	94:13 144:6
103:3,6,18,24	enable 45:13,19	126:1,14 127:6,13	explained 90:24
electrodes 38:14,17	enables 14:19	131:9	95:17 106:1 138:17
70:4 79:2 84:8 85:9	ended 27:10 41:8	examination 5:2	146:25 147:21,24
85:10	energy 1:7 2:7 6:14	7:21 8:6,11,16	explaining 146:13
electronics 137:11	engineer 36:1	examined 7:18	explanation 52:3,4
element 17:23 27:20	engineering 36:7,8,8	<b>example</b> 19:13 25:2	52:21
32:17,18 35:5 38:3	engineers 36:13	28:9 33:14 38:9	explicit 119:23
57:10 58:5,22 68:21	entire 20:25 23:23	63:12 65:8 82:8	138:25
80:5 100:2 118:4,10	24:9 43:23 45:9	85:13 86:21 91:17	explicitly 81:19
119:3 121:12 122:9	46:12 50:4,9,21,24	95:5 106:13,18	explore 75:18
122:10 135:19	51:2 90:11 95:15	113:18 116:11	<b>expose</b> 46:6 110:21
elements 17:16	145:23	117:14 120:10,13	<b>exposed</b> 46:4,15
24:11 25:14 30:22	epoxy 20:16,22 22:5	120:14,16 121:2	54:20,23 104:19
30:23 33:19,25 34:6	equally 79:24 88:14	125:16 149:21	109:13 110:2,20
35:9,22 37:24,25	88:19,19 105:3	150:11,12 151:2	111:11,19 133:14
38:1,2 43:20 62:25	140:5	examples 65:22	133:15,24,25
64:10 73:1 119:18	essence 16:1 110:18	84:18 112:14	145:11
119:23 120:1,3,13		116:11	
<u></u>	I	I	

exposure 34:14 extend 94:25 97:1 132:5,23 134:19,25 extending 68:10 79:8 extending 68:10 79:8 extendis 18:8 77:5,18 80:2 117:7 80:2 117:1 80:2 117:7 80:2 117:1 80:2 117:7 80:2 117:1 80:2 12:3 12:4 100:2 1 104:2 118:5,7,1,0 12:13	54.14	m 160100170	110 1 11 11 11 11	1060 100 0 100 10
extend 94:25 97:1 132:5,23 134:19,25 extending 68:10 79:8 extending 68:10 79:8 80:2 117:7 80:2 117:7 30:15 31-4,111 32:13 30:15 31-4,111 32:13 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,44,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 31:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 37:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 30:16 37:3 32:6,12 32:18 36:19,22,23 33:10,14,16 34:7,24 36:19 37:9,10,13,22 138:13,18 36:24 72:3 88:6,7 95:7 1102:111:9 135:13 136:18 137:102:111:19 131:1 136:18 131:3 133:17 fair 4:15 19:14 28:15 falds 91:14 familiar 11:8 27:4 far 60:4 65:15 71:21 74:13 77:4,18 95:3 98:25 99:19,20 101:24 106:13 113:3 33:17 fast 65:20 favor 105:14 field 17:7 36:2,7,15 follow: 105:12 filed 11:11 11:5 filed 15:2,17,19 103:2 103:5,5,20 fields 36:4  19:15,7,10122:13 118:5,7,2,13 12:14 119:15,7,20,10 118:5,7,20,10 118:5,7,2,13 12:14 119:15,7,20,10 118:5,7,2,13 12:14 121:5,7,20,10 121:13 127:10 128:16,314 121:5,7,20,10 138:13,13,45 122:15 123:20,122 138:13,14 129:11 134:5 123:20 125:21 136:11,127:19 136:11 127:1,9 138:13,18 14:11 138:13,13 108:10 118:5,7,2,13 12:14 119:4,4,20,11 138:7,18,25 118:5,7,2,13 12:14 119:4,4,20,11 118:5,7,2,13 12:14 119:4,4,20,11 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13 118:5,7,10122:13	exposure 54:14	figure 16:21,22 17:2	110:4,11,11 111:11	126:3 130:3 132:12
132:5,23 134:19,25   21:3,4,23 2:14   115:3,4,5,5,6,7,8   149:1   five 28:16   79:8   25:13,14,20 26:3,15   121:5,7,10 122:13   122:15 123:21,22   121:5,7,10 122:13   122:15 123:21,22   121:5,7,10 122:13   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   122:15 123:21,22   123:15 123:21,23   122:15 123:21,22   123:15 123:21,23   122:15 123:21,22   123:20 126:22   26:18,43,45   36:19 42:23 36:24,25 58:7   138:17,18   126:11 132:4   129:11 134:5   123:20 125:21 125:20 126:9,25   16:23 62:18 63:16   67:68,10 76:23   138:13 136:18   125:12 125:20 126:9,25   77:3,5,16,21 78:3,5   138:13 136:18   79:24,25 80:18 6:20   137:18   79:24,25 80:18 6:20   137:18   79:24,25 80:18 6:20   107:24 108:5   107:24 108:5   107:24 108:5   107:24 108:5   122:13 138:13   123:13	, -	1		•
extending         68:10         25:13,14,20 26:3,15         118:5,7,12,13 121:4         five 28:16           79:8         26:25 27:21,22         118:5,7,12,13 121:4         five 28:16           extends         18:8 77:5,18         28:22 29:4,4,17         30:15 31:4,11 32:13         122:15 123:21,22         fexible 17:3 88:5           extent         22:23         33:10,14,16 34:7,24         124:22 125:7,23,24         95:8 99:18 100:14           external         17:3 20:12         27:18,20 28:3,3,4         33:10,14,16 34:7,24         127:22 128:13,14         100:21 104:20           27:18,20 28:3,3,4         42:23 56:24,25 58:7         30:16 31:3 32:6,12         58:18,22 60:3,5,6         60:24,24 61:8,8,11         filters         142:20         123:20 126:9,25         123:20 126:9,25         77:3,5,16,21 78:3,5         filters         144:20         fi	Į.	1		
79:8         extends         18:8 77:5,18         28:22 29:4,14,17         121:5,7,10 122:13         flew 140:23,23         flex 140:20         flex 140:21         flex 140:21         p5:8 180:14         flow 10:14         flow 10:14         flow 10:14         flow 10:14         flow 10:14         flow 10:14         flow 11:14         flow 11:14         flow 12:23         p5:8 9:18 100:14         flow 11:14         flow 12:14         flow 12:15         flow 12:16         flo	1			
extends         18:8 77:5,18         28:22 29:4,14,17         30:15 31:4,11 32:13         122:15 123:21,22         flexible         17:3 88:5           extensively         87:16         30:15 31:4,11 32:13         124:22 125:7,23,24         95:8 99:18 100:14           externt         17:3 20:12         33:10,14,16 34:7,24         127:23 128:13,14         100:21 104:20           27:18,20 28:3,3,4         30:16 31:3 32:6,12         35:19,379,10,13,22         129:11 134:5         123:20 125:21           30:16 31:3 32:6,12         58:18,22 60:3,5,6         films         87:21         126:11 127:1,9           30:24 72:3 88:6,7         61:23 62:18 63:16         films         87:21         136:1,17 135:24           95:7 110: 2111:19         67:6,8,10 76:23         77:3,5,16,21 78:3,5         finally 16:17         financially 6:22         102:21           125:11 30:4,11,19         79:4,12,5,17,18,23         76:6,9,12,77,23         77:3,5,16,21 78:3,5         76:1 77:23 82:18,19         137:10,11 138:6,9           137:18         99:25 10:4,24,5 80:1 86:20         84:24 85:10 105:12         16ws 13:13 13:1         148:19           4         99:25 10:4,94         99:15,15,19,20,20         99:25 10:4,94         13:11:1 148:24         16ws 13:4 149:11         13:9:16,18           fairfax         4:16         4			' ' '	
80:2 117:7         30:15 31:4,11 32:13         124:22 125:7,23,24         95:8 99:18 100:14           extent 22:23         32:22,22 33:1,3,4,5         126:12,13 127:3,4         100:21 104:20           external 17:3 20:12         33:10,14,16 34:7,24         126:12,13 127:3,4         100:21 104:20           external 17:3 20:12         33:10,14,16 34:7,24         126:12,13 127:3,4         100:21 104:20           27:18,20 28:3,3,4         42:23 56:24,25 58:7         36:19 37:9,10,13,22         129:11 134:5         123:20 125:21           30:16 31:3 32:6,12         58:18,22 60:3,5,6         61ms 87:21         128:11,17 135:24         136:1,18           36:24 72:3 88:6,7         61:23 62:18 63:16         61:23 62:18 63:16         films 87:21         128:11,17 135:24         136:1,18           95:7 110:2 111:19         67:68,10 76:23         77:3,5,16,21 78:3,5         78:69,12,17,23         153:13         173:01         16:17         16ims 87:21         16ims 87:21         16ims ex:2         17:3,18         16ims ex:2         16ims ex:2         16ims ex:2         17:3,18         16ims ex:2         100:21         100:21         100:21         100:21         100:21		i -	, ,	
extensively         87:16         32:22,22 33:1,3,4,5         126:12,13 127:3,4         100:21 104:20           external         17:3 20:12         33:10,14,16 34:7,24         127:23 128:13,14         119:4,7 121:25           external         17:3 20:12         36:19 37:9,10,13,22         129:11 134:5         123:20 125:21         123:20 125:21           27:18,20 28:3,3,4         42:23 56:24,25 58:7         138:17,18         126:11 127:1,9           30:16 31:3 32:6,12         58:18,22 60:3,5,6         films         87:21         128:11,17 135:24           36:24 72:3 88:6,7         69:7 110:2 111:19         67:68,10 76:23         finally 16:17         filipping         88:22 89:20           125:20 126:9,25         77:35,16,21 78:3,5         finally 16:17         filipping         88:22 89:20           127:8 128:8,10         78:6,9,12,17,23         78:6,9,12,17,23         76:17:23 82:18,19         137:10,11 138:6,9           137:18         86:25 87:8 93:9,22         79:32,25 80:1 86:20         86:25 87:8 93:9,22         153:13         160 10:12 59:3 70:8         137:10,11 138:6,9           fabricate         97:12         99:25 100:4,9,10         99:25 100:4,9,10         99:25 100:4,9,10         107:13 110:14         110:14         130:11 148:24         160us 52:23         16us 85:11         50:23         60us 52:3	1	1	[ ·	
extent         22:23         33:10,14,16 34:7,24         127:23 128:13,14         119:4,7 121:25           external         17:3 20:12         36:19 37:9,10,13,22         129:11 134:5         123:20 125:21           27:18,20 28:3,3,4         30:16 31:3 32:6,12         38:18,22 60:3,5,6         fllms         87:21         128:11,17 135:24           30:16 31:3 32:6,12         58:18,22 60:3,5,6         fllms         87:21         128:11,17 135:24           36:24 72:3 88:6,7         61:23 62:18 63:16         finally         16:17         films         87:21         138:11,17 135:24           95:7 110:2 111:19         67:6,8,10 76:23         77:3,5,16,21 78:3,5         finally         16:17         finacally         62:2         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         102:21         103:11         136:6         137:10,11         138:6,9         137:10,11         138:6,9         137:10,11         138:6,9         137:10,11         138:6,9         137:10,11         138:6,9         137:10,11         138:6,9         137:10,11         138:6,9         138:11         138:11         138:11         138:11         148:19 <t< td=""><td></td><td>1</td><td>, ,</td><td></td></t<>		1	, ,	
external         17:3 20:12         36:19 37:9,10,13,22         129:11 134:5         123:20 125:21         123:20 125:21           27:18,20 28:3,3,4         30:16 31:3 32:6,12         36:19,22,23         36:24,24 61:8,8,11         films 87:21         128:11,17 135:24         126:11 127:1,9           36:24 72:3 88:6,7         60:24,24 61:8,8,11         filters 144:20         films 87:21         136:1,18         136:1,18           95:7 110:2 111:19         67:6,8,10 76:23         77:3,5,16,21 78:3,5         filmally 16:17         filipping 88:22 89:20           125:20 126:9,25         77:3,5,16,21 78:3,5         77:86,9,12,17,23         find 10:12 59:3 70:8         137:10,11 138:6,9           129:11 130:4,11,19         31:1 36:18         79:24,25 80:1 86:20         84:24 85:10 105:12         102:21           137:18         96:17,17 97:17 98:1         96:17,17 97:17 98:1         131:1 148:24         139:16,18           fabricate 97:12         96:17,17 97:17 98:1         113:11 148:24         139:16,18           fact 119:25         96:17,17 97:17 98:1         108:10         108:10           fact 3 8:15 9:14         1125:3,4,9,20 126:4         1126:5,8,19 127:2           fairfax 4:15 19:14         128:16,16,19,23         129:1,4,5,10,14,16         18:11,13 19:5,7           far 60:4 65:15 71:21         74:13 77:4,18 95:3	_			
27:18,20 28:3,3,4 30:16 31.3 32:6,12 32:18 36:19,22,23 36:24 72:3 88:6,7 95:7 110:2 111:19 125:20 126:9,25 127:8 128:8,10 129:11 130:4,11,19 131:1 136:18 137:18  61:23 62:18 63:16 67:6,8,10 76:23 77:3,5,16,21 78:3,5 127:8 128:8,10 138:11 136:18 137:18  79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 79:8,12,15,17,18,23 76:1 77:23 82:18,19 138:13,18 141:1 138:13,18 141:1 138:13,18 141:1 148:19 16abricate 97:12 fabricating 67:17 98:4 125:11 fact 119:25 factors 38:6 150:17 fair 4:15 19:14 28:15 fairfax 4:16 falls 91:14 familiar 11:8 27:4 far 60:4 65:15 71:21 74:13 77:4,18 95:3 98:25 99:19,20 101:24 106:13 113:3 133:17 fast 65:20 fast 65:20 fast 65:20 fall 97:13 favor 105:14 field 17:7 36:2,7,15 102:12,17,19 103:2 103:5,5,20 fields 36:4  42:23 56:24,25 58:7 fillms 87:21 fillses 144:20 fillins 87:21 fillins 16:12 fill 10:12 59:3 70:8 fill 01:12 59:3 70:8 fill 01:12 59:3 70:8 fill 01:12 59:3 70:8 fill 01:12 125:3 8:12 100:12 follow 94:6 136:8 137:10,11 138:6,9 138:13,18 141:1 148:19 flow 94:6 136:8 137:10,11 138:6,9 138:13,18 141:1 138:13,13 13:5 focus 52:23 followed 24:10 followed	1	1	•	-
30:16 31:3 32:6,12         58:18,22 60:3,5,6         films 87:21         128:11,17 135:24           32:18 36:19,22,23         60:24,24 61:8,8,11         filters 144:20         filters	1			
32:18 36:19,22,23 36:24 72:3 88:6,7 95:7 110:2 111:19 76:68,10 76:23 125:20 126:9,25 127:8 128:8,10 129:11 130:4,11,19 131:1 136:18 137:18 79:24,25 80:1 86:20 137:18 137:18 86:25 87:8 93:9,22 96:17,17 97:17 98:1 126:5,8,19 127:12 127:4 13 77:4,18 95:3 98:25 99:19,20 101:24 106:13 113:3 133:17 13:1 33:13 113:3 133:17 13:1 65:20 130:2,4 61:2,8,11 102:12,17,19 103:2 1103:5,5,20 1103:11,136 113:5,18,10 113:1,136:18 113:1,18 115:11 125:3,49,20 126:4 126:5,8,19 127:12 128:16,16,19,23 113:3 133:17 143:18 161:18 17:18 18:10 18:10 18:10 18:11,13 19:5,7,7 18:11,13 19:1,14 18:11,13 19:1,14 18:11,13 19:1,14 19:11,14 19:11,14 19:11,14 19:11,14 19:11,14 19:11,14 19:11,14 19:11,14 19:	1	1	-	•
36:24 72:3 88:6,7         61:23 62:18 63:16         finally 16:17         flipping 88:22 89:20           95:7 110:2 111:19         67:6,8,10 76:23         153:13         flipping 88:22 89:20           125:20 126:9,25         77:3,5,16,21 78:3,5         76:1,723         153:13         flow 94:6 136:8           127:8 128:8,10         78:69,12,17,23         76:1,77:23 82:18,19         131:10;11 138:6,9           129:11 130:4,11,19         86:25 87:8 93:9,22         76:1,77:23 82:18,19         138:13,18 141:1           137:18         86:25 87:8 93:9,22         107:24 108:5         117:13 127:10         138:13,18 141:1           138:125:11         99:24,25 80:1 86:20         84:24 85:10 105:12         flowing 137:9           fabricate 97:12         99:15,15,19,20,20         99:15,15,19,20,20         99:15,15,19,20,20         99:15,15,19,20,20         99:15,15,19,20,20         99:15,15,19,20,20         99:15,15,19,20,20         108:10         finished 124:11         focusing 82:14         follow 16:25 45:11         follow 16:25 45:11         53:25         follow 16:25 45:11         53:25         follow 16:25 45:11         53:25         followed 24:10         53:44:4 90:13         108:10         54:14 90:13         118:11,13 19:5,7,7         128:14,61,61,9,23         129:14,5,10,14,16         13:15,16,16,18,21         13:11,14,61,61,18,21         13:11,13 18:11         <	1		(	128:11,17 135:24
95:7 110:2 111:19			<b>\</b>	136:1,18
125:20 126:9,25   77:3,5,16,21 78:3,5   78:6,9,12,17,23   78:6,9,12,17,18,23   76:1 77:23 82:18,19   138:13,18 141:1   137:18   86:25 87:8 93:9,22   107:24 108:5   107:24 108:5   107:19 113:11 148:24   139:16,18   139:16	-			<b>flipping</b> 88:22 89:20
127:8 128:8,10	95:7 110:2 111:19	67:6,8,10 76:23	financially 6:22	102:21
129:11 130:4,11,19	1	77:3,5,16,21 78:3,5	153:13	
131:1 136:18	1		i .	, ,
f         86:25 87:8 93:9,22         107:24 108:5         flowing 137:9           fabricate 97:12 fabricating 67:17 98:4 125:11 fact 119:25 factors 38:6 150:17 fair 4:15 19:14 28:15 fairfax 4:16 falls 91:14 familiar 11:8 27:4 far 60:4 65:15 71:21 74:13 77:4,18 95:3 98:25 99:19,20 101:24 106:13 113:3 133:17 fast 65:20 favor 105:14 field 17:7 36:2,7,15 102:12,17,19 103:2 103:5,5,20 fields 36:4         86:25 87:8 93:9,22 90:10,20 93:29:6 96:8,8 90:10,20 10:24; 107:13 110:14 13:11 148:24 139:16,18 focus 52:23 focusing 82:14 foil 88:5:11 fine 8:5,13 13:5 focusing 82:14 foil 98:18 follow 16:25 45:11 107:13 110:14 108:10 finished 124:11 foil 98:18 follow 16:25 45:11 107:13 110:14 108:10 finished 124:11 foil 98:18 follow 16:25 45:11 107:13 110:14 108:10 finished 124:11 foil 98:18 follow 16:25 45:11 108:10 finished 124:11 foil 98:18 follow 16:25 45:11 113:17,18 115:11 125:3,49,20 126:4 129:1,45,10,14,16 129:2,1 133:18 142:18 129:1,45,10,14,16 129:2,1 133:18 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:5,16,16,18,21 142:18 131:11 148:24 139:16,18 focus 52:23 focusing 82:14 foil 8:5,13 13:5 follow 16:25 45:11 132:13 13:5 follow 16:25 45:11 132:13,18 10:10 finished 124:11 153:25 follow 16:25 45:11 132:13,18 10:10 finished 124:11 125:3,49,20 126:4 129:1,45,10,14,16 129:1,45,10,14,16 129:1,33:18 14:18 17:9 foregoing 152:6 13:4,6,11 13:13:13:13:13 13:5,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18,21 13:15,16,16,18	1 1			138:13,18 141:1
f         93:22 95:6 96:8,8         117:13 127:10         flows 138:4 139:14           fabricate 97:12         96:17,17 97:17 98:1         96:17,17 97:17 98:1         131:11 148:24         139:16,18           fabricating 67:17         98:4 125:11         98:5,12,14,14,17         99:15,15,19,20,20         finds 85:11         focus 52:23           fact 119:25         factors 38:6 150:17         fair 4:15 19:14         107:13 110:14         108:10         follow 16:25 45:11           fairfax 4:16         125:3,4,9,20 126:4         113:17,18 115:11         finshed 124:11         53:25           familiar 11:8 27:4         126:5,8,19 127:12         firm 7:7,7,9         followed 24:10           far 60:4 65:15 71:21         129:21 133:18         129:21 133:18         23:16 27:19 31:11         foregoing 53:2           fals 13:3 13:17         fast 65:20         96:17 107:15         57:4 61:21,24 62:8         63:25 67:16 75:25         form 18:17 22:20,23           favor 105:14         fill 97:13         fill 30:19,19 43:7         93:17 95:20 97:11         38:25 39:20 42:9           fill 30:15,5,20         fill 36:4         117:13 127:10         118:6,7,11,13,19         42:5 49:8 50:2 51:8           fill 36:4         10:11         113:17,18 115:11         113:17,18 115:11         113:17,18 115:11         113:17,18 115:11		1		
fabricate         97:12         96:17,17 97:17 98:1         131:11 148:24         139:16,18           fabricating         67:17         98:5,12,14,14,17         finds         85:11         focus         52:23           fact         119:25         fact         119:25         fine         8:5,13 13:5         focusing         82:14           fact ors         38:6 150:17         99:25 100:4,9,10         24:23 77:8 104:13         foil 98:18           fact ors         38:6 150:17         107:13 110:14         108:10         foil 98:18           fair fax         4:16         13:17,18 115:11         finished         124:11         53:25           fairlia         91:14         126:5,8,19 127:12         firm         7:7,7,9         followed         24:10           far         60:4 65:15 71:21         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,4,5,10,14,16         129:1,13         13:15,16,16,18,21         foregoing         152:6           far         65:20         66:17 107:15	137:18	4		, 0
fabricate spiration         97:12         98:5,12,14,14,17         finds         85:11         focus         52:23           98:4 125:11         98:5,15,19,20,20         99:25 100:49,10         24:23 77:8 104:13         focus         52:23           fact 119:25         fact 15 19:14         107:13 110:14         108:10         finished         124:11         follow         16:25 45:11           fair fax         4:16         125:3,4,9,20 126:4         125:3,4,9,20 126:4         firm 7:7,7,9         firm 10:20         followed         24:10         54:14 90:13         following         53:25         following         53:24         following         53:25         following         53:4,6,11         foregoing         152:4         foregoing         152:4         foregoing         152:4         foregive         46:18 75:21         92:15         <	f	1	1	l .
fabricating         67:17         98:4 125:11         99:15,15,19,20,20         fine         8:5,13 13:5         focus         52:23           fact         119:25         factors         38:6 150:17         99:25 100:4,9,10         107:13 110:14         108:10         follow         16:25 45:11           fair         4:15 19:14         125:3,4,9,20 126:4         firm         7:7,7,9         firm         7:7,7,9         firm         7:7,7,9         firm         followed         24:10         54:14 90:13         followed         24:10         54:14 90:13         followed         24:10         54:14 90:13         following         53:25         followed         24:10         54:14 90:13         following         53:25         followed         24:10         54:14 90:13         following         53:25         following         53:26         following         53:26         following         53:26         following         53:26         following         53:4,6,11         foregoing         15:4         form         15:4,6,11	fabricate 97:12	1	i e	,
98:4 125:11 fact 119:25 factors 38:6 150:17 fair 4:15 19:14 28:15 fairfax 4:16 falls 91:14 familiar 11:8 27:4 far 60:4 65:15 71:21 74:13 77:4,18 95:3 98:25 99:19,20 101:24 106:13 113:3 133:17 fast 65:20 favor 105:14 field 17:7 36:2,7,15 102:12,17,19 103:2 103:5,5,20 fields 36:4  99:15,15,19,20,20 99:25 100:4,9,10 107:13 110:14 113:17,18 115:11 125:3,4,9,20 126:4 126:5,8,19 127:12 128:16,16,19,23 129:1,4,5,10,14,16 129:21 133:18 142:18 142:18 153:25 followed 24:10 54:14 90:13 following 53:2 follows 7:19 foregoing 152:6 153:4,6,11 follow 16:25 45:11 53:25 followed 24:10 54:14 90:13 following 53:2 follows 7:19 foregoing 152:6 153:4,6,11 foregoing 15:6 153:4,6,11 foregoing 15:6 153:4,6,11 foregoing 15:6 153:4,6,11 foregoing 15:6 153:4,6,11 fore			l l	1
fact         119:25         99:25 100:4,9,10         24:23 77:8 104:13         foil 98:18           factors         38:6 150:17         107:13 110:14         108:10         follow         16:25 45:11           fair         4:15 19:14         13:17,18 115:11         125:3,4,9,20 126:4         126:5,8,19 127:12         firm         7:7,7,9         followed         24:10           falls         91:14         126:5,8,19 127:12         firms         10:20         first         11:18 17:9         followed         24:10         54:14 90:13         following         53:25         following         53:2         following         53:4         following         53:4         following         53:4         following         53:4         foregoing         15:4         forgive         46:18 75:21         95:17         13:15,16,16,18,21         42:13 48:4 52			1	_
factors         38:6 150:17         107:13 110:14         108:10         follow         16:25 45:11           fair         4:15 19:14         13:17,18 115:11         113:17,18 115:11         finished         124:11         53:25           fairfax         4:16         126:5,8,19 127:12         firm         7:7,7,9         followed         24:10           fairfax         4:16         128:16,16,19,23         first         11:18 17:9         following         53:2           fair 60:4 65:15 71:21         129:21 133:18         142:18         129:21 133:18         142:18         foregoing         152:6           74:13 77:4,18 95:3         98:25 99:19,20         96:17 107:15         57:4 61:21,24 62:8         63:25 67:16 75:25         form         153:4,6,11         foregoing         152:6           13:3 133:17         fast         65:20         96:17 107:15         63:25 67:16 75:25         63:25 67:16 75:25         form         18:17 22:20,23           favor         105:14         90:10,18,18,19,20         107:18 111:16         43:8,20 44:8,22         45:5 49:8 50:2 51:8           102:12,17,19 103:2         80:12 99:10         116:21 117:16,21         17:23,25 118:3,4,6         54:10 56:3,22 61:17           103:5,5,20         93:8,17 94:20 95:10         118:6,7,1,1,3,19	· ·			
fair 4:15 19:14         113:17,18 115:11         finished 124:11         53:25           fairfax 4:16         125:3,4,9,20 126:4         firm 7:7,7,9         followed 24:10           fails 91:14         128:16,16,19,23         firms 10:20         first 11:18 17:9         following 53:2           far 60:4 65:15 71:21         129:21 133:18         142:18         142:18         13:15,16,16,18,21         foregoing 152:6           98:25 99:19,20         101:24 106:13         filed 11:11 113:5         filed 11:11 113:5         63:25 67:16 75:25         form 18:17 22:20,23           favor 105:14         field 17:7 36:2,7,15         102:12,17,19 103:2         89:5 90:9,10         90:10,18,18,19,20         90:10,18,18,19,20         117:23,25 118:3,4,6         45:5 49:8 50:2 51:8           fields 36:4         36:4         95:11,20 104:17,18         119:1,4,6,19,24         68:19 69:4 72:3	Į.	1		1
28:15 fairfax 4:16 falls 91:14 familiar 11:8 27:4 far 60:4 65:15 71:21 74:13 77:4,18 95:3 98:25 99:19,20 101:24 106:13 113:3 133:17 fast 65:20 favor 105:14 field 17:7 36:2,7,15 102:12,17,19 103:2 103:5,5,20 fields 36:4  125:3,4,9,20 126:4 126:5,8,19 127:12 128:16,16,19,23 129:1,4,5,10,14,16 129:21 133:18 142:18 142:18 142:18 142:18 131:15,16,16,18,21 142:18 142:18 131:15,16,16,18,21 142:18 13:15,16,16,18,21 142:13 48:4 52:20,20 157:4 61:21,24 62:8 163:25 67:16 75:25 161 170:18 111:16 170:18 111:18 17:9 18:11,13 19:5,7,7 18:11,13 19:5,7,7 18:11,13 19:5,7,7 18:11,13 19:5,7,7 18:11,13 19:5,7,7 19 153:4,6,11		1		
fairfax         4:16           falls         91:14           familiar         11:8 27:4           far         60:4 65:15 71:21           74:13 77:4,18 95:3         129:21 133:18         129:21 133:11         129:2			1	
familiar         11:8 27:4         129:1,4,5,10,14,16         18:11,13 19:5,7,7         follows         7:19           far         60:4 65:15 71:21         129:21 133:18         129:21 133:18         13:15,16,16,18,21         153:4,6,11         1				
familiar       11:8 27:4       129:1,4,5,10,14,16       18:11,13 19:5,7,7       follows       7:19         far       60:4 65:15 71:21       74:13 77:4,18 95:3       142:18       142:18       31:15,16,16,18,21       153:4,6,11       153:4,6,11         forgive       46:18 75:21       96:17 107:15       57:4 61:21,24 62:8       63:25 67:16 75:25       63:25 67:16 75:25       63:25 67:16 75:25       63:25 67:16 75:25       63:27 97:10       60llows       7:19         faxt       65:20       96:17 107:15       63:25 67:16 75:25       63:25 67:16 75:25       63:25 67:16 75:25       63:27 97:10       93:17 95:20 97:11       93:17 95:20 97:11       38:25 39:20 42:9         field       17:7 36:2,7,15       46:5 67:15,16,25       80:18 89:5 90:9,10       90:10,18,18,19,20       116:21 117:16,21       43:8,20 44:8,22       45:5 49:8 50:2 51:8         103:5,5,20       93:8,17 94:20 95:10       93:8,17 94:20 95:10       118:6,7,11,13,19       54:10 56:3,22 61:17         fields       36:4       95:11,20 104:17,18       119:1,4,6,19,24       68:19 69:4 72:3	falls 91:14	1 ' ' '		1
Tar 60:4 63:13 71:21         74:13 77:4,18 95:3       142:18       31:15,16,16,18,21       153:4,6,11         98:25 99:19,20       96:17 107:15       57:4 61:21,24 62:8       92:15         101:24 106:13       113:3 133:17       63:25 67:16 75:25       92:15         102:12,17,19 103:2       102:12,17,19 103:2       102:12,17,19 103:2       103:5,5,20       103:5,5,20       103:8,17 94:20 95:10       118:6,7,11,13,19       153:4,6,11       153:	familiar 11:8 27:4		1 .	· · · ·
74:13 77:4,18 95:3         98:25 99:19,20         42:13 48:4 52:20,20         forgive 46:18 75:21           101:24 106:13         96:17 107:15         57:4 61:21,24 62:8         92:15           13:3 133:17         filed 11:11 113:5         63:25 67:16 75:25         form 18:17 22:20,23           18:0 17:13         89:22 91:12,24         24:7,11 27:1 34:13           102:12,17,19 103:2         46:5 67:15,16,25         107:18 111:16         38:25 39:20 42:9           103:5,5,20         90:10,18,18,19,20         116:21 117:16,21         45:5 49:8 50:2 51:8           103:5,5,20         93:8,17 94:20 95:10         118:6,7,11,13,19         54:10 56:3,22 61:17           63:5 64:23 67:24         68:19 69:4 72:3	far 60:4 65:15 71:21			
98:25 99:19,20 101:24 106:13 113:3 133:17  fast 65:20 favor 105:14 field 17:7 36:2,7,15 102:12,17,19 103:2 103:5,5,20 fields 36:4  figures 33:2 93:6 96:17 107:15 96:17 107:15 107:18 111:16 107:18 111:16 117:23,25 118:3,4,6 118:6,7,11,13,19 119:1,4,6,19,24  figures 33:2 93:6 96:17 107:15 57:4 61:21,24 62:8 92:15 form 18:17 22:20,23 24:7,11 27:1 34:13 38:25 39:20 42:9 45:5 49:8 50:2 51:8 117:23,25 118:3,4,6 118:6,7,11,13,19 63:5 64:23 67:24 68:19 69:4 72:3	74:13 77:4,18 95:3			1
101:24 106:13 113:3 133:17  fast 65:20 favor 105:14 field 17:7 36:2,7,15 102:12,17,19 103:2 103:5,5,20 fields 36:4  96:17 107:15 filed 11:11 113:5 filed 11:11 11:10 filed 11:11 11:10 filed 11:11 113:5 filed 11:11 11:10 filed 11:		1 0	1	, ,
113:3 133:17       filed 11:11 113:5       63:25 67:16 75:25       form 18:17 22:20,23         fast 65:20       fill 97:13       89:22 91:12,24       24:7,11 27:1 34:13         film 30:19,19 43:7       46:5 67:15,16,25       107:18 111:16       43:8,20 44:8,22         103:5,5,20       90:10,18,18,19,20       117:23,25 118:3,4,6       54:10 56:3,22 61:17         63:25 67:16 75:25       63:25 67:16 75:25       63:25 67:16 75:25         89:22 91:12,24       24:7,11 27:1 34:13         38:25 39:20 42:9       43:8,20 44:8,22         116:21 117:16,21       45:5 49:8 50:2 51:8         117:23,25 118:3,4,6       54:10 56:3,22 61:17         118:6,7,11,13,19       63:5 64:23 67:24         65:20       65:11,20 104:17,18			1	
fast       65:20       fill       97:13       89:22 91:12,24       24:7,11 27:1 34:13         favor       105:14       film       30:19,19 43:7       93:17 95:20 97:11       38:25 39:20 42:9         field       17:7 36:2,7,15       46:5 67:15,16,25       107:18 111:16       43:8,20 44:8,22         102:12,17,19 103:2       90:10,18,18,19,20       116:21 117:16,21       45:5 49:8 50:2 51:8         103:5,5,20       93:8,17 94:20 95:10       118:6,7,11,13,19       54:10 56:3,22 61:17         63:5 64:23 67:24       63:5 64:23 67:24         95:11,20 104:17,18       119:1,4,6,19,24       68:19 69:4 72:3			+	
favor       105:14       film       30:19,19 43:7       93:17 95:20 97:11       38:25 39:20 42:9         field       17:7 36:2,7,15       46:5 67:15,16,25       107:18 111:16       43:8,20 44:8,22         102:12,17,19 103:2       80:18 89:5 90:9,10       116:21 117:16,21       45:5 49:8 50:2 51:8         103:5,5,20       93:8,17 94:20 95:10       118:6,7,11,13,19       54:10 56:3,22 61:17         63:5 64:23 67:24       63:5 64:23 67:24         95:11,20 104:17,18       119:1,4,6,19,24       68:19 69:4 72:3	fast 65:20	1	1	•
field       17:7 36:2,7,15       46:5 67:15,16,25       107:18 111:16       43:8,20 44:8,22         102:12,17,19 103:2       80:18 89:5 90:9,10       116:21 117:16,21       45:5 49:8 50:2 51:8         103:5,5,20       90:10,18,18,19,20       117:23,25 118:3,4,6       54:10 56:3,22 61:17         63:5 64:23 67:24       95:11,20 104:17,18       119:1,4,6,19,24       68:19 69:4 72:3	1	1		ì
102:12,17,19 103:2 103:5,5,20 fields 36:4  80:18 89:5 90:9,10 90:10,18,18,19,20 93:8,17 94:20 95:10 95:11,20 104:17,18  116:21 117:16,21 117:23,25 118:3,4,6 118:6,7,11,13,19 119:1,4,6,19,24  45:5 49:8 50:2 51:8 54:10 56:3,22 61:17 63:5 64:23 67:24 68:19 69:4 72:3				
103:5,5,20 fields 36:4 90:10,18,18,19,20 93:8,17 94:20 95:10 118:6,7,11,13,19 63:5 64:23 67:24 68:19 69:4 72:3	1	,	_	
fields     36:4       93:8,17 94:20 95:10     118:6,7,11,13,19       95:11,20 104:17,18     119:1,4,6,19,24       63:5 64:23 67:24       68:19 69:4 72:3	1	1 ' ' ' '	1 ' '	1
95:11,20 104:17,18   119:1,4,6,19,24   68:19 69:4 72:3	• •	*	1	1
105:6 109:13,24   121:2,3,4,4,6,9,10   73:9 75:4,10 83:12			1 1 1 1	
		105:6 109:13,24	121:2,3,4,4,6,9,10	73:9 75:4,10 83:12

[form - holes] Page 15

83:15,22,23 85:21	fpc 95:8 125:21	27:1 34:13 38:25	139:24 144:8,9
88:9,10,24 89:21	126:11 127:1	39:20 40:7 41:3	good 6:4 7:23,24
90:13 91:21 93:17	128:11	44:22 45:5 49:8	19:20 55:1 100:20
95:1,17 98:19 99:6	frankly 77:12	51:8 54:10 56:22	117:5,11 126:23
99:24 104:4 105:9	freedom 142:24	61:17 63:5 64:23	150:14 151:3
106:7 114:15 115:7	front 40:15 116:19		
i	1	68:19 75:4,10 86:6	governed 24:6
118:2 122:6,18	full 13:16 51:6	86:11 87:4 88:24	great 46:2
125:8 129:19 130:6	function 102:11	92:18 98:19 99:6,24	greek 26:8,10,20
132:7,25 136:25	143:14	101:5 104:4 105:9	ground 86:2
137:16 139:9	further 10:2 27:25	122:6,18 125:8	group 101:16
146:24 147:1,4,16	31:20 36:11 42:22	129:19 130:6 132:7	119:22
149:9,22	62:1,2 90:16 91:8	132:15,17,25	grouped 60:7,16
formation 51:23	111:7 151:9 153:10	136:25 137:16	guess 123:9
58:3 75:3 130:3	153:12	139:9 146:24	guidance 40:17
formed 22:22 23:16	furthermore 56:5	147:16 149:9	guide 41:6 83:4
23:22 31:22 37:5,7	145:8	151:10	95:12 105:15
37:15,21 38:5 42:14	g	give 16:4,6 25:3	<b>guided</b> 31:15 76:8
42:18,25 43:4,11,18	gain 56:5 68:12	39:10 40:2,10,15	h
43:21 44:3 48:4	73:10,24 74:9,22	41:7 51:13 108:4	hand 60:6 77:2 78:6
53:3,4,13 56:19	110:25 133:9	128:2	78:7,18
58:3,25 59:2,17,18	gap 21:20 97:7	given 75:19 109:16	happen 28:4 114:3
61:21 62:3,8 64:1,3	102:15 131:19	151:12	happens 28:7 146:9
68:15 70:24 71:1,3	134:12,15 142:21	gives 91:17 112:13	hatalis 1:14 2:14 5:3
71:5,18 81:6 97:10	gaps 100:3	giving 65:13	6:7 7:17 41:14,19
97:16 105:7,10	gate 23:11 38:16	glass 15:6,11 17:4	151:13 152:4,16
110:5 111:13 115:8	42:20 43:1,2,4,16	17:11,19 20:19,25	head 16:5
117:24 118:25	43:18 44:2,4,11,13	22:11 30:9,10 42:25	hearings 12:23
119:2,6 120:20	44:16,17 45:18	43:6,8 77:13 90:12	heart 54:1
129:2 141:20	46:12 50:14 58:4,10	112:10,15 144:7,8,9	height 130:2,15
forming 23:17 31:22	58:13,22 61:12	144:20	held 6:11 25:4
42:14 43:6 48:24	76:24 142:19 143:9	<b>go</b> 6:17 9:22 24:25	help 70:9 71:8 76:5
61:22 62:9 64:2,4	143:13 145:4	28:11 35:4 37:8	77:9,15 80:1 81:17
68:6 71:20 74:11	147:23,25 148:2,8	42:16,19,22 51:25	hereto 152:8
100:1	147.23,23 146.2,6	54:4 57:23,25 62:7	high 45:8 65:21
forms 22:25 26:7	general 35:16 64:17	68:5 82:2 84:20	83:20 102:10 110:1
44:14	64:24 73:5,7,13,16	100:1 103:21 128:6	111:18 147:2
formulate 92:16	i e	142:16 143:5 145:6	i de la companya de
formulated 76:18	73:17 74:17 82:4,5	150:5	higher 10:14 98:17
forth 153:5	83:12 85:20	goal 88:4	highlighted 99:17
<b>found</b> 85:6,10 95:12	generally 17:1 19:21	goes 14:1 19:8 20:15	highly 148:9
96:13 108:24 109:1	21:3 78:23	25:24,25 30:20 45:9	hirakata 11:6 12:3
112:8 131:8	generate 114:5	53:10,10	hitting 150:1
four 13:6,8,11,12,13	getting 54:1	going 9:10 10:9	hole 46:8 51:25
13:13 15:16 28:19	gibson 3:5 7:2,12,12	22:14 25:7 29:17	holes 44:3 47:22
28:20 95:14,15	8:13,23 22:20 24:7	54:8 126:17 139:3	49:5 51:24 55:23
	Varitant Chicago P		

89:13 95:9 125:22	implicit 118:17	74:6 75:2 95:10,20	insulator 37:16
126:12 127:2	implied 121:15	101:12 109:5,7	38:10,23,24 41:2
128:12,18	122:3	125:23 126:13	47:21 48:21 52:12
hope 75:20	implies 120:19	127:3 128:13	61:13 115:14
hoping 8:8	124:25 135:14	146:23 147:2,4	128:25 129:3
hour 80:15	136:1	148:20	insulators 38:21
humidity 110:1	imply 117:20 119:13	individual 119:19	39:17,19 40:24 41:1
111:18	120:1	information 58:4	41:5
hundred 50:18	<b>import</b> 87:25	76:9,17	intact 49:20 145:18
hundreds 18:14	important 64:17	informed 40:13	intellectual 4:14
28:20	<b>improve</b> 64:25 65:1	inherently 120:1	intend 8:18
husch 4:4 7:7	73:10	initial 12:17 23:22	inter 11:6 12:16
huschblackwell.co	improved 15:25	initialed 152:7	23:14 57:3,5 63:24
4:11,12	32:2 66:8 68:12	ink 152:7	90:20 93:8,17 94:20
hypothesis 59:20,21	111:6	innolux 1:4 2:4 6:13	95:10,20 103:8
hypothetical 51:10	improvement 15:19	innovation 113:6	111:13 114:16
51:11 141:9 142:11	inch 21:24 83:14	innovations 130:23	115:3,8 125:7,23
142:13 143:14	include 34:6 42:10	131:6	126:12 127:3
147:9 149:11	63:13 67:24 70:19	inside 18:1,7,8	128:13
150:11	71:14 133:8	21:17 35:8 37:25	interacts 150:9
hypothetically	includes 61:5 82:11	38:1 60:11 139:19	interconnect 31:10
50:12 59:16 147:8	115:4 147:18	141:2	interdisciplinary
147:17 149:3,5	including 80:18	instance 91:4	36:2
i	income 9:20 10:7,24	insulating 38:15	interested 6:22
ideally 65:4	increase 16:18	42:6 47:16,20 48:17	153:13
identical 91:10	38:18 64:19 66:11	51:22 53:3 60:15	interesting 142:10
99:16	102:14,14,19	67:15,16 69:20 72:1	interference 6:20
identified 32:7	increased 143:1	87:19 89:10,12,16	interval 18:16
identify 7:5	increases 65:10	89:25 101:23 109:3	
		69.23 101.23 109.3	introduce 74:21
	66:15	109:4,8,14 110:5,7	97:7
ignorance 75:21	66:15 increasing 64:14	109:4,8,14 110:5,7 110:11 111:1,5,12	1
ignorance 75:21 illinois 3:17 4:9	66:15 increasing 64:14 65:6 66:25	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11	97:7
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5	66:15 increasing 64:14 65:6 66:25 independent 28:16	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20	97:7 introduced 27:3 introducing 15:13 invention 16:18
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10 implement 35:20	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1 indicate 34:9 67:20	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10 122:13,14 123:20	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25 inventor 49:24,24
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10 implement 35:20 69:1 74:10 93:24	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1 indicate 34:9 67:20 133:5	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10 122:13,14 123:20 123:22 124:22	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25 inventor 49:24,24 50:2 94:1
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10 implement 35:20 69:1 74:10 93:24 implementation	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1 indicate 34:9 67:20 133:5 indicated 32:16	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10 122:13,14 123:20 123:22 124:22 127:23 134:5	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25 inventor 49:24,24 50:2 94:1 inventors 94:9,10,24
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10 implement 35:20 69:1 74:10 93:24 implementation 102:7	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1 indicate 34:9 67:20 133:5 indicated 32:16 50:19 72:12 79:7	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10 122:13,14 123:20 123:22 124:22 127:23 134:5 insulation 42:11	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25 inventor 49:24,24 50:2 94:1 inventors 94:9,10,24 96:1,6
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10 implement 35:20 69:1 74:10 93:24 implementation 102:7 implementations	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1 indicate 34:9 67:20 133:5 indicated 32:16 50:19 72:12 79:7 indication 81:22	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10 122:13,14 123:20 123:22 124:22 127:23 134:5 insulation 42:11 52:4,16,22 90:20	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25 inventor 49:24,24 50:2 94:1 inventors 94:9,10,24 96:1,6 inversely 38:14
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10 implement 35:20 69:1 74:10 93:24 implementation 102:7 implementations 95:24	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1 indicate 34:9 67:20 133:5 indicated 32:16 50:19 72:12 79:7 indication 81:22 83:5	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10 122:13,14 123:20 123:22 124:22 127:23 134:5 insulation 42:11 52:4,16,22 90:20 104:18	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25 inventor 49:24,24 50:2 94:1 inventors 94:9,10,24 96:1,6 inversely 38:14 invite 16:21 93:3
ignorance 75:21 illinois 3:17 4:9 illustrate 30:24 34:5 84:23 113:17 illustrating 34:2 illustration 34:7 impact 38:6,10 implement 35:20 69:1 74:10 93:24 implementation 102:7 implementations	66:15 increasing 64:14 65:6 66:25 independent 28:16 29:2 independently 28:13 114:18 index 5:1 indicate 34:9 67:20 133:5 indicated 32:16 50:19 72:12 79:7 indication 81:22	109:4,8,14 110:5,7 110:11 111:1,5,12 111:14,23 112:5,11 112:13,14,15,20 114:2,16,17,18 115:3,8 116:10,12 116:13 118:5,7,12 118:13 121:3,5,6,10 122:13,14 123:20 123:22 124:22 127:23 134:5 insulation 42:11 52:4,16,22 90:20	97:7 introduced 27:3 introducing 15:13 invention 16:18 66:10,24 90:24,25 91:3 92:2,6 94:21 105:25 106:2,5 112:25 inventor 49:24,24 50:2 94:1 inventors 94:9,10,24 96:1,6 inversely 38:14

<b>involved</b> 10:12 11:5	Ir	31:24 32:4,5,8	114:22 115:3,8,14
involves 51:23	k	37:12 38:15 41:25	116:7,8,10 118:18
inwards 140:25	keep 130:14	42:5,6 43:12 44:8	118:22,23,25 120:7
ipr 8:20,20	keeps 64:20	44:15 45:17,22,24	120:9,11,15,17,18
1 ~ ′	kind 14:10 26:8	45:25 46:3,6,7,8,14	120:20,22,23 122:2
ipr2013-00068 1:6	30:7 62:2 77:16	46:15,20 47:9,11,16	120:20,22,23 122.2
2:6 6:15	136:20		123:23 124:15,21
irvine 1:15 2:16 3:8	know 8:24 9:3 11:17	47:20 48:16,17,18	
6:1,12	15:4 29:6 32:25	48:22 51:22,25 53:3	125:6,7,23 126:12
isolated 138:2 141:1	45:21 47:15,19 48:2	54:12 55:4,7,8 56:9	127:3,24 128:13
issue 12:12 15:8	55:20 77:12,15	57:15 58:16,21	129:13 131:12,22
34:22 86:7 130:8	96:12 97:4 107:3	60:13,15 61:15	131:25 132:1,5,22
131:14,17 132:6	108:12,13 120:3,16	62:14,16,20 63:1,11	133:8,11,15,20,22
134:12 147:20,22	138:22 151:2,2,6	64:7,11 65:9 66:1	134:18,23 136:1,5,9
issues 8:11,16 9:16	knowledge 39:15	66:24 68:3,8,11,21	141:11,21 142:4,7
9:25 14:3	76:6	68:22,24,24 69:4,9	145:22,22 148:21
issuing 131:6	known 15:9 47:25	69:10,11,12,17,18	148:22 149:1,13
ito 20:24 22:13	51:21 54:12 68:1	69:20,21,23 70:12	150:8,9 151:4
69:23 70:10,19	100:6 112:25	70:13,24,24 71:3,14	layers 31:25 32:4
80:19 83:7,9,9,16	150:14	71:18 72:1,2,19,22	37:15 42:2,8,18
83:19,22 87:13 88:4	1	73:2,8 74:3,4,6,8,10	43:25 44:10 45:14
93:7 94:19 95:10,25	<u> </u>	74:18 76:24 77:5	45:15 47:16,19,20
101:23 102:1,4,15	label 19:8 37:3	80:19 81:2,6,9,16	47:22 48:20 50:1,3
103:17,19,24	labeled 19:19 23:19	82:22 83:23,25 84:1	50:20 51:7,22 52:5
104:11,11 105:21	27:19 30:23 37:1,2	84:7 85:3,7,14,19	52:13,17,23 53:1
109:2,4 112:10,13	77:17 133:25	86:22,23 87:12,15	55:9,12,21,22 56:6
113:1,15,21 114:1	labels 31:8	87:17,19,19 88:4,5	56:19 57:15 58:8,9
114:25 116:10	laboratory 1:7 2:7	88:9,10,11,23 89:4	59:19 62:17 63:11
125:23 126:13	6:14	89:4,10,12,14,14,17	63:12 66:5 68:11,15
127:3 128:13,21,25	laid 48:20 58:20	89:25 90:13,20 91:4	73:17,20,22,24 74:1
129:2,17,23 134:6	87:9 110:25	91:5,6,6,9,9 92:11	74:2,7 81:5 85:7,12
147:10,12,18,21,22	laminated 43:2,16	93:8,17 94:20 95:10	85:12,14,15,20,23
148:3,9,10 149:5,12	language 95:22	95:20,25 97:21 98:3	87:18 88:22 89:15
149:13,15,17	96:10,13 113:13	98:7,9,11,18,21	90:5 93:15 94:25
150:20,25 151:4	119:13 123:18	99:4,4 100:13	95:3,18 102:14
1	124:24 125:1,12,13	101:13,23 102:1,2,5	105:1,4 106:18,19
J	125:16,19 126:7,9	102:14,16,20,23	109:5 112:6,14
j 4:6 37:16	126:14 127:19	103:16,19,20 104:6	116:12,13 121:19
jeffer 3:4	large 83:13 84:4	104:6 105:7 106:20	123:4 127:22
jmbm.com 3:10	97:7	106:21 109:3,4,6,7	130:14,17,23
job 1:23	lasalle 3:16	109:8,14 110:1,6,7	131:20 135:15
johnson 3:13 7:11	law 3:6,15 4:7,14	110:15,16,19 111:1	136:7,11 141:12,19
july 1:16 2:18 6:1,5	7:7 10:20	111:4,5,13,14,23,23	149:17 150:1,12,13
41:12	lay 87:21 89:16	112:1,5,11,13,15,20	lcd 17:21
<b>jump</b> 59:7	layer 16:2 20:24	113:16,16,21 114:1	lead 53:2 106:20
	22:13 23:22 24:2,5	114:1,11,12,12,16	138:22,23
	Varitant Chicago D		

leads 140:1	46:20 47:7 50:4,22	41:25 42:13,15,15	146:8 147:3
learn 34:16	53:4,5,14,21 55:3	43:7,9,10,10,15	Ilc 151:14
leave 43:7 48:8,12	55:12 56:3,15 57:4	44:6,8 45:25 46:21	<b>Ilp</b> 3:4,13 4:4 7:8
49:17 52:12 54:12	57:5,14 58:21 59:18	48:4 49:2 50:10	located 111:8
145:18	60:14,14,18,20,21	51:5,7 52:7,18 55:4	location 34:5 70:2
leaves 46:4	61:4,7,19,20,20,21	55:25 56:1,2,19	144:14
left 18:4 19:15 21:5	62:1,6,13,13 63:22	57:1,8,8,8,9,10,15	long 24:13 31:19
21:8,10,13,14,24	64:1,9,13 66:2,14	57:18,19 58:20	52:19 74:11 83:15
23:24 26:17 48:4	67:11 68:15,16,24	59:17 60:7,9,13,15	83:16,22 102:25
54:7 60:12 77:5,18	69:10,14,19 70:11	60:18,21,23,24 61:1	108:4,10 140:4
77:25 78:6,18,24	70:14 71:15,18,20	61:5,7,12,22,25	148:11,13,14,24
79:3,22 96:22,23	71:25 72:4,15,22,23	62:3,4,8,9,9,18	longer 31:12,13,17
97:20 98:18,21,23	74:4,5,11,16,16,21	63:14,23,25 64:2,4	look 11:13 30:8,10
98:24,25 99:19,20	82:1,1,3 84:10 88:6	66:2 67:9 68:7,16	31:20 33:8 56:23
100:2,13,21 104:22	88:8 90:8,17,23	68:17,25 71:14,20	60:3 93:6 108:8,9
104:23,23	91:25 95:5 105:24	71:24 72:3,19,19	113:20,25 116:25
legal 6:9	111:9 114:8,9 115:5	73:19 81:10,11,11	121:19 123:3 135:6
length 50:4,22 83:25	117:7,8,14,16,22,23	81:18,19,23,23	looked 19:13 65:15
148:12	117:25 118:3,5,6,10	82:17,17,20,20,25	<b>looking</b> 23:8,9 26:15
lengthy 136:13	118:11,14,19,24	83:1,6,7,13,13,15,15	29:18 34:24 59:13
lens 80:1	119:1,5,6,12,14	83:16,16,22 89:24	63:18 76:10,23
letter 26:8,11,20	120:6 121:3,4,6,8	89:24 94:7 95:7,15	79:25 96:16 97:17
level 17:15 40:25	121:10,11,12,24,25	109:22 114:24	121:23 125:3 127:9
56:9 58:17,18,25	122:11,16,20,23	117:1 125:16,20	128:23 129:14
59:2 95:16 104:16	123:19,24 124:12	126:10,25 127:8	looks 22:8,16 26:21
light 82:22 85:22	124:13,23 125:4	128:8,10 129:12	lot 8:7,21 9:18 10:4
87:11 89:18 92:17	130:3,4,5,5,19,20	142:18,21 145:1	10:6 14:9 15:2,9
106:16 127:17	131:1,1 132:14	149:5	86:5 92:16 94:5
likewise 61:6	135:7,8,23,24 136:3	lining 42:24	95:2 145:13 149:23
<b>limit</b> 94:2	137:19 138:23	liquid 14:12 21:20	150:2,11,16,17
<b>limitation</b> 129:22,24	142:16,17,22 143:6	79:5 97:13 101:9	low 32:8 65:24
limitations 124:19	143:6,16,21 144:2	102:7,10,11	148:16
<b>limited</b> 88:3 125:17	144:16,17 148:10	list 52:25 59:4 82:10	lower 16:22 17:1,3,9
line 18:8 20:9 22:16	148:12,15,23,25	82:11 95:19,19	26:5 27:20,21 32:8
23:10,16,20 26:24	149:1	107:15,21 123:24	32:8 38:24 39:19
27:14 28:2 30:16,25	lines 18:2,11,12,12	listed 23:7 107:10	41:2 43:12 44:10
31:3,5,7,8,9,11,15	18:13,14,18,19,22	113:8 124:19	45:22,24,25 46:8,14
31:15,21 32:6,7,10	18:23,24,24 19:2,4	lists 65:22 106:13	52:1 55:4,7 56:3
32:12,12,16 33:4,9	19:4 23:2,6,13,15	107:5	58:16,17,18,24
33:15,22,23,24 34:1	23:17 24:12,13	litigation 9:18 10:5	61:15 68:23 69:9
36:20,20,22,22,23	26:16,23 27:25	10:8,12,24	89:14 90:13,16 91:4
36:24 37:1,4,18	29:22,24,25 30:2,7	litigations 9:21	91:9 99:21 100:3,6
42:9,12,19,20,22,24	30:12,13,14,14	little 21:23 28:25	100:15,22 102:13
43:1,15,19,25 44:4	31:21,22,23 32:9,17	58:8 63:21 67:5	102:17 103:22,22
44:11,13,18 46:15	32:18 33:21 37:20	72:10 78:21 96:16	

Joycovina 16.10	151:8	maximize 13:23	91:4,5,9 98:7 99:22
lowering 16:10		15:10	105:7 109:6 110:1
lunch 80:7,15	mark 4:6 7:8	mean 8:15 17:24	
m	mark.murphy 4:12		110:20,22 111:6,14
m 3:5	marked 8:1 18:9	18:1 22:21 27:9	111:19 133:13,14 134:14 146:23
machine 153:9	29:3,22 87:3 97:18	32:25 38:8 44:23	
magical 138:21	mask 23:18 24:6	45:24 48:12,12 49:9	147:10,11 149:6,19
magnification 19:18	47:6,6 49:4,17,19	51:2,15 54:7 63:17	149:22,25 150:8
magnifying 77:13	54:4,6,8 60:20	65:4 84:14,16 97:4	metallic 99:4
80:1	masked 49:3	101:14,15 116:16	metals 24:19 50:17
maintain 16:15	masking 24:3,10	118:21 119:5 120:8	134:6 140:8
74:20	45:1,7,10 46:1,2	122:7 137:13	microphones 6:18
maintaining 143:9	49:1,13,14 52:10,11	146:18 150:23	microscope 77:13
majority 46:2	53:17,23 54:5,11	meaning 31:5 35:19	<b>middle</b> 48:15 52:7
making 14:3,4 15:2	61:3	51:14	124:9
20:17 51:10 70:15	match 73:24 74:7	means 8:24 15:13	<b>milt</b> 6:7 41:14,19
73:18 74:17 92:10	material 22:4 23:1,1	20:15 44:2 47:8,9	miltiadis 1:14 2:14
101:9 120:3 130:11	23:2,5,9,18 29:5,8	54:11,21,22 55:23	5:3 7:17 152:4,16
mangels 3:4	29:12 30:5,5 32:9	56:18 61:3 64:21	mind 102:24 108:6
manner 111:15	36:6 37:7 39:23	117:23 121:7 136:6	mine 10:14
manufacturing	40:1,12 43:14 44:4	136:11 138:9	minimize 14:10
15:25 16:1,19 32:2	44:5,5,7,11,14 45:9	140:16 141:18	minimizing 15:4
64:14,18,19,21,25	53:4,5,14,21 54:13	144:18	minimum 63:13
1 ' ' ' '	54:16 55:3 57:19,20	meant 34:14	minute 92:19
65:6 66:3,7,12,16	58:19 60:14,18,20	measure 99:17	minutes 108:15
67:1 71:5 73:11,23	61:4,7,25 62:13,13	140:13	missed 16:6 39:7
94:7 145:2	63:14 68:1,15,16	media 151:13	misunderstood
manzo 4:5 5:6 7:1,6	69:11,14,20 70:11	meet 131:8	99:14
7:6,22 9:8 23:3	70:14 71:18,20,25	memory 151:1	mitchell 3:4
24:22 27:6 34:23	72:4,15,15,23 74:4	mental 108:12	mix 73:24 74:7
39:3 40:4,20 41:9	74:5,21 81:7,9,13	mention 84:7	modification 70:18
41:22 44:25 45:20	81:24 82:2,6,9,25	150:22	116:2,2,5,14
49:11 52:2 54:25	83:3,20 90:6 104:2	mentioned 29:9	modifications 91:2
57:21 62:15 63:6	104:3 135:22 136:4	103:7 115:12 144:3	91:13 92:7 106:3,10
65:25 69:3 75:7,15	136:4 140:7 142:24	mentioning 82:9,19	106:15 107:6,25
80:6,13 86:10,17	148:9 149:23 150:5	85:8	108:1 115:25
87:5 88:25 92:21	materials 62:14	mentions 83:19	148:18
93:2 98:22 99:7	64:9 72:23 73:22	met 13:6,7	modified 88:2 134:1
100:7 101:8 104:8	74:17 81:12,18	metal 22:23,24	modify 67:21,23
105:17 108:23	82:11,17,20 91:8,18	23:22,24 24:5,8,11	87:11 88:1 132:11
122:8 124:1,8	91:19,20 94:25	25:1,11 30:18,19,20	133:6 147:8 150:7
125:18 129:25	98:15 140:9 143:18	30:21,25 31:1,2	modifying 115:23
130:12 132:8,16,19	matrix 82:22 102:7	32:5,8 37:5 46:14	modulate 65:19
135:4 137:4 138:7	matter 12:5 40:9	46:15,16 47:20,22	molybdenum 82:12
141:5 143:25 146:7	76:3,7	51:25 52:1 63:1	91:7
147:7 149:2 150:18	, , , , ,	69:18 86:23 90:14	) 1.1
		Panarting Company	

moment 32:7,21	nitride 43:3,17	obtain 142:10	ones 12:9 24:17 30:2
128:2,3 139:19,20	44:19 46:5,13,23	obtained 140:19	112:7 117:18
140:21	47:6 49:3 67:13	obvious 85:23 88:13	127:15 150:8
monochrome 18:19	110:9 114:12 115:2	88:20 89:17 92:10	opaque 30:6 90:9,18
moriyama 113:25	115:5,7,9	94:14,16 98:9	open 27:10 41:8
114:3	normally 98:2	106:16 107:7 116:1	44:3 47:22 55:23
morning 6:4 7:23,24	north 4:15	116:3,4,14 131:19	
8:7 42:2 141:8	note 6:16	occasionally 33:20	opened 138:1
148:6	noted 151:15 152:7	56:21	opening 45:18,19
motivated 134:4	noted 131:13 132:7		56:8 88:9,10 89:12
		occupies 14:7	89:21,21 102:21
motivation 80:23	number 16:18 22:10	occupy 32:15	103:23 104:23,24
132:21 134:17	29:6,18,21 32:13	occur 49:23 135:16	122:13,14,23 123:8
move 104:13 122:9	41:13 57:12 64:14	occurring 44:3	123:10,11,12,13
144:13 146:10	64:19 66:11 77:2	142:9	124:12 125:7
moved 147:14	94:8,23 113:13	occurs 42:18	129:11 138:17
multiple 25:17	121:1 151:13	offers 133:12	openings 44:12
61:19 63:11,12	numbers 13:1 151:2	office 1:1 2:1 4:14	69:16,23 70:13
85:12 91:12 121:19	151:5	oh 26:14 78:11	71:17,19 72:1 84:5
121:22	numeral 61:10	143:21 144:1	93:18 106:23
murphy 4:6 7:8	numerals 127:14	okay 8:3 9:7 17:1,18	128:20
143:24	128:4	17:20 19:10,12,24	operate 103:22
n	0	22:14,19 23:4,25	operation 145:17
name 6:7 41:19	oath 6:21 7:15,18	25:6,12,23 27:12,24	opinion 93:25
82:12 153:16	153:7	28:15,24 29:3 32:11	opposing 18:25 19:1
nanometer 90:11	object 125:8 135:20	32:20 33:8 34:24	24:16 148:14
nanometers 90:19	135:22 146:24	42:12 47:2,12 48:14	opposite 67:3
narrow 14:14 23:20	147:16 149:9	48:21 52:24 53:18	opt 145:20
142:18,21	<b>objection</b> 9:2 22:20	54:3 55:14 59:24	<b>option</b> 105:19
narrower 14:19	24:7 27:1 34:13	61:11,15 62:24	options 87:25 88:12
narrowing 101:19	38:25 39:20 40:7	63:20 69:4 72:5,8	94:9
near 21:23	41:3 44:22 45:5	78:17 79:7,20 80:4	order 14:11 15:20
need 8:8,17 9:5	49:8 51:8 54:10	80:17 83:18 84:24	30:23 34:3 48:19
17:17 39:13 45:17	56:22 61:17 63:5	86:17 87:10 88:21	53:1 59:4 70:7 71:4
46:11,12 49:23 55:5	64:23 68:19 75:4,10	96:22 97:17 98:12	71:6 73:24 74:8,22
76:3 77:12 86:6	88:24 98:19 99:6,24	98:25 99:12 103:11	88:22 91:14 92:9
93:15 146:9	104:4 105:9 122:6	105:2 107:12	95:18 102:22
needed 14:11 39:17	122:18 129:19	108:17 109:20	104:20 105:3,8
70:6	130:6 132:7,25	111:7 116:25	107:20 110:24
needs 105:6,10	136:25 137:16	117:11,15 118:1	112:3,18 119:23
139:17	139:9	120:4,6 126:24	120:12,19,22
neither 94:1 153:12	objective 12:6 13:22	129:6,9 130:13	121:15,19 123:17
new 27:3 35:22	14:2 130:7	132:20 139:21	123:24 124:20,25
131:6	objectives 14:20	143:5 146:15 149:3	127:22 144:20
131.0	55,000,000	once 139:6 140:16	ordering 71:3
	Train (Clinia)		

[orders - penalty] Page 21

orders 89:20	110:9 114:12 115:1	parallel 18:2 55:25	partner 7:8
ordinarily 39:16	115:4,4,6 125:24	56:20 59:18 60:7	parts 49:18,20 52:9
69:8 74:24 89:3	126:13 127:4	100:1,23	65:16 73:21,22
93:19 101:11 127:6	128:14 146:23	pardon 26:2	97:19 104:19
ordinary 34:15	147:2,5 148:21	park 2:16 3:7 6:11	party 6:21
67:21,23 69:6 84:20	147.2,3 140.21	part 14:4 17:9,10,11	party 0.21 pass 130:20 131:2
87:10 109:15	p	22:3,8 24:2,20	passage 42:1 56:14
	<b>p</b> 26:12	-	57:14 59:3 62:7
110:23 112:2,17	<b>p.m.</b> 2:17 80:9,12	25:10 27:21,21 29:6	1
123:16 127:5 134:3	92:23 93:1 108:19	31:2 36:10 39:1,12	63:18,20 64:5
134:12,21 135:13	108:22 124:4,7	48:4 52:5,23 54:16	115:12 116:6
145:19	146:3,6 151:12,15	55:1 62:16,19 66:8	136:15 146:13
organic 54:13,16	package 57:18	66:13 69:2 77:8	passages 59:5 116:9
orientation 79:13	96:23 97:1,6,9,19	78:19 94:18 98:23	passivation 88:8,10
originate 57:1	pad 15:22 20:2,6,7,9	99:22 100:17 101:1	102:2,5,13,16,20,23
orthogonal 18:24	20:11,11,22 21:22	105:16,24 108:7	104:6,6 113:2
19:3	22:15,24 23:13,14	109:23 110:15	133:20,21 138:16
outcome 6:22	24:14 25:4,22 27:14	112:24 126:22	138:18 149:13
outcomes 123:5	27:14,16,23 28:15	127:6 133:23,24,25	patent 1:1,2,6,7,8
outlet 137:20,25	31:10,14 33:14 57:2	134:1 144:3	2:1,2,6,7,8,15 3:12
140:20	57:3,3,6 63:2,7,9,9	partes 11:6 12:16	4:3 6:10,13,15 7:6
outside 13:23 15:23	63:23,24 68:11,11	103:8	7:11 8:2 9:18,21
21:15,16 28:14	68:14 69:5,17 70:2	partial 45:19 46:9	10:8 11:5,6,8,19
30:14 35:7 37:24	70:19 72:11,14	47:9,13 50:8,25	12:2,18,19,21,24,24
79:10 112:22	74:14,19 75:2 80:19	51:3,17 55:6,10	12:25 13:1,12 22:3
133:17	81:8 87:13,15 133:7	partially 42:7 45:14	24:18 31:6,7 33:6
overcoat 67:9,12	133:13,17 134:8,24	47:13 50:1 55:7,12	39:9 42:17 48:3
75:3 142:19 143:12	pads 15:17,20 17:6	55:15,16 57:16 66:5	51:3,4 72:14,21
143:16 144:23	23:12 25:17 26:24	71:19	81:22 84:15,17 85:1
145:6 147:19	28:1,16,20,20 29:1	particle 56:7	87:2 93:4 94:11,12
149:14 150:10	35:4 63:22 67:24	particles 20:16	116:19 128:23
overlap 55:9	74:13 88:1,1 148:14	particular 11:22	130:1,13,16,22
overlapping 32:14	page 21:11 29:11	12:1 35:2 61:23	131:6,12,17 132:24
42:6 50:4,17	53:24	74:9 81:7 84:22	134:20
overnight 10:2	pages 1:25 96:17	88:14,15,18 94:3	patents 10:11 12:21
owner 1:8 2:8,15	panel 13:24 14:3,4	96:14 98:7,8 107:4	13:12 39:12 94:1
3:12 4:3 6:10 7:7,11	14:24	107:5 114:14	96:11 103:7
12:18	par 53:6	115:15 120:10	path 34:25 100:1
owners 12:19	paragraph 29:10	125:9,10,10,14	pattern 24:21 44:14
oxidation 68:1	1 2 1	127:23 129:15,20	46:25 61:4,6,7
oxide 43:3,17 44:19	41:23 48:15 56:14 56:16 64:12 67:18	132:10	patterned 43:20
46:5,13,23 47:6		particularly 61:19	44:7 54:14 90:13
49:3 67:25 69:12,18	67:20,22 69:5 71:13	92:10 103:12 113:4	patterning 45:1
69:21 70:2,3 74:6	72:7,17 73:3 91:11	parties 6:17,25	pause 48:6
75:2 95:10,21	107:16 118:16	153:14	penalty 152:5
101:12 109:5,7	143:20 144:2		
	Vitt Clicas D	L	

people 14:9 34:15	134:6 144:8	105:15 109:21	137:5,6,7,8,10
34:16 36:3,4,9	pieces 12:8 17:5,8	120:5 127:24 146:1	138:5 139:5 148:13
102:19	23:21 35:23 36:17	plug 139:25 140:14	148:15
peoples 36:16	37:6 113:21	plugged 136:21	pre 23:1
percent 50:18	pinhole 114:21	137:2,17 139:7,21	preexists 117:25
percentagewise 9:20	pinholes 106:19,19	139:22 140:4,12,16	preferred 23:6
10:7	106:24 109:4,19	140:22	63:15 72:24 84:12
perfectly 107:12	110:12,12,17,21	plugging 139:24	90:8,21,25 94:10
performance 65:1,5	111:25 112:6	plugs 140:13	106:1 115:24
65:21 101:15	pixel 35:10 37:19	plurality 142:18	premium 65:3
performing 62:4	38:2,4 58:1,23	point 9:4 34:20 35:3	prepare 12:14
period 95:11	67:14 70:4,8,12,23	39:22 54:6 59:5,25	prepared 11:17
periphery 74:13	70:24,25 71:2,10	70:9 76:19 77:11	40:10,14
perjury 152:5	74:22 79:1 83:10,21	132:10 142:3	preparing 13:8
permits 124:13	84:8 85:4,8,9,17	<b>pointed</b> 57:9 61:11	presence 53:9
persists 79:14	102:3,9,13,21,22	76:24	141:11 148:21
person 24:5 35:12	103:3,6,18,23	points 47:10,10	present 7:4 66:10
35:14,15,17 39:16	pixels 18:17 21:1	65:14 72:20	90:24 91:3 98:17
67:20,22,23 69:6	144:21	<b>pool</b> 12:10	105:25 106:5
74:24 87:10 89:3	<b>place</b> 6:17 40:17	portion 16:23,23	111:15 149:4
93:19 101:11	109:12,16 110:10	17:2,3 37:2,2 43:10	preserve 130:17,24
134:12 135:12	110:19 112:18	56:8,11 78:7,10,16	press 75:24 141:3
persons 84:19	134:23 144:13	100:4 104:22,22	presumably 89:6
<b>petition</b> 12:16 86:13	153:5	109:25 111:12,17	<b>presume</b> 53:7 77:23
petitioner 1:5 2:5	placed 20:22 93:16	<b>portions</b> 43:7 46:4	78:25
3:3 7:13 13:8 101:3	103:16 114:2	68:9 72:17 78:13,14	presumes 52:4,22
<b>ph.d.</b> 1:14 2:15 5:3	118:22 119:4,15	83:4 98:10 109:9	presuming 49:2
7:17 152:4,16	120:9,23 153:7	145:10	52:11
phenomena 140:10	placement 109:3,17	position 139:23	prevent 137:11
<b>photo</b> 46:25	111:4 113:1 129:23	possibilities 56:13	<b>prevented</b> 32:1 66:7
photoresist 54:12,16	1	possibility 107:1	<b>previous</b> 59:14,15
54:20,23 55:2	placing 7:25 11:4,10	<b>possible</b> 15:5 38:22	120:15 124:18
photosensitive	109:2 111:22	56:18 87:20 91:2	previously 8:2
54:13	112:10,13,19 113:4	92:7 93:24 106:3,11	primarily 13:19
physical 135:14	114:17,25 116:1	116:1,2,4,4 123:5	<b>prime</b> 33:5,9,15
physically 136:11	<b>plan</b> 26:15 34:9	138:25 142:2	<b>printed</b> 88:5 95:8
137:18 141:20	planar 61:2	<b>potential</b> 22:7 25:3	99:18 100:14,21
<b>physics</b> 36:5 140:8	planning 87:1	27:8 31:2 56:2	104:20 119:4,7
<b>pi</b> 26:7,8,11,20	plaza 2:16 3:7 4:8	65:19 136:7 137:19	121:25 123:20
pick 6:19	6:11	140:3 141:1	125:21 126:11
picture 77:16	please 6:16,24 7:5	potentials 28:13	127:1,9 128:11,18
107:18,19	7:15 9:25 11:16	power 15:22 20:1,6	135:24 136:2,18
pictures 107:21	39:22 42:10 59:5	20:7,9,12 23:12	<b>prior</b> 11:18,24 12:8
piece 17:9,9 25:11	60:2 76:19 81:17	27:13 31:14 63:22	15:15 35:23 36:17
35:21 55:18 59:25	98:16 103:13	103:22 136:20	39:9 40:14 47:23

55.10 05.00 110.00	numerational 20.14	103:6,24 106:9,22	rating 151:6
55:18 85:22 112:23	proportional 38:14	,	rationale 148:24
113:20,25 118:13	proposed 51:20	108:1,2 113:9 119:7	
131:8,15 133:4	proposing 14:18	149:12,21	react 110:8
153:7	15:23 51:19	puts 24:5	reacting 114:22
priority 113:6	protect 134:4	<b>putting</b> 53:12 68:18	read 12:4 16:4 34:15
private 6:19	protected 54:23	89:20 102:22 103:1	35:17 43:11 44:17
<b>probably</b> 8:19 19:8	110:3 111:20	105:19 113:15	52:25 57:13 58:6
problem 109:19	133:21 134:5	150:1	62:22 63:18 76:12
131:10,13	protection 74:23	q	94:22,23 105:24
problems 14:15	105:1 133:10,10	question 9:24 11:18	107:16 108:5
33:13 101:24	143:11	12:3 27:11 39:22	117:12 125:15
<b>proceed</b> 6:25 9:10	protective 67:8,12	40:8,11,18 41:8	127:21 143:6 152:5
proceeding 87:3	75:3 101:12 103:16	48:14 49:6 51:15	reading 12:2 53:7
112:24	103:19,20 104:18	53:6,25 54:2,4	55:11 64:13 93:25
proceedings 8:4	111:12 142:19	57:24 59:14,15	reads 31:8,21 32:3
15:1 39:2,23 55:19	147:19 149:14	70:17 75:14 77:3,19	37:18 42:13,19
131:5 153:4,6,8	protects 46:2 54:17	78:20 87:6 89:1	43:16,25 57:4 62:7
process 36:1,13	<b>provide</b> 15:14 20:23	92:16 104:12 122:8	63:22 90:8,9,17,23
45:11 46:11 48:24	39:13 74:12 76:13	•	95:6 109:23 115:6
54:15 57:13 71:2	82:10 87:12 105:4	132:11 139:11	121:24 142:17
73:23 75:1 81:15	122:15 129:22	146:15 147:3,9	144:16
90:12 92:12 101:15	130:9 131:9 142:6	149:11	really 54:1
144:12 145:18	provided 11:24 12:8	questioning 53:9	reapply 144:14
149:24	87:14,15 95:9	79:21 138:24	reason 97:2,4 147:1
processing 87:25	125:22 128:12	questions 8:7,21 9:5	147:21
101:25	136:17	9:11,13,19 10:4,7	reasonable 16:24
product 65:3 88:18	provides 20:7 27:23	16:8 51:12 75:20,22	reasoning 50:7
products 88:15	30:22 35:5 84:17	75:25 76:1,2,8,10	reasons 88:17
professor 7:23 9:9	122:19 123:8	86:7,12 93:5 100:25	101:10,14,15,16,22
11:4 25:19 32:20	143:10	100:25 101:21	102:25 113:16
33:13 38:21 41:23	providing 15:15	116:18 117:18	recall 39:11 40:18
46:18 48:2 66:19	126:12 127:2	151:9,10	62:21 81:17,18,21
67:18 75:16 80:14	proximately 44:1	quite 47:17 58:13	90:3 103:9 129:24
86:18 93:3 99:8	purpose 14:13 30:24	87:16 104:12	131:24 140:8
101:9 108:24	31:7 34:1 81:3 91:3	124:16	141:10,13,22,25
110:14 124:9 132:9	106:4 131:4 147:6	<b>quote</b> 67:12 118:18	142:2
136:19 139:2 141:6	purposes 34:7 85:19	118:20 119:11,12	receive 15:22 20:10
146:8 151:8	put 18:15 23:23	126:5,6 143:3	25:17 28:2 36:11
1	24:8 25:2,6 39:6,9	quoted 24:18	76:9
projection 29:14	39:24 43:20 44:5,13	quoting 111:10	received 9:20 10:8
propagate 19:3	-	r	28:7
propagates 19:23	46:20,23 48:7 49:17	raised 10:1	i
proper 40:18	53:1,11,12 63:7	range 91:2 106:4	receives 20:12 25:15
property 4:14	69:22,22,23 87:22	rates 10:13	25:22
150:20	87:23 88:7,11 89:5	14105 10.13	recess 41:10,15
	93:16,18 95:18,19		80:10,17 92:24

108:14,20 124:5	103:10 124:12	relating 86:13	removed 46:6 97:22
146:1,4	referring 39:8,13	relationship 27:13	
recites 118:11	58:7,9 63:16 66:8	32:21,24 120:12,24	99:23 100:5 142:4,7
recognize 93:11,14	79:1 82:1 91:18,20	121:18,21 123:19	removing 51:16
recognized 93:11,14	93:23 95:6 97:25	127:16,21 125:19	98:6 102:20 144:22
			145:4 147:25 148:2
recollection 81:25	98:20 104:21	relative 13:23 15:6	148:8
82:18 87:7	125:19 126:4,5,24	15:10,15 18:9 34:5	render 131:19
record 6:5,18 7:5	128:17 143:21	91:24 146:10	repeat 8:9 9:5
9:14 41:11,21 59:22	148:23	147:15 150:21	rephrase 78:17,22
60:1 67:2 80:9,12	refers 22:3 31:9	151:5 153:13	81:21 89:1
92:22,25 108:18,21	47:8 51:4,4 57:10	relatively 130:14,17	replaced 41:17
124:4,7 146:2,5	72:18 83:9 103:2	130:24	replicate 92:12
151:11 153:8	132:12	relay 119:17,22	reported 1:21 74:2
recorded 6:6	reframe 108:6	120:21,22	reporter 2:19 6:8
recording 6:16	refresh 86:20 87:6	relevance 40:7 41:3	7:15 153:2
records 101:7	regard 8:10 26:14	relevant 11:25	reports 10:11
rectangle 19:17,19	30:16 31:5 39:15	12:10 15:7 36:10	reposition 144:19
22:24 23:19	98:6 139:7,12	70:11 77:24 127:11	represent 78:18
recused 144:18	146:16,21	147:3	represented 10:21
reduce 14:24 16:9	regarding 10:2	reliability 68:12	10:23
16:14 73:11 102:18	region 16:10 17:20	109:6 111:6	request 6:10 12:18
103:20	17:22,25 20:3 21:4	reliable 68:2	12:20
reduced 15:24	21:5,14,18 29:3,13	relied 13:19	require 15:17 70:1
142:23	29:20 34:25 35:1,7	rely 8:9,15 80:22	118:12 136:15
reduces 65:9 130:1	35:8 54:19 63:10,10	97:24 106:14	researches 15:9
refer 8:3 13:2 16:5	63:10 74:9,19 78:6	relying 55:14,16,17	resembles 26:10
17:15 63:9 79:23	79:10,14 81:8 97:22	remain 43:21 46:3	resin 93:8,16 94:20
81:1 95:12 116:23	102:3 103:18 105:4	49:3,18	95:9,20 125:7,23
128:4	118:19 119:1,5,12	remaining 23:24	126:12 127:3
reference 8:1 13:18	119:14 132:5,13,13	49:21	128:13
29:18 47:4 49:7	132:23 133:13,17	remains 54:17	resist 68:1
52:6 64:13 82:21,25	134:8,15,15,19,24	remember 13:1	resistance 15:25
86:24 90:25 95:4,11	134:25 143:10	80:20 86:18 103:14	56:3 65:10,11,12,24
96:13 106:1 109:9	145:23 147:25	105:16 146:12	83:20 98:17 99:17
113:8,9 127:10	regions 15:21 17:5	151:5	99:21 100:4,6,15,18
131:16	24:11,12,17,25	removal 51:16	100:21 147:2
references 11:23,25	29:14 35:4 44:20	142:22 144:17	148:16,22
112:23 113:22	47:1,3 53:11 54:22	145:13	resistant 111:24
133:4	55:9 64:3,9,10	remove 38:23 46:11	resisted 83:17
referred 13:4 14:6	89:13	46:12 49:19 50:9,10	resistive 147:5
17:8 18:12 20:8	rehearing 12:19,20	50:14,23 51:2 98:10	148:9
41:24 42:1 45:13	relate 8:22 103:13	103:24 106:24	respect 21:8,9,11,12
46:10 57:2,3 60:4	120:14	144:14,19,25	77:22 88:23 118:7
61:9 67:5 72:13	related 6:21 9:12,17	145:12,22	129:23
73:2 81:19 83:8	10:7 39:22 103:3,5	, <del>-</del>	
		Conorting Company	<u> </u>

respectively 31:23	rise 26:17	schlitter 3:14 7:10	134:19,25 135:23
respond 76:13	risk 104:14 144:16	7:10 8:18 9:7 86:16	136:2 144:20
response 10:10	144:21	science 36:5,6	section 21:6,7 32:3
12:17 39:10 40:11	risks 145:14	scope 91:15 101:19	33:4,21 34:2,11
41:7 50:16 131:23	riverside 4:8	101:20	35:2,8 42:12 44:1
į –	robinson 4:14	scott 4:21 6:7 41:17	58:6 61:2 84:3,4
responses 100:16			1
responsive 39:4	robust 109:18	seal 14:6,9,10,11,14	90:23 91:11 114:15
restrain 101:18	run 18:2 59:17	14:15 16:10 17:12	125:15 127:20,25
restricted 91:1 92:2	running 56:20	17:22,23,25 20:21	136:10 143:2,8
92:6 106:2	100:23 144:21	20:21,22 21:9,12,13	sectional 61:3
result 66:3 68:2	runs 31:19 136:20	21:14,17 29:13,20	sections 34:5 42:16
91:15 112:4 113:2	148:11	35:7,8 37:8 97:13	43:5,24 52:25 109:1
135:1 142:21 147:2	S	130:8,15	119:23 123:3
148:18	sandwiched 47:21	sealant 14:19 16:14	see 9:4 11:1,13 15:8
results 111:6	satisfy 51:17	16:15 20:3 24:16,16	16:22 20:4,5 21:25
retained 151:14	save 34:3 77:10	29:3,5,7,12,15 30:3	22:1 25:20 26:3,4
revealed 123:7	saying 26:15 40:5,8	30:4,4,5,14 37:25	28:22 29:19 30:6,9
reverse 110:24	48:10 52:16 70:5,18	38:1,2 60:11,11,12	30:11 33:10,17,18
112:18	72:21 95:23 107:25	77:23,24,25 78:18	33:21 37:13 50:5
reversing 105:3	115:18 116:15,17	78:24 79:4,10,10	52:18 57:1,17 58:18
112:3	141:10	96:19 119:9,11,15	59:11 60:6,9 61:15
review 11:6 12:16	says 31:11 33:6	128:24 129:16,23	62:10 63:12 64:13
12:21 39:10 40:10	42:23 44:18 45:15	130:21 131:2	64:16 66:18 67:2
41:6 108:15 127:25	56:4 57:14 60:8	133:17,20 134:15	71:7,9 73:14 74:20
reviewed 12:8,15,15	61:20 62:2 66:5	135:1 144:24,25	75:11,12 76:12,14
12:16,17,18 112:24	67:22 72:14 91:12	146:17 149:7	76:16 77:2 78:3,12
ridge 4:15	106:10 108:1 119:9	sealing 29:8 142:23	79:3 82:21,24 89:8
right 12:12 19:6,21	119:11 122:10	142:25 143:18	93:7,9 95:16 97:21
19:25 21:4,8,10,16	125:19 126:5	144:6,9,18,19	97:25 112:3 114:1
21:17 24:3,6 26:5	128:21 135:23	seals 14:5 79:5	121:20 123:5,16
26:18 27:7,24 28:1	143:13	search 11:18	124:20 127:5
29:21 33:11 40:22	scan 55:3 60:20	second 17:9 23:15	128:19,20,21,25
42:3 43:5 46:1 48:9	62:13 68:15 71:20	31:13 59:2 63:25	132:12 135:10,11
48:17 49:16 51:1,7	71:25 72:22 81:25	67:15 72:2 83:23	140:18 142:3
53:19 54:8 60:6,10	i	85:24 91:17 118:11	147:21
61:13 64:22 76:25	scanning 18:23,23	118:14,19,22,23	seeing 79:22
77:2,17,20 78:7	19:2 31:22 32:9	119:1,5,6,11,12,13	seen 33:20,23
81:3 82:19 84:6	42:15,20,23,25 43:7	119:14,20,24 121:6	segment 31:12 37:4
85:5 86:10,23 97:19	43:9,15,19 44:4,11	121:7,10,24 122:11	sel 10:6,17,18,21,23
107:23 108:13	46:14,20 53:4,14,21	122:13,14,15,20,23	selected 11:25 12:9
116:22 126:7	58:20,21 61:6 62:4	123:19,20,22,24	145:21
129:13,15 131:22	62:9 68:24 74:3,16	124:13,21,23 125:4	selecting 142:24
141:17 144:13,21	81:11,19,23 82:1,20	127:23 128:24	selection 12:11
ripped 146:22	82:25 83:7,12,15	129:3 130:5 132:5	91:20 94:6 143:17
147:13		132:13,13,23 134:5	72.20 7 110 2 15117
21/120	Varitary Chicago F		

selectively 53:11	36:18 39:8 51:4	110:10 126:19	<b>skilled</b> 15:3 34:19
sell 65:3	52:5,25 55:1 56:4	129:10,21	34:22 35:12 37:11
semiconductor 1:7	59:9 61:18 62:12,22	sic 142:10	39:16 45:15 47:14
2:7 6:14 64:18	62:23 63:15 64:8	side 18:25 19:2,3	47:18 55:20 62:10
sense 74:14	65:13 66:3,21 67:21	21:24 24:16 25:18	66:23 69:1,8 70:21
sensible 75:22	67:24 68:4,10,20,23	30:10 31:12,13,16	73:14 74:20,24
sensitive 6:18			1
	69:2 70:19,23 71:10	31:17 56:20,20 60:6	85:11 88:12 89:3
sent 137:24	72:25 73:4 74:2,15	60:9,9 77:3 78:4,6,7	92:8 93:19 94:4,14
sentence 43:15	74:21 76:23 81:1,4	78:15,18 97:19,20	96:3 101:11 106:14
91:12,17,25 92:5	81:8,20,22 82:15,16	98:18,21,23,24	107:6 127:7 133:6
95:15 106:8 119:19	83:3,7,19 84:7	148:14,14	skipping 92:16
120:11,13,18,21	87:11,13,25 88:1	sides 15:14,16,16,18	slater 4:21 6:7 41:17
121:17 123:1	131:12,14,18,21,25	15:18 19:1 78:9,13	slice 33:8,15,22
124:17 126:4 127:4	132:2,4,22 133:1,2	78:14	slit 134:1
127:12,13 128:5,7	133:3,4,5,7 134:18	signal 18:11,11,22	small 22:15 84:2
128:15	134:22 141:9 142:9	20:10,12,14 25:17	socket 136:21,23
sentences 95:14	142:11,12,13,16	25:22 27:23 28:2,7	137:3,15 139:6,7
119:21	143:8,13 147:24	30:22 34:25 56:10	solid 29:24
separate 28:13,13	148:7,8,11,18,19,25	65:13,18 100:1	solutions 6:9
59:17	shielded 82:22	signals 15:14,16,22	solve 109:18 131:10
separated 48:17	<b>short</b> 19:1,11 24:15	74:12	somebody 112:2
51:22 55:21,25	41:9	signed 11:14	someplace 54:7,8
81:15 129:18	shorten 86:5	significantly 10:14	somewhat 9:15
sequence 48:18 58:2	shorter 31:12,16	silicon 43:3,3,17,17	21:13 26:21
67:16 73:20 75:1	shorthand 2:19	44:18,19 46:13,13	sorry 13:7 14:20
89:20 92:13 97:11	153:1,9	67:12 115:4,5,6,7,9	23:8 25:19 32:6
117:19,20 118:17	<b>show</b> 36:19 38:4	151:3	56:15 65:10 79:16
119:17,22,25 120:2	50:20,21 55:19 67:8	similar 89:19 93:5	85:18 108:2 113:10
122:3 123:4	94:12	96:5 131:9	115:12 118:23
series 10:5	<b>showing</b> 107:20	similarly 118:9,17	134:9 144:1 148:20
serve 14:12 147:5	142:1	simply 71:6 92:12	sort 43:22 136:16,16
serves 143:14	<b>shown</b> 17:14 20:18	107:9	<b>sound</b> 88:19
set 43:10 60:7 106:6	21:23 26:24 31:11	single 37:12 60:13	source 136:16
136:19,22,24 137:1	33:10,16 37:9,9,11	68:21 73:2 81:9	south 3:16 4:8
137:8,9,14 138:10	37:21 42:23 61:8,14	91:6	<b>space</b> 34:3 97:18
139:3,4,5,8,14,19	63:14 68:8 79:15,17	sit 61:12	spacing 18:17
153:5	79:19,25 80:2 87:8	size 15:6,10,11 38:6	130:18,24 131:15
setting 37:3	93:18 98:5 110:5	38:10	134:12
sgibson 3:10	125:4 129:8,21	skill 34:15 67:21,23	spattering 90:12
<b>shape</b> 94:25	133:7,17 142:18	69:6 76:6 84:20	speaking 19:21 21:3
<b>shaped</b> 33:24 34:1	shows 33:5 35:7,9	87:10 109:15	<b>specific</b> 15:24 16:8
shiba 7:25 8:4 12:24	36:21,21,23 43:19	110:23 112:3,17	16:13 18:16 21:19
13:18,21,22 14:1,18	58:22 61:1 64:6	123:16 127:5 134:3	48:19 58:8 73:12,13
14:21 15:12,13,23	67:3 79:8 86:21	134:13,21 135:13	73:18 81:2 82:9,16
16:4,6,9,17 33:2	93:22,23 107:13	145:19	84:21,21,22 85:13
	TI '( (C1)'	<u> </u>	1

85:16,17 88:17	statements 15:3	stripped 48:5	118:2 120:7,8,11,19
90:15 97:11 103:13	121:20 142:8	structure 16:2 31:24	120:21,24 121:2,3,5
105:22 107:15,21	states 1:1 2:1	33:9 38:2 41:25	121:8,11,13 130:18
107:24 108:24	stems 67:1	42:5 43:3,17 45:17	130:19,25,25
123:17 124:25	step 23:16 24:10,10	48:16 49:22,25 50:5	131:19 144:22
i .	31:22 42:14 44:3	50:14 52:9 60:17	145:3,7,15,16
132:21 134:17		62:14 64:7,11 65:8	146:10,11 147:14
<b>specifically</b> 74:15 82:15 83:9 85:19	45:1,8,8,10,10 46:1	65:9,24,24 66:2,10	140.10,11 147.14
	46:2,25 48:5 49:1	66:24 67:17 68:3	substrates 14:5
108:3 115:21 116:7	49:13,15 52:11,12		
specification 44:18	53:17,23,23 54:11	69:5,9,25 70:16	16:11,16 17:13 20:20 21:20 41:5
71:8 77:8	54:15 60:20 61:4,21	71:14 72:11 73:2,16	
specifications 12:7	62:4,8 64:1,4 70:1,5	74:8,10,18 85:14	79:4 130:21 131:3
53:8 59:4 66:22	98:3 134:14 135:3	89:18,19 96:19,20	131:15
70:9 71:11 72:18	145:5	97:20 100:13,15	subsurface 90:11
76:12 84:17 94:1,22	steps 14:4 16:19	107:10 109:18	sued 10:19,21
125:14 127:11,17	49:23 53:2 64:15,20	124:13 125:11	sufficiently 104:2
127:18,21	64:21,25 65:6 66:3	141:9,19 142:12,14 144:7 145:23 148:1	suggest 108:7
specifics 41:6	66:12,16 70:15,20		suggested 91:24
specify 67:11	70:22,22 71:4,6	148:2,4	suggesting 52:13
spend 32:20	73:10 101:25	structures 35:21,22	suggestion 83:6
spent 47:17	steptoe 3:13 7:10	51:19,20 60:19 68:8	107:24 132:4,21
spoken 131:21	steptoe.com 3:19	72:22 73:8,15 76:5	134:17
spot 98:7	sticking 149:16	81:5 84:21 85:21	suggests 88:22 89:2
square 19:8 25:10	150:23,24	97:1 113:14 131:9	suite 2:16 3:7,16 4:8
sschlitter 3:19	stop 80:6	131:10 143:14	4:15 6:11
stacked 38:21 39:17	stopping 24:15	150:2,3	sukegawa 12:24
40:24 41:1	stops 19:11 138:6	studied 33:2 40:2,12	13:21 14:20 55:18
stan 7:10,12	storage 37:13,18,20	50:16	80:22 82:8 86:1,13
stanley 3:5,14	37:21 38:3,4,7,10	subject 12:5 40:9	86:18,20,24 87:2,12
start 13:18 18:4,24	38:19 42:24 58:5	76:7 110:7 123:1	88:22 89:2,7,23,25
19:3 26:16 51:9	straight 33:22	133:16,24 subparagraph	90:3,5 91:15 92:5 93:23 96:5,8,16
52:19 102:19 120:17 145:9	straightforward 150:4	121:23	97:23 104:7,14,15
started 123:15	street 3:16	subscribed 153:16	105:5,12,18,20,23
started 123.13 starting 24:14 26:9	streets 137:22	subscript 37:16	105:3,12,18,20,23
30:21 37:5 105:25	strength 16:10,15	substance 22:2	107:24 109:1,10,11
109:22 139:20	102:12 104:1	substitute 128:3	109:11,17 111:21
starts 18:14 19:22	stresses 149:12,23	substrate 17:11,18	112:12,22 113:7,8
117:7	150:2,8,16	17:19 20:8,17,18,19	113:11,17,19 114:7
state 52:21 139:8,12	strike 24:3 25:9,21	20:25 22:8,9,12,12	115:13,20,25 126:8
141:4 152:13 153:2	119:10 122:8 128:9	22:13 23:23 24:9	126:15 127:10,14
statement 52:19,21	130:15 134:9	42:25 43:6,8,23	127:15 128:5,7,16
56:24 124:17,18	146:20	45:10 82:24,24	128:20,22 133:2,4
147:18	striping 49:5,9	90:12 112:16,20	134:2,9,22 138:16
11/.10	201.2pmg 17.5,5	117:17,21,24,24	10.10,5,5,22,100,10
	1	11111921921921	<u> </u>

[sum - three] Page 28

sum 84:5	talk 55:17 101:3	television 136:19,22	143:7 145:25
summary 16:5	143:20	137:1,8,9,23 138:2	150:19 151:8
supplied 140:20	talked 131:24 139:4	138:10,14	theoretically 33:10
148:15	talking 21:10 35:11	tell 11:16 47:5 58:15	thereof 153:11
<b>supply</b> 20:1,6,9	42:9 52:8 80:17	59:1 77:4,13 79:12	thermal 149:24
23:12 27:14 31:14	84:15 104:5 110:15	83:2 142:11	thick 97:3,5,7
63:22 137:5,6	114:25 115:13	telling 51:13	150:15
support 47:12 52:16	146:9	tells 55:1 122:4,4	thickness 38:9,12,15
57:17 59:22 60:1	talks 43:5 61:18	temperature 45:8	38:18 83:24 90:11
supported 56:24	135:7,8	term 9:1 27:5	90:19 148:19,20
supporting 108:5	tantalum 82:12 91:7	terminal 15:21 17:5	thin 148:22
sure 8:15 9:22 10:3	tape 80:8,12 96:23	21:5,14 34:25 35:3	thing 64:8 65:2
25:8 26:10 59:8	96:25 97:6,9,19	65:16 78:7,10,12,14	111:2 119:18
60:3 91:22 92:21	124:3,7	78:16 88:1 95:3	things 16:3 22:15
94:23 99:13 108:10	taught 68:3 89:7	109:24 111:17	30:24 31:1 45:7
108:16 113:6,9	111:3 142:12	134:7,24	65:23 76:11
114:5	teach 49:24,25 50:2	terminates 19:6	think 9:5 13:19 22:4
surface 16:13 20:25	116:9 131:18	35:6	22:18,19 23:25
46:3 109:14 111:2	teaches 65:13	terminology 27:3	28:23 29:10 34:21
111:13 113:1	106:18 111:22	terms 35:16,25	39:4 40:1,11 42:1
surrounded 18:5	115:25 142:13	104:2	53:16,22 56:24 57:2
surrounds 26:8	147:24 148:7	testified 7:19 21:22	59:10 60:25 62:17
switch 136:24 137:7	teaching 35:20	62:17 71:16 113:12	62:19 70:17 72:6,8
137:11,13,21,21	45:23 62:23 66:21	testify 39:24 98:13	72:13 76:8 79:15,16
138:1,3,6,12,14,21	68:10,20 69:2 71:10	testifying 75:17	79:17 86:2,6,12
139:8,11,13,13,16	73:4,5,7,12,14,16,18	153:7	87:7 88:3 96:18
139:18,20 140:25	74:17 81:4 82:16	testimony 8:10	99:10,11 100:15
141:3	84:19,20,24 85:2,11	32:11 39:14 62:21	101:6 103:10,15
switching 23:11	85:20 89:8,23	71:12,21 72:5,9	105:24 110:18
91:14,18,23 92:9	105:23 109:2,11,12	75:18 76:15 80:14	113:16 114:9
system 101:16	110:18 115:22	86:4 91:23 92:8,17	116:21 117:12
138:19	132:3,21 134:17,22	100:25 148:17	124:1,9 126:16,22
szuladzinski 4:22	142:15 143:8	151:12 152:8	129:9 132:9 135:7
41:16,20	teachings 34:1	testing 104:21	145:25 149:10
t	35:19 106:16	text 59:4 107:17,23	150:24
t20 4:15	109:17	125:13 126:24	thinks 34:18
take 6:17 35:20 41:9	tear 145:1	tft 42:21 43:2 67:13	third 23:13 63:23
48:8,13 86:9 87:24	technical 8:11,16,25	tfts 23:10 62:2	119:24 135:19,19
92:19 108:9,14	9:16 101:10,14,22	thank 7:3,14 9:8	thorough 40:2
109:15 110:23	102:25	10:3 11:2,3 19:12 21:2 26:6 52:3	thought 10:1 12:9
112:2,17 119:21	technically 38:22	86:17 87:4 91:22	66:22 72:12 100:24 three 12:21 15:16
146:1	technique 43:23		18:20 34:4 37:15
taken 2:15 34:11	technique 43:23	96:15 100:8,11,24 105:2 112:21	39:12,14 62:11 74:1
153:4	technology 30.11	132:17 135:5 141:7	•
		152.17 155.5 141.7	74:2 85:7,12 112:11

[three - understand] Page 29

121:9	135:19	truth 75:23 76:3,20	87:24 88:2 89:15,24
time 7:4 13:16 41:12	touching 27:9 47:10	try 33:19 34:6 38:4	93:15,24 94:9 99:25
43:12 47:17 50:9	135:17 136:2,11	39:10 40:16 68:13	100:22 102:25
59:6 63:2 66:23	trace 34:24	76:22 94:20 114:5	108:4 112:14
77:10 92:15 97:15	trademark 1:1 2:1	123:4,5 145:12	116:11,11,12
108:5,9 112:25	trail 1:2 2:2	trying 15:5 23:25	119:18,22 120:1,2
113:5 151:15 153:5	training 36:11	32:21 37:24 42:17	120:13 121:16
times 18:20 69:19	transcribed 153:10	49:24,25 50:5,13,23	123:5 130:21 131:3
81:14 94:8,23	transcript 152:6	51:11,18 75:18,23	131:15 135:12,15
113:13	transcription	78:1 93:13 101:20	135:16 136:11
tin 67:25 69:12,17	153:11	121:14 138:22,23	140:1,5,8,11,13,17
69:21 70:2,3 74:6	transcripts 8:19	138:25 148:7	140:24 141:12,12
75:2 95:10,20	transfer 22:4	tuesday 1:16 2:18	141:19,19 149:8
101:12 109:5,7	transfers 22:7	6:1	151:6
125:23 126:13	transistors 35:9	tungsten 91:8	type 23:11
127:4 128:13	97:12	turn 67:18 117:6	typically 33:21
146:23 147:2,5	transparent 67:24	137:7 138:3,4,5,21	43:22 51:23
148:20	74:6 80:18 81:2	turned 136:24	
today 8:9,14,19 15:2	84:7 85:3,8,18	137:14	U 11.6
39:13 40:3,11	86:21 87:12,14,18	turning 107:23	u.s. 11:6
113:22	88:11 89:5 104:17	turns 8:20	unartful 78:21
today's 12:14	105:6 109:13,24	tv 136:24 137:14	unbroken 30:12,13
151:12	110:4,10,19 111:4	139:3,4,5,8,14,19,23	unclear 24:4
top 16:5 30:8 32:14	111:11,16,22 112:1	140:22	uncommon 33:18
38:22 40:25 43:6	112:19 114:11,21	twist 102:10,11	undercut 145:8,12
44:4 48:4,21 52:12	115:13 118:18,21	two 10:10,10,11	underlying 46:6
53:13,20 55:2 61:12	118:25 122:1,11,16	13:8,16 14:5 15:14	47:11 54:17 89:18
68:18 69:13 70:19	122:21,24 123:23	15:18 16:2,11,16	106:20 109:6
71:17 72:1 75:2	124:14,21 125:5	17:5,8 18:2 20:20	110:22 112:6
80:19 86:22 87:7,17	127:24 131:22,25	21:20 31:24 32:4	133:14 145:21
87:21 88:7 89:4,5	132:1,4,22 133:8,11	34:4 38:13,16,21	150:10
94:19 95:21 102:22	133:15 134:18,23	39:17,19 40:25	underneath 110:16
103:1,2,19,23,25	135:25 136:5,9	41:25 42:2,5,8,17	130:21 131:2
105:7,19,21 106:9	148:25	45:6 47:16,19,19,21	undersigned 153:1
108:2,2 109:16	transverse 14:8	48:6,7,16,16 49:25	understand 12:5
110:20 111:1,5	83:24 148:19,20	50:3,17 51:6,22	32:21 33:1 35:12,18
112:5,10 113:4	transversing 84:1	52:5,13,17,22 55:8	35:19 36:17 42:17
114:2,11,17 115:1,1	traverse 26:17,18	55:9,21,22,24 56:1	50:7,13,23 51:1,11
115:16,17,20	tried 12:4 14:9,14	56:2,4,19 57:15,15	51:18 53:15 68:13
116:10 129:17	39:6 40:24 49:22	62:14 64:7,11 66:1	71:12,21 72:8 75:18 76:4 77:18 82:14
145:20,21 149:15	50:7 94:13	68:3,8,11,14 69:4,9	88:21 91:23 93:13
topic 52:20 86:5	trouble 79:22	71:14 72:20 73:8,17	
total 151:13	true 67:3 104:14	74:8,10,18 78:13,13	99:8,10,11 101:20 104:12 114:23
touch 46:8,16 52:1	125:12 131:11	79:3 81:5,14,14	
87:22 89:14 135:15	152:9 153:8	85:13 87:17,20,20	115:18 116:15,16
	1	L	L

126:18 127:16	veritext 4:21,22 6:9	walls 122:22 123:13	waslaning 145.14
134:13 139:10	151:14	want 10:15 13:2	weakening 145:14 weekend 13:17
understanding 8:25	versa 88:9	16:4 29:6 39:7,8	
32:23 66:19,20	versus 6:13 89:22	1	weigh 74:25
•		46:5 47:1,3,5,6	weighed 75:9
71:22 76:5 86:11,14	vertical 104:23	49:18 54:6 55:21	weird 140:7
131:4	122:22 123:12,12	59:25 68:13 72:20	wet 54:21
understood 66:23	vga 18:19,20	74:8,10,25 77:7,10	whereof 153:15
unevenness 130:8	vice 88:9	80:25 83:4 86:3	whichever 88:13
unique 121:21 125:2	video 6:6,16 41:13	89:16 98:9 99:13	whispers 6:19
united 1:1 2:1	124:2	101:11,18 103:13	wide 14:16
unknowns 150:11	videographer 6:4	103:21 107:17	width 14:8 16:9,14
unusual 9:15	7:3,14 41:11,18,20	108:8 116:18	wire 111:15 137:2
<b>upper</b> 16:23 17:10	80:8,11 92:22,25	121:18 142:5,7	149:5,6,19,21,22
17:11 18:3 32:5	108:18,21 124:3,6	146:15	wired 31:14
38:23 44:8,15 46:7	146:2,5 151:11	wanted 49:20 66:18	wires 100:22 140:11
46:15 47:9 51:25	videographers 4:20	94:2 96:1 113:19	149:8
55:8 68:24 69:11	videotaped 1:14	124:16	wiring 14:18 15:24
71:17 72:2 86:22	2:14	wants 45:12,16	16:2,12 17:4,12
89:13 91:5,9 97:21	view 19:16 26:16	87:24 132:9	20:13,14 22:25
98:7 99:22 104:16	34:9 61:2,3 68:10	waste 14:16 15:4	23:13,15,16,20 25:4
105:7 110:1 111:14	131:18 133:1,2,4,5	wasted 14:11	25:25 26:7 27:19,20
113:1 114:1	134:1,9,11,22	wasting 59:6	27:22 28:5 31:9,11
upward 26:17	virginia 4:16	watanabe 12:25	31:15,18,18,21 32:1
use 8:18,24 24:9	visualization 106:14	13:21 17:16 131:16	32:5,17 37:5 41:25
34:16 62:14 68:11	visualize 49:22	131:18 133:2,5	42:13 43:11 44:9
73:21,22 74:8 77:21	volt 27:17,18 28:11	134:11,22	53:3,4 56:6 57:5,11
83:10,20 85:12 86:4	140:3,5,6	wavelengths 53:24	61:8,21 62:3,8
86:12 100:9 116:11	voltage 28:4,6,8,21	way 6:23 16:13 30:5	63:23,25 64:1 65:8
117:13 130:23	102:8,18 103:4,6	30:20 40:25 48:10	65:17,23 66:2,6
133:1,2 148:10	136:17 137:24	48:11,11 51:10,11	68:6 71:14 72:19
user 14:19	140:1,2,2,14,15,17	52:14 63:3,7 65:18	73:13,18,19 74:11
uses 104:15 130:14	140:18,20,24	67:21 69:24 80:3	74:15 86:23 90:14
130:16	voltages 103:22	93:16,21,21,22,23	90:16 91:4,5,9,10
usually 34:8	volts 25:3,4,5,6,7,14	94:9 101:19 106:24	97:21 99:4,19,23
utilized 131:16	28:9,10,11,12,12,12	121:21 125:2,10	104:16 105:8,21
utilizes 85:17,18	137:25	129:14 134:7 145:6	111:19 130:10
v	<b>volume</b> 1:17 2:15	145:14 148:7 150:9	133:18,19,21,23
valid 88:14,19	5:3 152:17	ways 24:20 87:20	141:12,15 142:17
value 143:8	vs 1:6 2:6	88:2 93:24 94:14,15	142:22 143:11,16
variations 91:13	w	121:22	144:17,22 145:10
various 91:13		we've 129:10	146:16,20,21,22
1	wall 136:21,23 137:3,10,15 139:6,7	weak 148:5	147:1,4 148:10,12
106:3,10 115:25	, , ,	weaken 143:15	148:15 149:1
verifying 33:13	139:22 140:13,22	148:1,2	wirings 27:22 28:3,3
		-	28:4 43:9 59:17
L		1	1 12:2 22:41

[wirings - zero] Page 31

62:16 81:4 91:19,21	<b>x2</b> 18:10,12 24:13		
97:12 99:25 110:2	<b>x3</b> 18:10,13 24:13		
134:4 141:12 145:8	<b>x4</b> 18:10,13 24:13		
wish 40:14 88:15,15	x5 18:10		
witness 5:2 22:21	<b>xi</b> 23:17 31:23 42:15		
24:8 27:2 34:14	61:22 64:2		
39:1,21 40:8 41:4	$\mathbf{y}$		
44:23 45:6 49:9	y 68:16 82:17		
51:9 54:11 56:23	yeah 29:8 33:18		
61:18 64:24 68:20	53:20 105:1 151:1		
75:5,11 92:19 98:20	years 36:15		
99:25 101:6 104:5	yesterday 8:7,14,19		
105:10 122:7,19	9:6,13 10:1 15:1,2		
125:9 129:20 130:7	36:13 47:15,17		
132:18 133:1 137:1	55:17 85:25 86:13		
137:17 139:10	87:17 93:6 94:17		
146:25 147:17	95:23 96:19 117:19		
149:10 153:15	136:13 138:15		
witnesses 153:6	yesterday's 8:10		
wonder 34:10 77:14	86:4 92:17		
word 47:13 55:14	vield 15:25 32:2		
55:16 117:13,20	64:25 65:5 66:7		
121:15 122:25	73:11		
123:2,6,7 125:1	yj 31:22 42:15,20,24		
127:12	43:1,4 58:20 62:4		
words 29:13 47:23			
126:3 127:16	Z		
135:18 143:2	zero 140:2,5,6,15		
work 10:8,16 13:15			
52:14 151:1			
worked 10:18 41:4			
workplace 36:12			
works 33:20			
worlds 65:7			
worry 99:10			
<b>worth</b> 65:1			
worthy 131:6			
writing 96:11			
wrote 10:10			
X			
x 19:4 68:17 72:15			
82:17			
<b>x1</b> 18:9,11,12,15			
19:6 24:13			