

Inter Partes Review of U.S. Patent No. 7,923,311

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**IN THE UNITED STATES PATENT & TRADEMARK OFFICE
BEFORE THE PATENT TRIAL AND APPEAL BOARD**

IN RE U.S. PATENT NO. 7,923,311

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LABORATORY CO., LTD.

TITLE : ELECTRO-OPTICAL DEVICE
AND THIN FILM TRANSISTOR
AND METHOD FOR FORMING
THE SAME

**REQUEST FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 7,923,311
UNDER 35 U.S.C. §§ 311-319 AND 37 C.F.R. § 42.100 ET SEQ.**

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1001. U.S. Patent No. 7,923,311 to Zhang, et al.
1002. Japanese Patent No. H1-144682 to Noguchi (including translation).
1003. U.S. Patent No. 5,270,567 to Mori, et al.
1004. (Intentionally Left Blank)
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1006. Japanese Patent No. H2-234125 to Taniguchi, et al. (including translation).
1007. Japanese Patent No. H1-180523 to Matsuzaki, et al. (including translation).
1008. U.S. Patent No. 5,198,694 to Kwasnick, et al.
1009. Peter Van Zant, *Microchip Fabrication: A Practical Guide to Semiconductor Processing*, pp., 222–28, 298 (2nd ed. 1990).
1010. U.S. Patent No. 6,756,258 to Zhang, et al.
1011. Action Closing Prosecution, U.S. Patent No. 6,756,258, Filed May 14, 2008.
1012. *Semiconductor Energy Lab. Co. v. Chi Mei Optoelectronics Corp.*, 531 F. Supp. 2d 1084 (N.D. Cal. 2007).
1013. Prosecution history of U.S. Application Serial No. 11/898,833, which issued as U.S. Patent No. 7,923,311.
1014. Declaration of Jerzy Kanicki, D.Sc.
1015. Joint Claim Construction and Prehearing Statement, *Semiconductor Energy Laboratory Co., Ltd. v. ChiMei Innolux Corp., et al.*, SACV12-0021-JST (C.D. Cal.), filed Nov. 12, 2012.

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