United States Patent [19]

Koden

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4,862,234

[45] Date of Patent:

Aug. 29, 1989

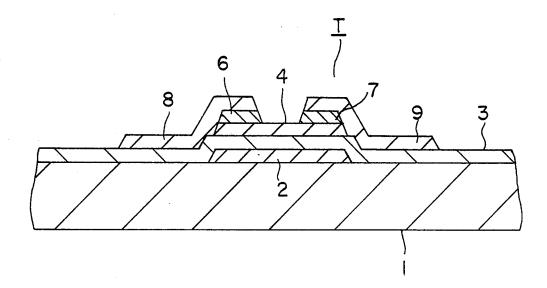
[54] THIN-FILM TRANSISTOR		
[75]	Inventor:	Mitsuhiro Koden, Nara-shi, Japan
[73]	Assignee:	Sharp Kabushiki Kaisha, Osaka, Japan
[21]	Appl. No.:	125,961
[22]	Filed:	Nov. 27, 1987
[30] Foreign Application Priority Data		
Nov. 29, 1986 [JP] Japan		
[58]	Field of Sea	arch 357/23.7, 4, 2
[56] References Cited		
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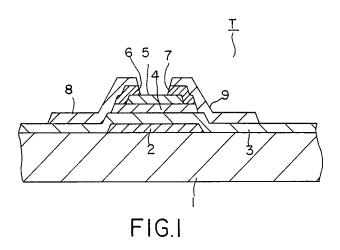
Primary Examiner—Martin H. Edlow Attorney, Agent, or Firm—Irell & Manella

[57] ABSTRACT

A thin-film transistor comprising an insulating substrate; an opaque metal gate electrode disposed on a portion of said insulating substrate; a gate insulating layer disposed on said insulating substrate including said gate electrode; and a-Si semiconductor film disposed on the portion of said gate insulating layer, said a-Si semiconductor film having been formed to attain self-alignment with respect to said gate electrode; a-Si contact films constituting source and drain regions, respectively, with a gap therebetween disposed on said a-Si semiconductor film, the outer end of each of said contact films being formed to attain self-alignment with respect to said gate electrode; source and drain electrodes, respectively, disposed on said source and drain regions, the thickness of each of said a-Si semiconductor film and said a-Si contact film being 100 Å or more and the total amount of thicknesses thereof being 1,000 Å or

3 Claims, 6 Drawing Sheets





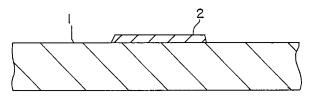
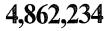
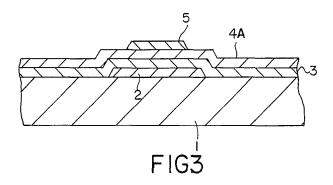
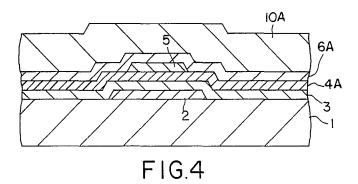


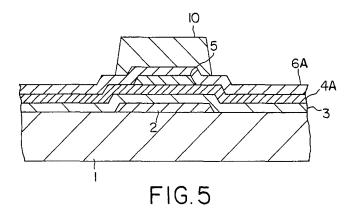
FIG.2

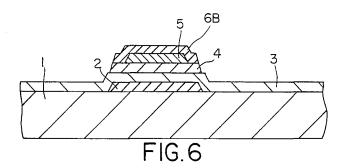


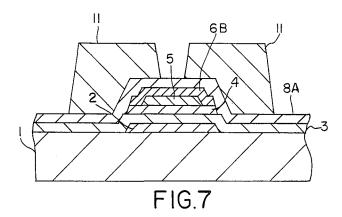


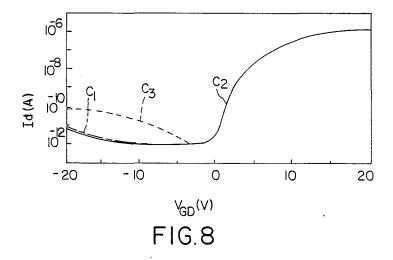
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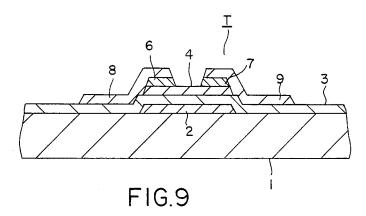


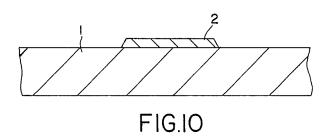












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