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(54) **ELECTRO-OPTICAL DEVICE AND THIN FILM TRANSISTOR AND METHOD FOR FORMING THE SAME**

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This patent is subject to a terminal disclaimer.

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(58) **Field of Classification Search** 438/150-166; 257/E21.133, 413, 414, E27.111, E29.151, 257/278, 294
See application file for complete search history.

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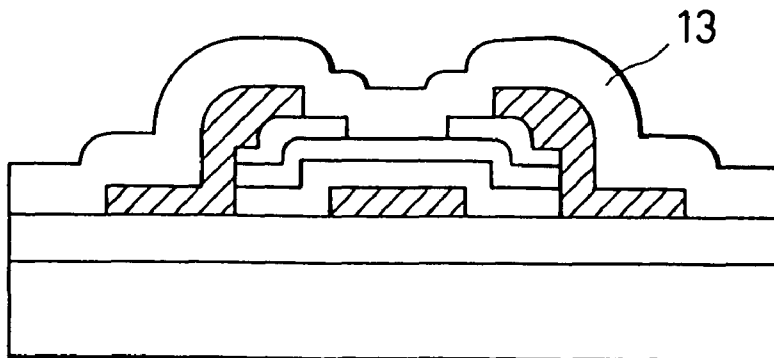
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(57) **ABSTRACT**

A semiconductor device having a pair of impurity doped second semiconductor layers, formed on a first semiconductor layer having a channel formation region therein, an outer edge of the first semiconductor film being at least partly coextensive with an outer edge of the impurity doped second semiconductor layers. The semiconductor device further includes source and drain electrodes formed on the pair of impurity doped second semiconductor layers, wherein the pair of impurity doped second semiconductor layers extend beyond inner sides edges of the source and drain electrodes so that a stepped portion is formed from an upper surface of the source and drain electrodes to a surface of the first semiconductor film.

54 Claims, 8 Drawing Sheets



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