IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

In re U.S. Patent No. 6,240,376 Trial Number:_____

Filed: July 31, 1998

Issued: May 29, 2001

Inventors: Alain Raynaud
Luc M. Burgun

Assignee: Mentor Graphics Corporation

Title: Method and Apparatus for Gate-Level Simulation of Synthesized Register Transfer Level Designs with Source-Level Debugging

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PETITION FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 6,240,376 UNDER 35 U.S.C. §§ 311-319 AND 37 C.F.R. § 42.100 ET SEQ.

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EXHIBIT LIST

1001. U.S. Patent No. 6,240,376 to Raynaud, et al.

1002. Prosecution history of application Serial No. 09/127,584, which matured into the '376 patent.

1003. Chen, et al., "A Source-Level Dynamic Analysis Methodology and Tool for High-Level Synthesis," Proceedings of the Tenth International Symposium on System Synthesis, 1997, pp. 134-140, Sep. 1997.

1004. Koch, et al., "Breakpoints and Breakpoint Detection in Source Level Emulation," ISSS Proceedings of the 9th International Symposium on System Synthesis, pp. 26-31 (1996).

1005. Koch Publication information from CiteCeerX and ACM archives.

1006. Koch, et al., "Debugging of Behavioral VHDL Specifications by Source Level Emulation," Proceedings of the European Design Automation Conference, pp. 256-261, September 1995.

1007. U.S. Patent No. 6,132,109 to Gregory, et al.

1008. HDL-ICE™ ASIC Emulation System, Quickturn Design Systems, Inc.

1009. Prosecution history for application Serial No. 08/566,401, which matured into U.S. Patent No. 5,838,948.

1010. U.S. Patent No. 5,960,191 to Sample, et al.

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Petitioner Synopsys, Inc. ("Synopsys" or "Petitioner") respectfully requests *inter partes* review for claims 1-15 and 20-33 of U.S. Patent No. 6,240,376 (the "'376 patent," attached as Ex. 1) in accordance with 35 U.S.C. §§ 311-319 and 37 C.F.R. § 42.100 et seq.

I. MANDATORY NOTICES

Pursuant to 37 C.F.R. § 42.8(a)(1), Synopsys provides the following mandatory disclosures.

A. Real Party-In-Interest

Pursuant to 37 C.F.R. § 42.8(b)(1), Petitioner certifies that Synopsys, Inc. is the real party-in-interest.

B. Related Matters

Pursuant to 37 C.F.R. § 42.8(b)(2), Petitioner is unaware of any judicial or administrative matters that would affect, or be affected by, a decision in this proceeding. The '376 patent was involved in litigation styled as Mentor Graphics Corp. v. EVE-USA, Inc. and Emulation and Verification Engineering, SA, 6:06-CV-341-AA, which was dismissed with prejudice on November 30, 2006.

C. Lead And Back-Up Counsel

Pursuant to 37 C.F.R. § 42.8(b)(3), Petitioner provides the following designation of counsel:

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