

Meeting Verification Challenges

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VP, Synopsys Emulation Division

Verification Group

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What's New: *Synopsys Acquires EVE*

- #1 worldwide HW/SW co-emulation provider
 - 450+ emulation systems shipped
 - 60+ customers worldwide
- Complements and strengthens Synopsys verification solution
 - Market-leading emulation performance, capacity, cost-per-gate
 - Broadest library of transactor VIP
 - Large team of emulation experts
 - World-wide presence
- EVE is now part of Synopsys' Verification Business Unit
 - ~140 employees (60% R&D)



20+ billion gates
installed so far

NEWS RELEASE

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Synopsys Acquires Eclipse

Acquisition Adds High-Performance, High-Capacity Emulation to Verification Platform

MOUNTAIN VIEW, Calif., October 4, 2012 -- Synopsys, Inc. (Nasdaq: SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today completed the acquisition of Eclipse, a leading supplier of emulation platforms for SoC verification. Emulation is a rapidly growing solution in the spectrum of technologies used to verify today's highly complex systems on chips (SoCs). Integrating Eclipse's technology with Synopsys' best-in-class platform of simulation, debug, verification IP (VIP), coverage, static verification, low power verification, FPGA prototyping and virtual prototyping solutions will give Synopsys customers access to the broadest verification offering in the industry. The terms of the acquisition have not been disclosed. Synopsys does not expect the transaction to have a material impact on its financial results in 2012 or 2013.

"Synopsys has long led in delivering high-performance verification solutions to address the verification challenges associated with the growing complexities in SoCs," said Manoj Gandhi, senior vice president and general manager of Synopsys' Verification Group. "By adding Eclipse's technology and engineering talent, Synopsys is expanding its investment in verification to continue to bring new technology innovations to our customers."

ZeBu Hardware Products Overview

ZeBu-Server

- Latest generation of FPGA-based emulators able to map very large designs
- Eminently scalable
- The new standard for Hardware / Software Co-Verification



ZeBu-Blade2

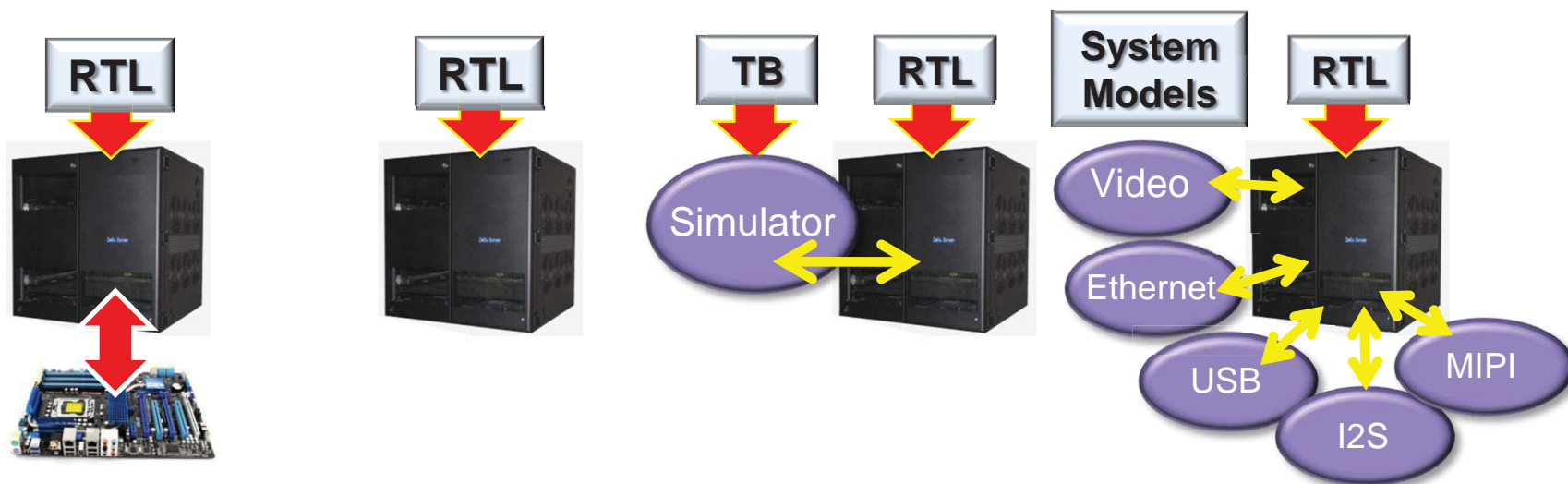
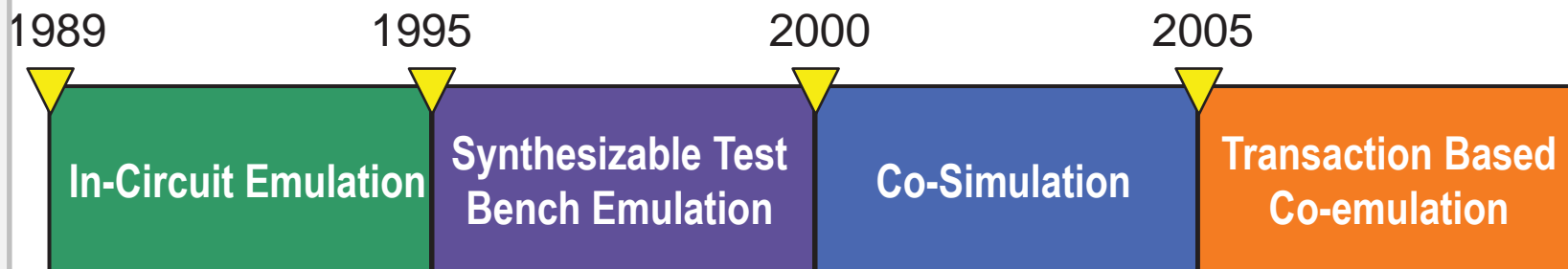
- Newest member of the ZeBu emulation family
- The first based on Xilinx Virtex6-LX760 FPGAs
- A paradigm shift in emulation, from the lab to the desktop, for the entire design team



**ZeBu-Blade2 Fast ASIC
Desktop Emulator**

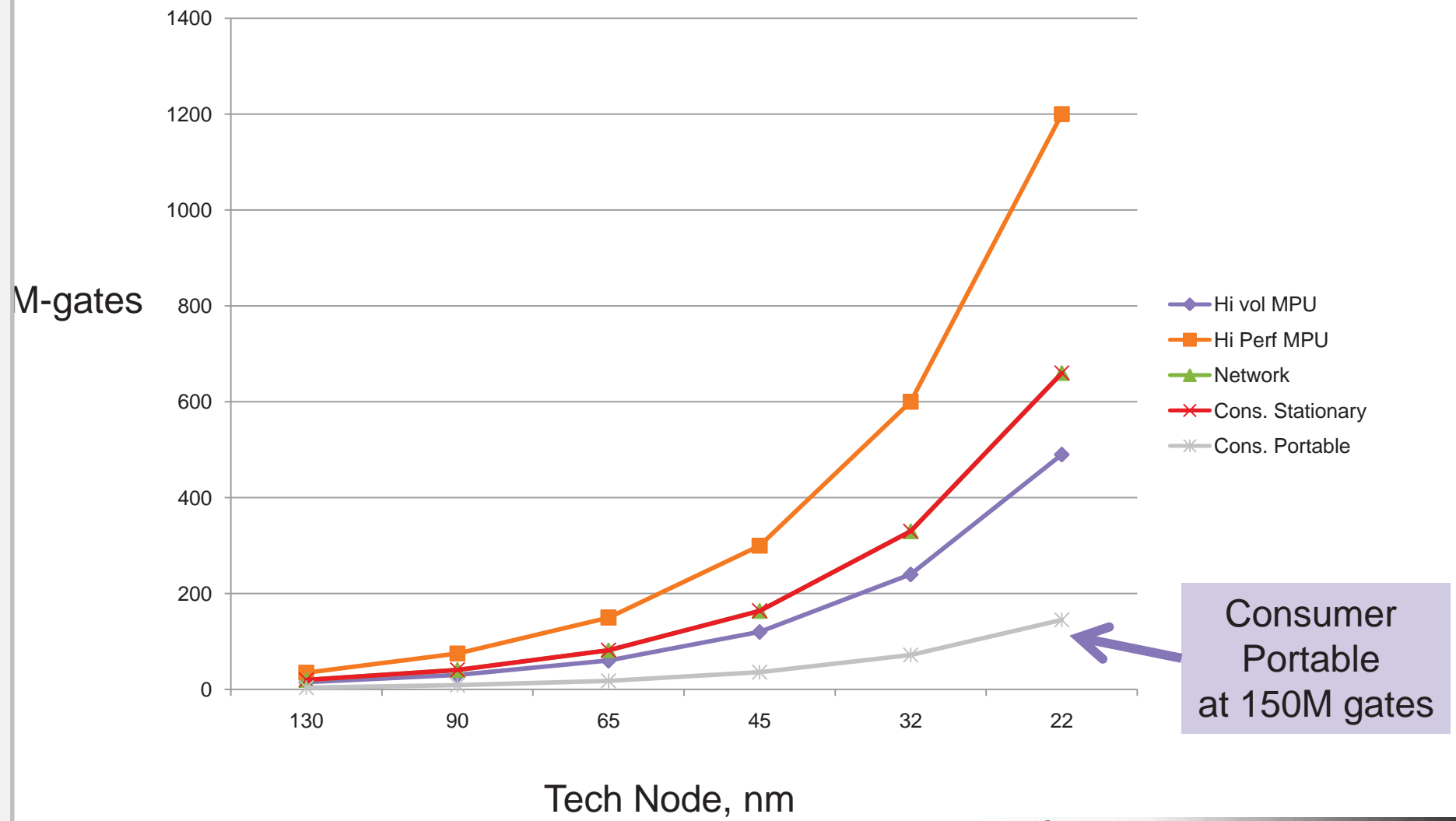
Evolution of Emulation...

“Emulation” = Acceleration + In-Circuit Emulation



- Emulation systems initially only did “In-Circuit Emulation”
- The market is moving more towards Transaction-Based Verification
- Emulators now do: Co-simulation, Transaction-Based Ver., STB, In-Circuit

Design Size by Technology Node-Derived from ITRS Data



Consumer Portable at 150M gates

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