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(54) **MULTI-FINGER CAPACITOR**

**Publication Classification**

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(52) **U.S. Cl.** ..... **257/532; 257/E29.325**

(57) **ABSTRACT**

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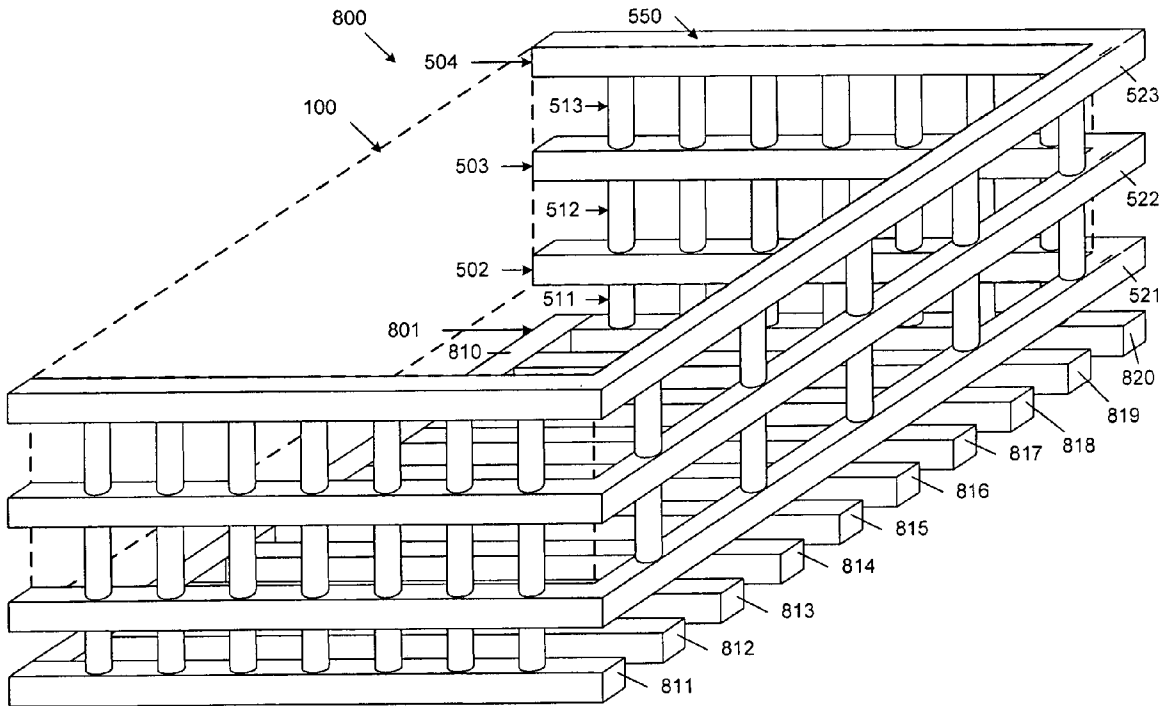
A multi-finger capacitor structure includes a capacitor input node having a first set of conductive fingers, a capacitor output node having a second set of conductive fingers interleaved with the first set of conductive fingers, and a conductive plate and/or pattern connected to the capacitor input node, and located between a substrate and the first and second sets of interleaved conductive fingers. The conductive plate/pattern renders the parasitic capacitance of the capacitor output node negligible, thereby imparting desirable operating characteristics to the capacitor structure. The capacitor input node may also include Faraday electric walls that laterally surround the capacitor output node, thereby limiting electrical energy leakage.

(21) Appl. No.: **11/949,002**

(22) Filed: **Nov. 30, 2007**

**Related U.S. Application Data**

(60) Provisional application No. 60/868,668, filed on Dec. 5, 2006.



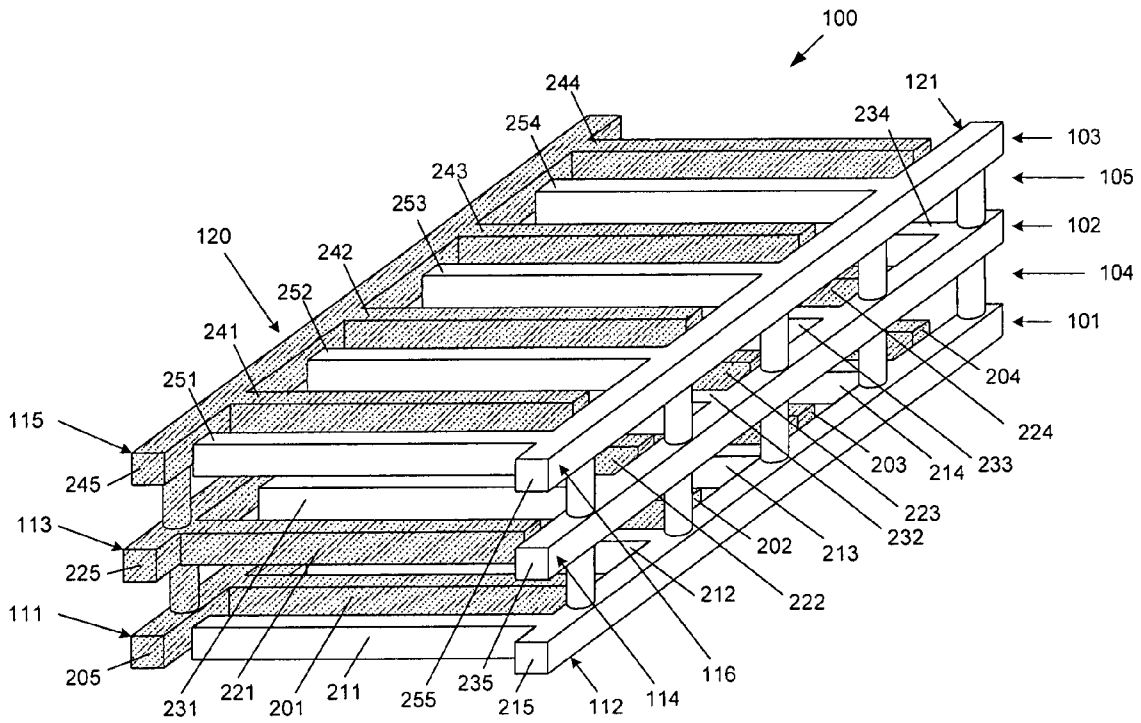


FIG. 1  
(PRIOR ART)

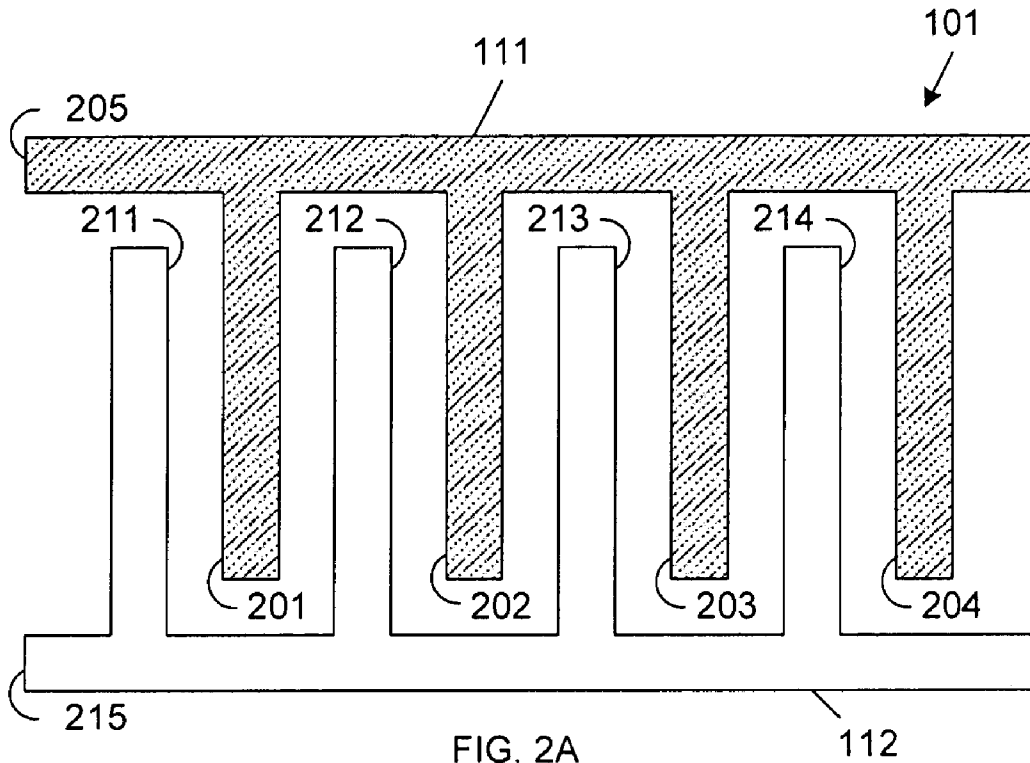


FIG. 2A  
(PRIOR ART)

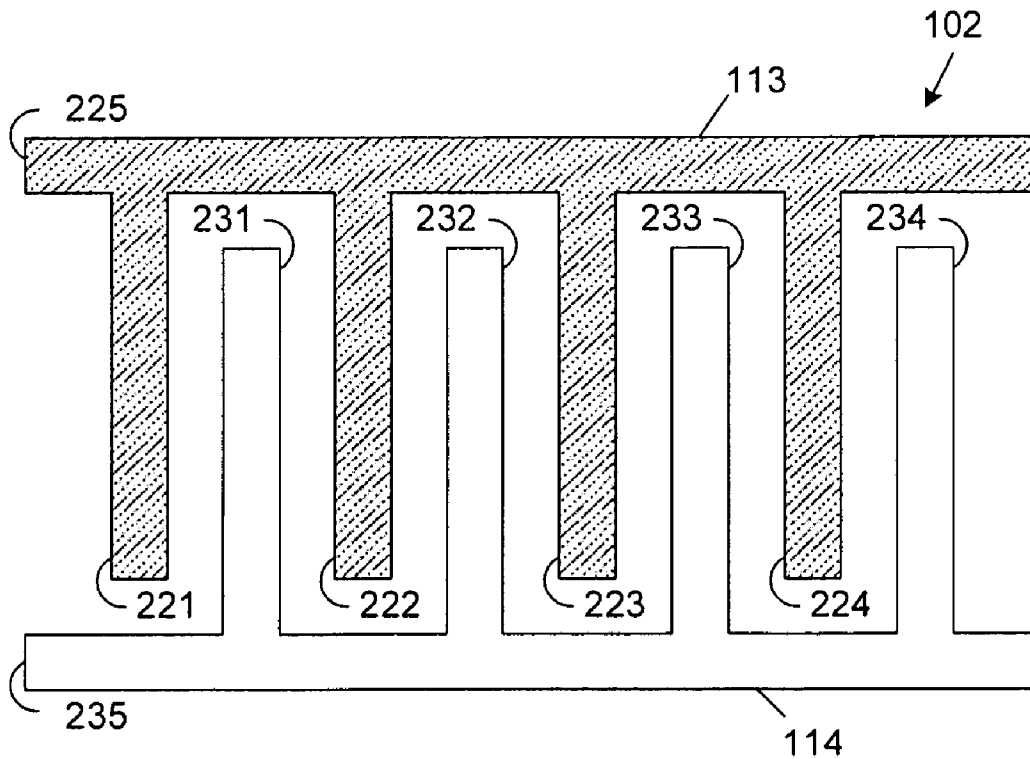


FIG. 2B  
(PRIOR ART)

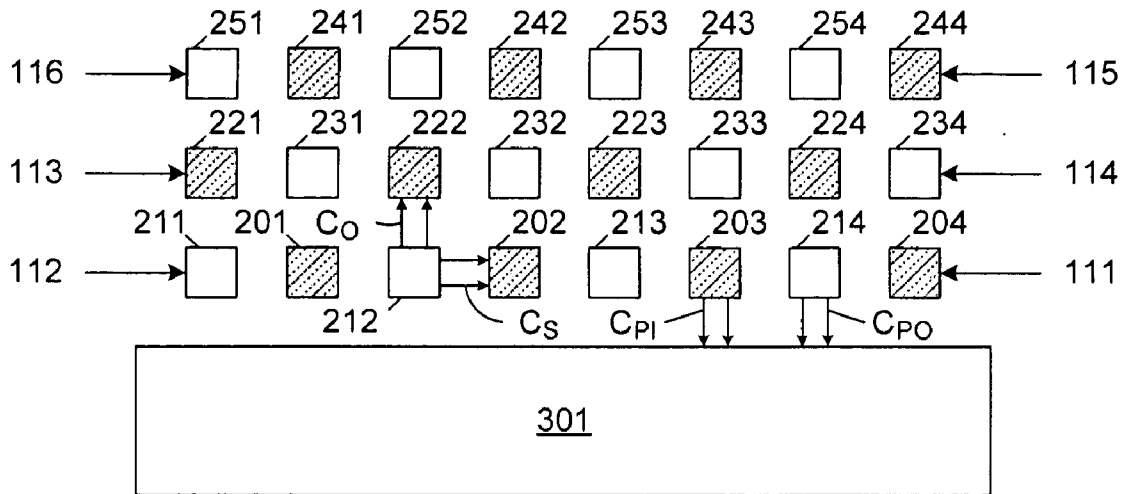


FIG. 3  
(PRIOR ART)

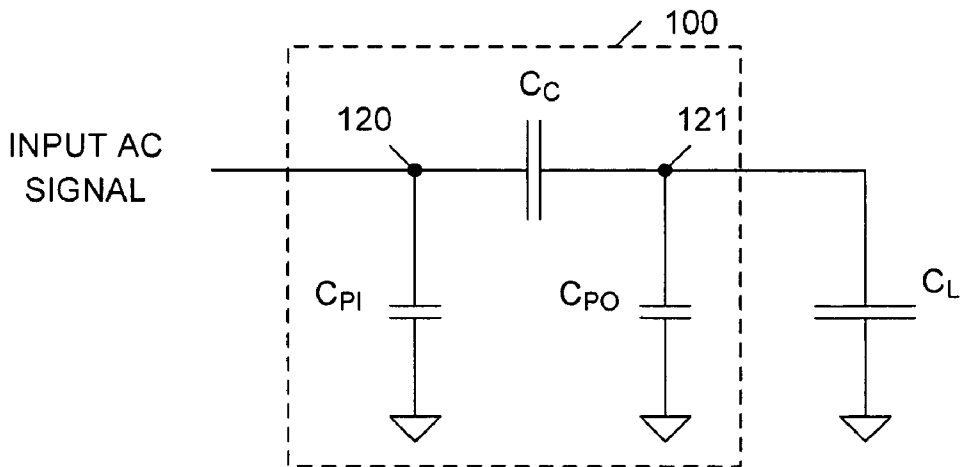


FIG. 4  
(PRIOR ART)

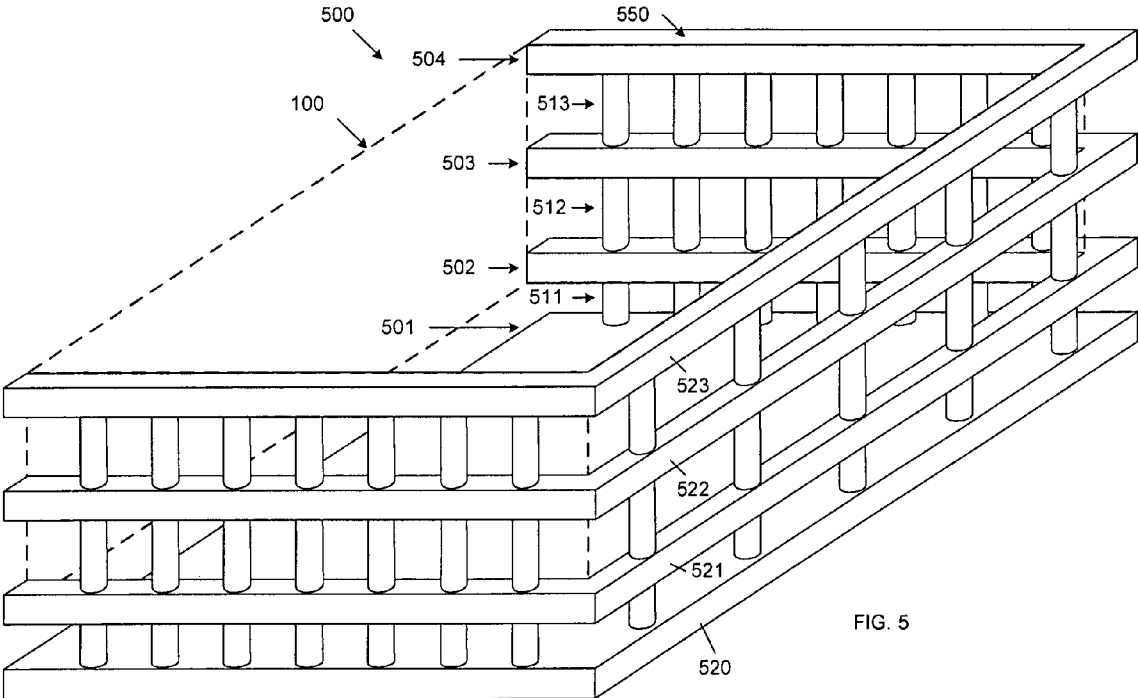


FIG. 5

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