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### (12) United States Patent Paul et al.

### (54) SHIELDED CAPACITOR STRUCTURE

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- (\*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.
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- (51) Int. Cl.<sup>7</sup> ..... H01L 27/108
- (52) U.S. Cl. ..... 257/306; 257/306; 257/307

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 (45) Date of Patent: May 18, 2004

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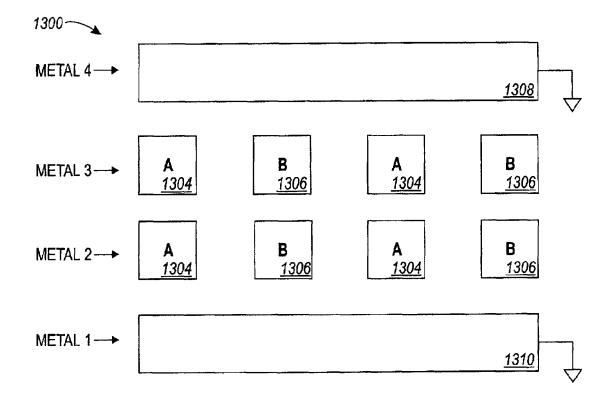
\* cited by examiner

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### (57) ABSTRACT

A method and apparatus if provided for shielding a capacitor structure formed in a semiconductor device. In a capacitor formed in an integrated circuit, one or more shields are disposed around layers of conductive strips to shield the capacitor. The shields confine the electric fields between the limits of the shields.

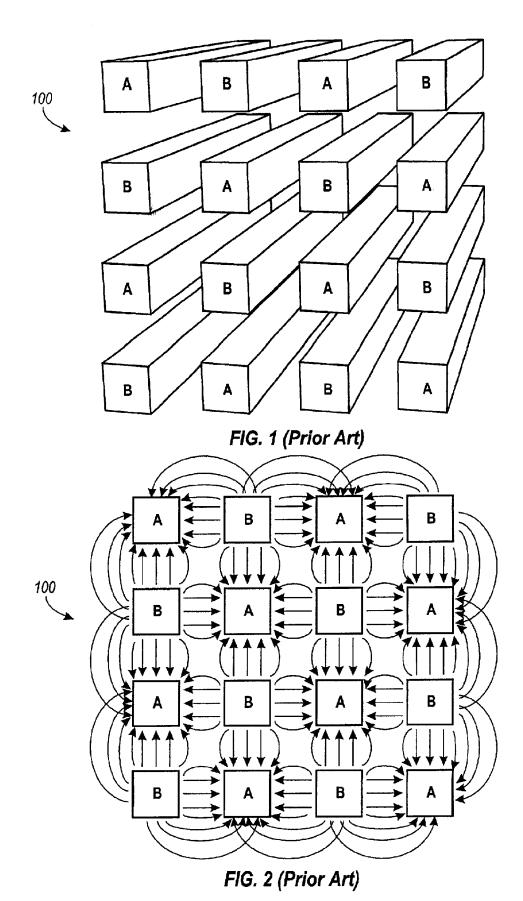
### 44 Claims, 7 Drawing Sheets

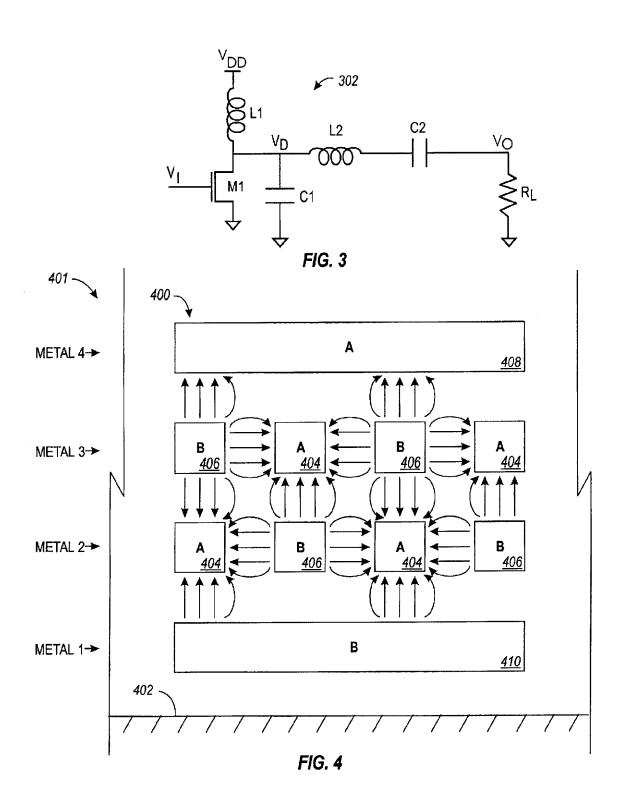


IVM 1006 IPR of U.S. Pat. No. 7,994,609

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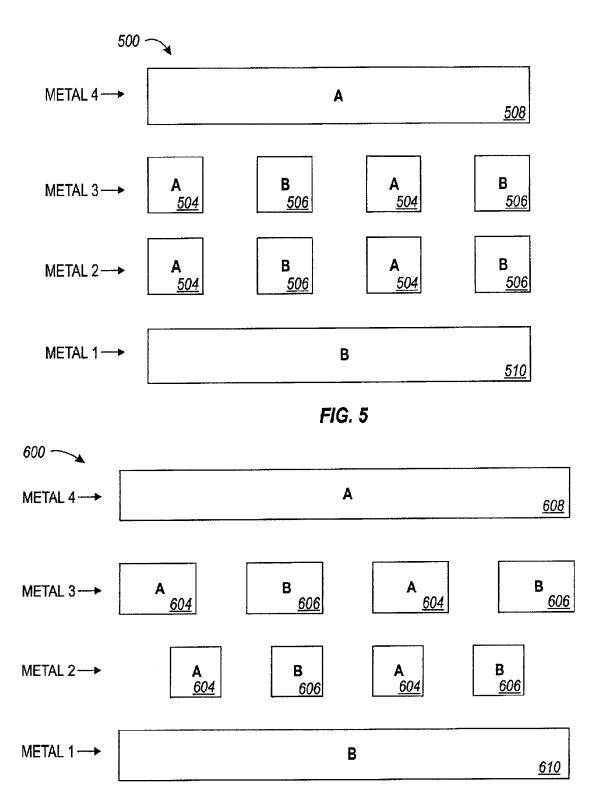
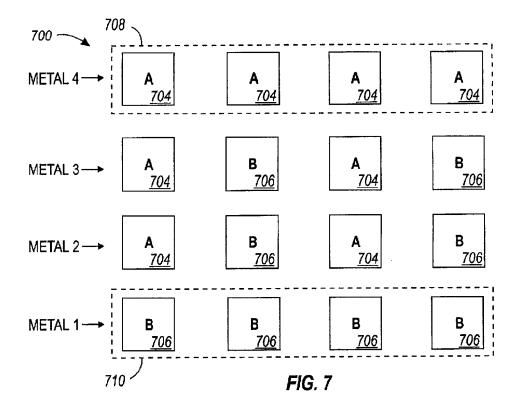


FIG. 6



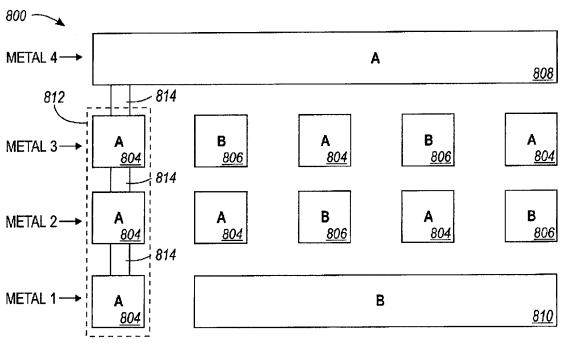


FIG. 8

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