UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

INTELLECTUAL VENTURES MANAGEMENT, LLC

Petitioner

v.

XILINX, INC.

Patent Owner

Case IPR2012-00023

Patent 7,994,609

PETITIONER INTELLECTUAL VENTURES MANAGEMENT, LLC'S REPLY TO PATENT OWNER'S RESPONSE

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Petitioner Intellectual Ventures Management, LLC ("IVM") provides this reply under 37 C.F.R. § 42.23 to Patent Owner Xilinx's Patent Owner's Response dated May 7, 2013. *Inter partes* review of claims 1-19 of U.S. Patent No. 7,994,609 was instituted on February 12, 2013.

I. Statement of relief requested

IVM seeks cancellation of claims 1-19 of the '609 patent and denial of Xilinx's First Motion to Amend filed May 7, 2013 (First Motion to Amend, Paper No. 17). An opposition to the Motion to Amend is being filed concurrently.

II. Original claims 1-19 are obvious over the art cited in the Petition.

In its Patent Owner's Response, Xilinx does not separately address any grounds of patentability instituted for independent claims 1 and 13 and their dependent claims 3-7, 9-12, and 14-17. Thus, Xilinx concedes that these claims are unpatentable over the instituted grounds. Xilinx, instead, focuses its reply solely on independent claim 18 and dependent claims 2, 8, and 19. But, as described below, the arguments presented by Xilinx are contradicted by the evidence of record, including the deposition testimony of its own expert, Dr. Blanchard.

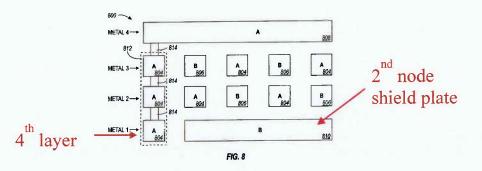
A. Claim 2 is obvious over Paul¹ and Anthony²

¹ U.S. Patent No. 6,737,698 (IVM1006).

² U.S. Patent No. 7,439,570 (IVM1007).

1. The combination of Paul and Anthony discloses the limitations of claim 2.

Claim 2 recites "wherein the third conductive layer is a metal layer of the IC and the fourth conductive layer is a poly layer of the IC, the shield capacitor portion including a first node shield plate formed in the metal layer from a plurality of metal stripes and a second node shield plate formed in the poly layer." As explained in the Petition metal layer 1 and bottom shield plate 810, shown in FIG. 8 of Paul which is reproduced below with annotations in red, disclose a "fourth layer" and a "second node shield plate," respectively. (Petition, Paper No. 3, p. 24.)



Although Paul's metal layer 1 is not a poly layer, Anthony teaches that the bottom layer of a capacitor can be formed in a poly layer instead of a metal layer: "[a]s an alternative to the use of a metal layer as shown in FIG. 3B...the bottom shield plate 36 can be implemented with a polysilicon or diffusion layer." (IVM 1007, 4:49-52.) Petitioner's expert, Mr. Johnson, further explained in his First Declaration that one of ordinary skill in the art would have been motivated to

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