

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Rozman, *et al.* Docket No.: ARAC-01RE3C1C1
Serial No.: TBD Filed: Herewith
Reissue of: 7,484,247 Issued: January 27, 2009
Title: System and Method for Protecting a Computer System from Malicious
Software

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination on the merits, the Applicants respectfully submit the amendments and remarks set forth below.

IN THE SPECIFICATION:

Before the heading “Cross Reference to Related Patents and Applications” please insert:

Cross Reference to Multiple Reissue Applications

This is a reissue continuation of U.S. Reissue Patent Application Serial No. 13/015,186 (now, U.S. Patent No. RE43,987), which is a reissue continuation application of U.S. Reissue Patent Application Serial No. 12/854,149 (now, U.S. Patent No. RE43,103), which is a reissue application of U.S. Patent No. 7,484,247, entitled “System and Method for Protecting a Computer System from Malicious Software,” issued on January 27, 2009. The following are related reissue applications: U.S. Reissue Patent Application Serial No. 12/720,147 (now, U.S. Patent No. RE43,528) from U.S. Patent No. 7,484,247, filed on March 9, 2010, U.S. Reissue Patent Application Serial No. 12/720,207 (now, U.S. Patent No. RE43,500) from U.S. Patent No. 7,484,247, filed on March 9, 2010, and U.S. Reissue Patent Application Serial No. 12/941,067 (now, U.S. Patent No. RE43,529) from U.S. Patent No. 7,484,247, filed on November 7, 2010. All of the above applications are incorporated herein by reference.

Before the heading “BRIEF DESCRIPTION OF THE DRAWINGS”, please insert the following material into the specification. All of the added material has been previously incorporated by reference and the amendment contains no new matter.

TERM DESCRIPTION

Memory: This term is intended to broadly encompass any device capable of storing and/or incorporating computer readable code for instantiating the client device referred to immediately above. Thus, the term encompasses all types of recording medium, e.g., a CD-ROM, a disk drive (hard or soft), magnetic tape, and recording devices, e.g., memory devices including DRAM, SRAM, EEPROM, FRAM, and Flash memory. It should be noted that the term is intended to include any type of device which could be deemed persistent storage. To the extent that an Application Specific Integrated Circuit (ASIC) can be considered to incorporate instructions for instantiating a client device, an ASIC is also considered to be within the scope of the term “memory.”

IN THE CLAIMS:

1.-20. (Canceled)

21. (New) A method, comprising:

executing instructions in a first logical process with one of a first electronic data processor and a second electronic data processor employing a common operating system, the first logical process accessing data in at least one of a first memory space and a second memory space;

executing instructions in a second logical process with one of the first electronic data processor and the second electronic data processor employing the common operating system, the second logical process accessing data in at least one of the first memory space and the second memory space, and exchanging data across a computer network; and

protecting data residing in the first memory space from a malware process downloaded from the computer network and operating as part of the second logical process.

22. (New) The method of claim 21 further comprising generating data from the first logical process and the second logical process for display.

23. (New) The method of claim 21 wherein the first and second electronic data processors are part of a multi-core electronic data processor.

24. (New) The method of claim 21 wherein the first logical process executes instructions with the first electronic data processor and accesses data in the first and second

memory space, and the second logical process executes instructions with the second electronic data processor and accesses data in the second memory space.

25. (New) The method of claim 21 further comprising protecting the first logical process from executing instructions initiated by the malware process downloaded from the network and executing as part of the second logical process.

26. (New) The method of claim 21 further comprising restoring at least one corrupted data file residing on the second memory space from an image residing on the first memory space.

27. (New) The method of claim 21 further comprising automatically deleting at least one data file residing on the second memory space when the second logical process is terminated.

28. (New) A computer, comprising:
a first electronic data processor employing a common operating system and configured to execute instructions in a first logical process, the first logical process configured to access data in at least one of a first memory space and a second memory space;
a second electronic data processor employing the common operating system and configured to execute instructions in a second logical process, the second logical process configured to access data in at least one of the first memory space and the second memory space, and exchange data across a computer network; wherein data residing in the first memory space is protected from a malware process downloaded from the computer network and operating as part of the second logical process.

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