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STAMOULIS & WEINBLATT LLC

VIA CM/ECF AND HAND DELIVERY

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September 10, 2018

The Honorable Maryellen Noreika United States District Court for the District of Delaware 844 N. King Street Wilmington, DE 19801

Re: Godo Kaisha IP Bridge 1 v. OmniVision Technologies, Inc., C.A. No. 16-290 (MN)

Dear Judge Noreika:

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Pursuant to Your Honor's Order (D.I. 80), the parties submit this letter identifying the ten disputed claim terms that they will argue at the claim construction hearing on October 31, 2018:

No.	Patent/Claims	Claim Term
1.	'796 patent (1, 3) '431 patent (1, 5, 9) '401 patent (1, 29)	"a plurality of read-out lines each being selectively connected to at least two of the [plurality of] transfer transistors"
2.	^{'796} patent (1, 3) ^{'431} patent (1, 4, 5, 9, 12) ^{'401} patent (1, 4, 29)	"photodiode"
3.	 '796 patent (1, 3) '431 patent (1, 5, 9) '401 patent (1, 29) 	"a plurality of floating diffusion sections each being connected to ones of the [plurality of] photodiodes via each of a plurality of transfer transistors"
4.	'796 patent (1, 3)	"the plurality of read-out lines are disposed between the first and the second photodiodes"
5.	'431 patent (1, 5, 9)	"the first read-out line and the second read- out line are disposed between the row n and the row n+1"

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No.	Patent/Claims	Claim Term
6.	'431 patent (1, 5, 9)	"one of the plurality of reset pulse lines is disposed between the row n+1 and the row n+2"
7.	'401 patent (1, 29)	"the first read-out line is adjacent to the second read-out line"
8.	'324 patent (1, 5)	"multilayered structure of first and second films"
9.	'950 patent (12)	"second sidewalls on a side surface of a step portion"
10.	'677 patent (1, 4)	 "a sum perimeter of the first linear pattern, the second linear pattern, and the dummy pattern per unit area is equal to or less than a perimeter of the first linear pattern per unit area" "a sum perimeter of the first gate electrode pattern, the second electrode pattern, and the dummy pattern per unit area is equal to or less than a perimeter of the first linear pattern per unit area is equal to or less than a perimeter of the first gate electrode pattern.

Respectfully,

/s/ Stamatios Stamoulis

Stamatios Stamoulis (#4606)

cc: Counsel of Record (via CM/ECF)

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