Case 1:16-cv-00290-MN	Document 1-9	Filed 04/22/16	Page 1 of 16 PageID #: 244

EXHIBIT I



(12) United States Patent Ikoma et al.

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US 7,709,900 B2

(45) **Date of Patent:**

*May 4, 2010

(54) SEMICONDUCTOR DEVICE

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(*) Notice: Subject to any disclaimer, the term of this

patent is extended or adjusted under 35

U.S.C. 154(b) by 121 days.

This patent is subject to a terminal dis-

claimer.

(21) Appl. No.: 11/892,053

(22) Filed: Aug. 20, 2007

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(30) Foreign Application Priority Data

Jul. 22, 2004 (JP) 2004-213903

(51) Int. Cl.

H01L 23/62 (2006.01)

(52) U.S. Cl. 257/369; 257/371; 257/E27.064

See application file for complete search history.

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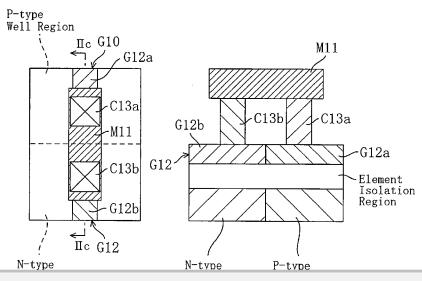
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Primary Examiner—Phat X Cao (74) Attorney, Agent, or Firm—McDermott Will & Emery LLP

(57) ABSTRACT

A semiconductor device includes a semiconductor substrate; a diffusion region which is formed in the semiconductor substrate and serves as a region for the formation of a MIS transistor; an element isolation region surrounding the diffusion region; at least one gate conductor film which is formed across the diffusion region and the element isolation region, includes a gate electrode part located on the diffusion region and a gate interconnect part located on the element isolation region, and has a constant dimension in the gate length direction; and an interlayer insulating film covering the gate electrode. The semiconductor device further includes a gate contact which passes through the interlayer insulating film, is connected to the gate interconnect part, and has the dimension in the gate length direction larger than the gate interconnect part.

10 Claims, 7 Drawing Sheets





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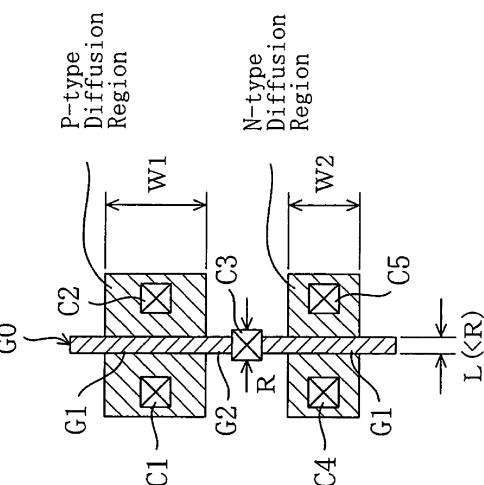


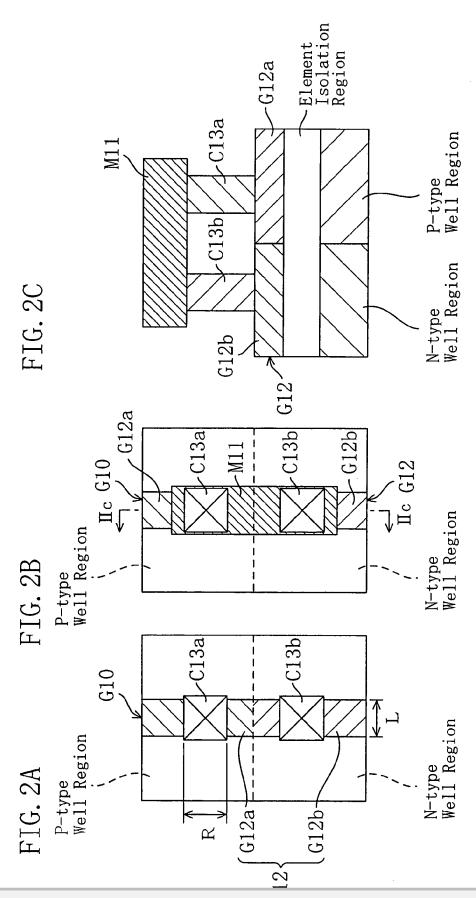
FIG. 1A

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