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## EXHIBIT T

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11. A MOS (metal-oxide-semiconductor) image pick-up device comprising:

(A) a semiconductor substrate,

(B) an imaging region formed on the semiconductor substrate by arraying (C) plural unit pixels, and

(D) a peripheral circuit region comprising (E) a driving circuit for operating the imaging region formed on the semiconductor substrate;

where each of (C) the unit pixels comprises: (F) a photodiode, (G) MOS transistors, and (H) a first device-isolation portion

(D) the peripheral circuit region comprises (I) a second device-isolation portion for isolating devices in (E) the driving circuit wherein the following are satisfied:

I. (H) the first device-isolation portion has a structure comprising:

A. an electrically insulating film formed on a surface of the substrate so as to erode the substrate to a depth in a range of 10 nm to 100 nm, or

B. (J) an impurity diffusion region formed within (A) the substrate directly under the substrate surface, and

II. (H) the first device-isolation portion and (I) the second device-isolation portion have respective structures selected so as to be different from each other.

Claim 11

A MOS (metal-oxide-semiconductor) image pick-up device comprising:

(A) a semiconductor substrate,

(B) an imaging region formed on the semiconductor substrate by arraying (C) plural unit pixels, and

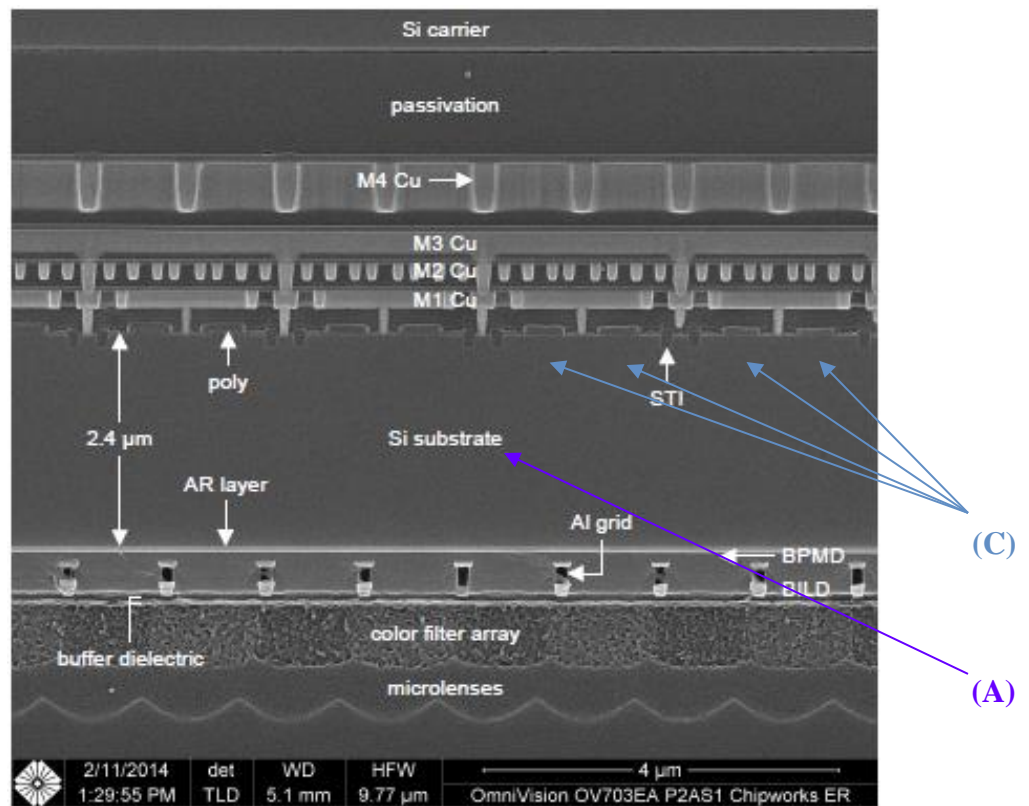


Figure 3.1.6 General Structure – Pixel Array

Claim 11

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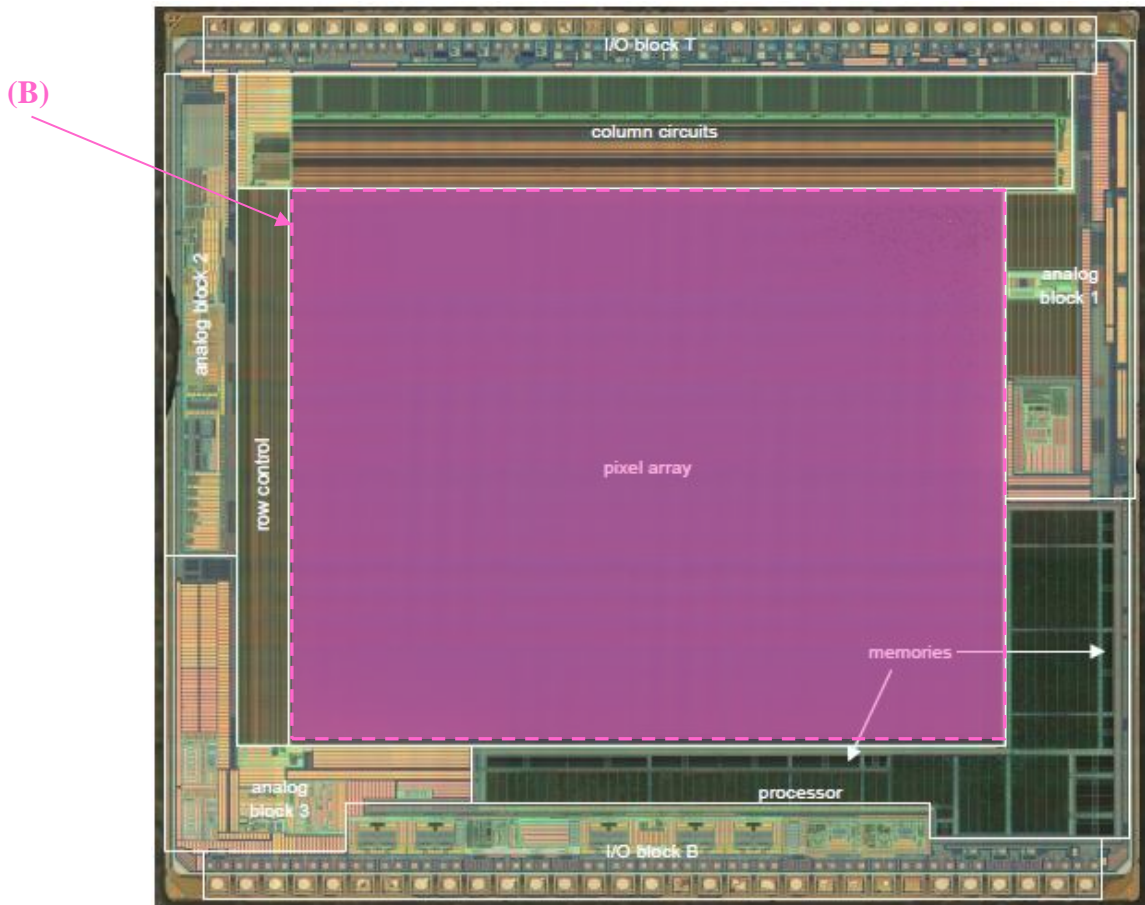


Figure 2.5.1 Annotated Poly Die Photograph

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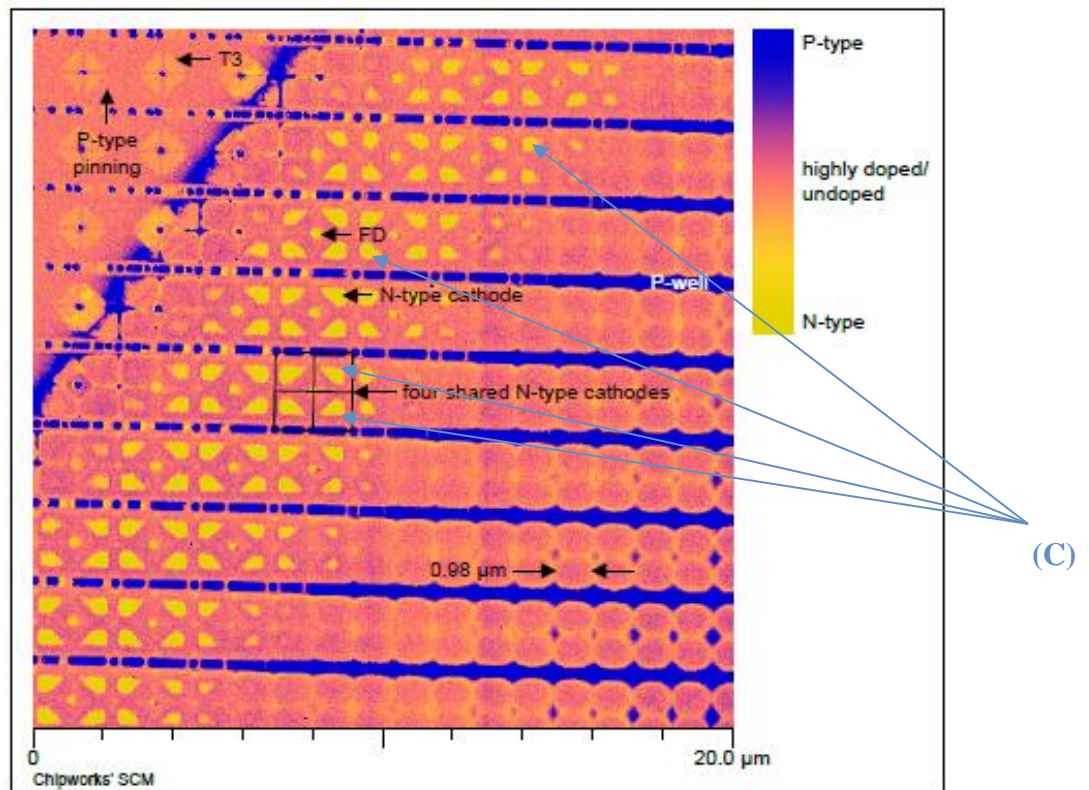


Figure 4.2.8 N-Cathode Overview – SCM



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