

1 UNITED STATES DISTRICT COURT  
2 NORTHERN DISTRICT OF CALIFORNIA  
3 SAN FRANCISCO DIVISION  
4

5 VOIP-PAL.COM, INC., Case No. 3:22-CV-3199-JD  
6 Plaintiff,  
7 v. **DECLARATION OF VOJIN**  
8 **OKLOBDZIJA, PH.D.**  
9 GOOGLE LLC,  
10 Defendant.

12 VOIP-PAL.COM, INC., Case No. 3:22-CV-3202-JD  
13 Plaintiff,  
14 v.  
15 META PLATFORMS INC., et. al.  
16 Defendants.  
17

18 TWITTER, INC., Case No. 3:21-CV-9773-JD  
19 Plaintiff,  
20 v.  
21 VOIP-PAL.COM, INC.,  
22 Defendant.  
23  
24

25 I, Vojin Oklobdzija, declare as follows:

26 **I. INTRODUCTION**  
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1           1.       I have personal knowledge of the facts contained in this declaration and, if called as a  
2 witness, I could and would competently testify to those facts. I am being compensated at my normal  
3 consulting rate. My compensation does not depend on and in no way affects the substance of my  
4 statements in this Declaration.

5  
6           **II. QUALIFICATIONS**

7           2.       I am an independent consultant with Integration Corp. I reside in Berkely, California,  
8 and my place of business is at 855 Marina Bay Pkwy, Suite 130, Richmond, CA 94804. I am over the  
9 age of eighteen, and I am a citizen of the United States.

10           3.       I have over 50 years of commercial, industrial, and university experience in all aspects  
11 of Electrical Engineering such as: Electronics (Analog and Digital), Telecommunication (Radio and  
12 Signal Processing), Computer Science (Computer Architecture and Computer Arithmetic), Computer  
13 Design (Hardware) and Semiconductor Integrated Circuits.

14  
15           4.       I received a Dip. Ing. degree (MSc EE equivalent) in Electronics and  
16 Telecommunications from the University of Belgrade, Yugoslavia in 1971, a Master of Science degree  
17 in computer science from the University of California, Los Angeles in 1978, and a Doctor of  
18 Philosophy (Ph.D.) degree in computer science from the University of California, Los Angeles in  
19 1982.

20  
21           5.       Since 1997, I have been the owner and Principal Researcher at Integration Corp., which  
22 provides services to the electronics industry including consulting, research, product development,  
23 design, and testing. My previous industry experience includes employment at Xerox Corp., IBM T. J.  
24 Watson Research Center, Sun Microsystems, AT&T Bell Laboratories, Fujitsu Ltd., Hitachi Ltd.,  
25 Sony Corp., Siemens/Infineon Corp., Intel Corp., Samsung Corp., and several startup companies, such  
26 as Skyera Inc., Wave Semi Inc., Esperanto Technologies Inc., SambaNova System Inc., and including  
27 my own, now defunct, startup company Silicon Analytics Inc.  
28

1           6.       In addition to my work at Integration, I have been a Professor of Engineering at  
2 University of California, Davis since 1991, and University Professor Emeritus since 2006. I was also  
3 a professor at University of California, Berkeley from 1988 to 1990, Sydney University (Australia),  
4 Ecole Polytechnique Federal du Lausanne (Switzerland), University of Texas, and University of New  
5 Mexico.

6  
7           7.       From 1991 to 2006, I was a tenured Full Professor at the University of California,  
8 Davis. While there, I established a Computer Engineering (“CE”) program in the Electrical  
9 Engineering Department, which later became the Electrical and Computer Engineering Department to  
10 reflect the addition of Computer Engineering. I taught all the critical courses in the CE curriculum,  
11 such as Digital Systems I and Digital Systems II, Computer Architecture, Assembly Language and  
12 Computer Organization, and Digital Integrated Circuits, as well as graduate courses, such as Advanced  
13 Logic Design, Computer Architecture, High-Performance Computer Architecture and Computer  
14 Arithmetic. During my tenure at other universities, I also taught courses in VLSI Design, Low-Power  
15 VLSI Circuits Design, and Digital Logic Design, and at UC Berkeley Extension in Silicon Valley. I  
16 started the Advanced Computer System Engineering Laboratory (“ACSEL”) at the University of  
17 California, Davis in 1992. ACSEL consisted of my graduate students, professors associated with the  
18 group, industrial researchers, and past doctoral students. ACSEL has been working on solving  
19 important problems associated with computer and electronic/VLSI systems.  
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22           8.       Upon my retirement as a university professor in 2012, I returned to work full-time in  
23 the industry. I joined Skyera Inc. (“Skyera”), a startup in San Jose, California, where I had the title of  
24 Senior Director, Processor Design. At Skyera, I managed a group of engineers involved in designing a  
25 proprietary processor for Skyera. The Skyera processor consisted of many CPUs on the chip with two  
26 levels of cache hierarchy and DRAM memory.  
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1           9.       After Skyera was acquired in 2014 by Hitachi, I commenced working as a consultant  
2 for Wave Semi Inc. on multi-CPU chip design. My work was on the CPU and arithmetic elements of  
3 the processor, which was later tailored towards machine learning.

4           10.       In December of 2015, I switched to Esperanto Technologies Inc. (“Esperanto Tech.”), a  
5 startup company working on a “machine-learning” chip. During my work at Esperanto Tech.,  
6 I personally worked on CPU and memory design.

7           11.       In April of 2018, I joined SambaNova Systems Inc. (“SNS Inc.”), a Palo Alto based  
8 startup that is one of the two leading companies in machine learning, having received over one billion  
9 dollars in funding. At SNS Inc., I designed specialized CPUs tailored for machine learning.

10           12.       As detailed in my curriculum vitae, in addition to my industry, private consulting, and  
11 research work, I have published numerous articles in industry journals and proceedings, presented  
12 papers at industry technical and professional meetings, and participated in electronics and computer  
13 science industry professional meetings, conferences, trade groups, and professional organizations.

14           13.       Since 1995, I have been a Fellow of the Institute of Electrical and Electronic Engineers  
15 (“IEEE”), a professional organization with over 400,000 members in more than 160 countries. IEEE  
16 states: “IEEE Fellow is a distinction reserved for select IEEE members whose extraordinary  
17 accomplishments in any of the IEEE fields of interest are deemed fitting of this prestigious grade  
18 elevation.” No more than 0.1% of the IEEE voting membership on record may be elevated to Fellow  
19 in a year. Since 2014, I have been “Life Fellow of IEEE.” From 2014 to 2016, I served as President  
20 of IEEE Circuits and Systems Society (IEEE’s oldest and founding society), Vice President of the  
21 IEEE Circuits and Systems from 2012 to 2014, and Chair of Vision Committee of IEEE Circuits  
22 and Systems Society prior to 2014. In addition, during this time, I served on the Board of Governors  
23 of the IEEE from 2012-2016.  
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1 14. I am the named inventor on 20 U.S. patents and four currently pending patents, all of  
2 which are directed in general to computers, electronic circuits, and integrated circuits.

3 15. My experience as it specifically relates to networking dates to 1999-2000 and the work  
4 on network encryption processors, for two companies Blue Steel Networks Inc. and Digital Archways  
5 Inc. Blue Steel Networks Inc. was successfully sold to Broadcom Corp and became part of Broadcom,  
6 owned by Avago. The processor was an ad-on network card which handled encrypted network traffic.  
7 The second company related to network processors was Digital Archways Inc, which dealt with the  
8 same secure network processor. The company failed and went out of business due to the lack of  
9 funds.  
10

11 16. I have provided a copy of my complete curriculum vitae as an attachment to this  
12 declaration as Exhibit A.  
13

### 14 III.TASK

15 17. I have been asked to provide testimony regarding the understanding of a person of  
16 ordinary skill in the art (“POSITA”) at the time of invention regarding the technology disclosed and  
17 claimed in VoIP-Pal’s patents in general, and U.S. Patent Nos. 8,630,234 (“the ’234 patent”) and  
18 10,880,721 (“the ’721 patent”) in particular. This declaration is not the first time I have provided  
19 testimony for VoIP-Pal. I provided expert reports in *VoIP-Pal.com, Inc. v. Amazon.com, Inc.*, 6:21-cv-  
20 00668 (WDTX); *VoIP-Pal.com, Inc. v. Verizon Communications, Inc.*, 6:21-cv-00672 (WDTX); *VoIP-*  
21 *Pal.com, Inc. v. T-Mobile USA, Inc.*, 6:21-cv-00674 (WDTX) relating to the ’234 and ’721 patents. I  
22 also have submitted declarations in *VoIP-Pal.com, Inc. v. Meta Platform, Inc.*, 3:22-cv-4279  
23 (NDCAL) and *VoIP-Pal.com, Inc. v. Google LLC*, 3:22-cv-5479 (NDCAL) relating to U.S Patent No.  
24 10,218,606. While I am not a lawyer and have no formal legal training, I am a prolific inventor with  
25 issued Patents, and have worked at the intersection of patents and computer technology for the past  
26 several years. In making the statements in this declaration, I have relied on my education in computer  
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