```
1
   IRELL & MANELLA LLP
   Morgan Chu (70446)
   MChu@irell.com
   Benjamin W. Hattenbach (186455)
   BHattenbach@irell.com
   Samuel K. Lu (171969)
   SLu@irell.com
   Olivia L. Weber (319918)
   OWeber@irell.com
   1800 Avenue of the Stars, Suite 900
   Los Angeles, California 90067-4276
 7
   Telephone: (310) 277-1010
   Facsimile: (310) 203-7199
 8
   FOLIO LAW GROUP PLLC
   C. Maclain Wells (221609)
   Maclain@foliolaw.com
10
   2376 Pacific Ave.
   San Francisco, CA 94115
11
   (415) 562-8632
12
    Attorneys for Defendant
13
   DEMARAY LLC
                            UNITED STATES DISTRICT COURT
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                          NORTHERN DISTRICT OF CALIFORNIA
15
                                    SAN JOSE DIVISION
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                                               Case No. 5:20-cv-09341-EJD
    APPLIED MATERIALS, INC.,
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                Plaintiff,
                                               DEMARAY LLC'S RESPONSIVE CLAIM
                                               CONSTRUCTION BRIEF
20
          VS.
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    DEMARAY LLC,
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                Defendant.
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#### I. INTRODUCTION

The WDTX court has already construed each of the terms that Applied proposes for construction, and in each case, Judge Albright rejected either Applied's proposed construction or one that is substantively similar. Exs. C, D (WDTX Claim Construction Orders). The parties agree that the WDTX court's determinations are entitled to "reasoned deference." *Finjan Inc. v. Symantec Corp.*, No.14-cv-02998, 2017 WL 550453, at \*3 (N.D. Cal. Feb. 10, 2017). Because claim construction "is not an obligatory exercise in redundancy" requiring courts to substitute other language for understandable claim terms, *O2 Micro Int'l Ltd. v. Beyond Innovation Tech. Co.*, 521 F.3d 1351, 1362 (Fed. Cir. 2008), the WDTX court gave most of the disputed terms their plain and ordinary meaning. Consistent with those determinations, in Applied's four recent *inter partes* review petitions, it did not seek construction of even a single one of these claim terms. Wells ¶ 13.¹ The Court should see Applied's proposals for what they are—an invitation for conflicting rulings from a different court and an avenue for appeal. No further constructions are necessary.

#### II. THE DEMARAY PATENTS

The Demaray Patents<sup>2</sup> generally concern equipment and processes used to deposit thin films in the production of semiconductor products. Glew ¶ 18. Layers of those films, which are deposited in chambers within reactors, form structures such as transistors and electrical interconnections of the sort that make up modern integrated circuits. Glew ¶ 18.

The patents focus on a process called physical vapor deposition ("PVD") sputtering in which metal particles from a "target" create a plasma that deposits the films on a semiconductor wafer. Glew ¶ 19. The patents describe approaches for preventing undesired buildup of the

example citations are provided to the '657 patent specification.



<sup>&</sup>lt;sup>1</sup> All exhibits are attached to the Declaration of C. Maclain Wells ("Wells") filed herewith. Also referenced is the Declaration of Dr. Alexander Glew ("Glew") also filed herewith, Ex. K.

<sup>2</sup> The "Demaray Patents" are U.S. Patent Nos. 7,381,657 and 7,544,276 ("'657 patent" and "'276").

patent," respectively) (Exs. A-B). Given that the specifications are substantively equivalent,

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