# EXHIBIT 12

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# Silicon-Correlated Voltus Power Signoff of Embedded FPGA IPs on Advanced FinFET Nodes
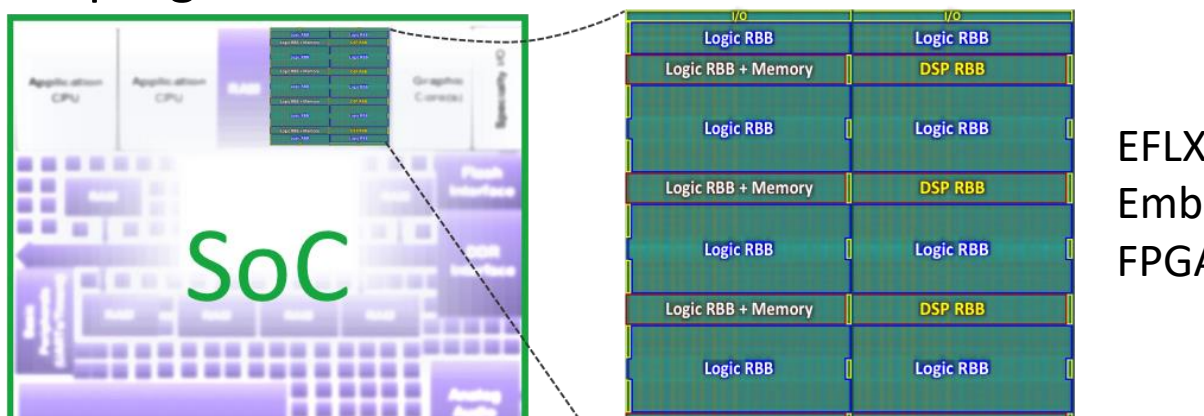
# Agenda

- What is Embedded FPGA IP ?

- Overview of Flex Logix eFPGA flow

- How to intergrade eFPGA in your Design ?

- Why Power Signoff is important to for eFPGA ?

- Voltus Signoff flow
    - Dynamic Power calculation  and SI co-relation
    - IR drop

# What is Embedded FPGA IP ?

# Make ICs Flexible

- Reconfigure RTL in your SoC/MCU <u>anytime</u>
  - Keep up with changing algorithms, industry standards, and customer requests
  - One chip can serve multiple applications
  - Longer chip life, higher ROI
- EFLX® Embedded FPGA IP cores and software
  - Any size and process
  - Software to program it

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