EXHIBIT B

SECOND AMENDED COMPLAINT FOR PATENT INFRINGEMENT

(12) United States Patent Lee et al.

(45) Date of Patent:

(10) Patent No.:

US 7,256,486 B2 Aug. 14, 2007

(54)	PACKAGING DEVICE FOR
	SEMICONDUCTOR DIE, SEMICONDUCTOR
	DEVICE INCORPORATING SAME AND
	METHOD OF MAKING SAME

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(*)	Notice:	Subject to any disclaimer, the term of this
		patent is extended or adjusted under 35
		$H \subseteq C \setminus 154(h)$ by 0 days

(21)	App	J No	. 10/	608 6	0.5
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(65) **Prior Publication Data**

US 2004/0262738 A1 Dec. 30, 2004

(51)	Int. Cl.	
	H01L 29/22	(2006.01)

- (52) **U.S. Cl.** **257/690**; 257/784; 257/690

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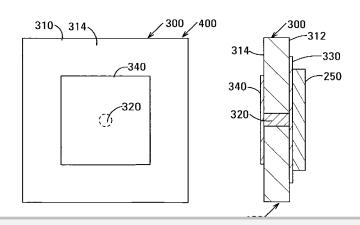
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Primary Examiner—Sara Crane

(57) ABSTRACT

The packaging device includes a substrate, a mounting pad, a connecting pad and an interconnecting element. The substrate is substantially planar and has opposed major surfaces. The mounting pad is conductive and is located on one of the major surfaces. The connecting pad is conductive and is located on the other of the major surfaces. The conductive interconnecting element extends through the substrate and electrically interconnects the mounting pad and the connecting pad. The packaging device has a volume that is only a few times that of the semiconductor die and can be fabricated from materials that can withstand high-temperature die attach processes. The packaging device can be configured as the only packaging device used in the semiconductor device or as a submount for a semiconductor die that requires a high-temperature die attach process.

6 Claims, 8 Drawing Sheets





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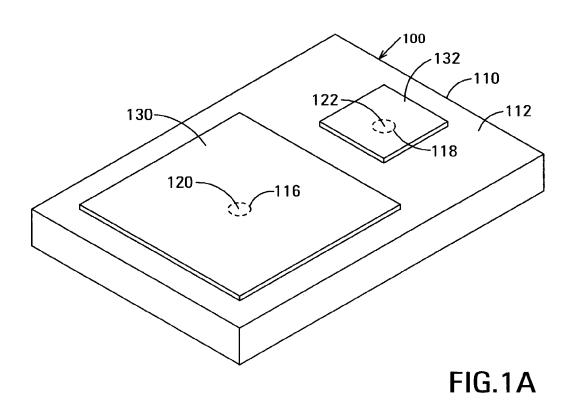


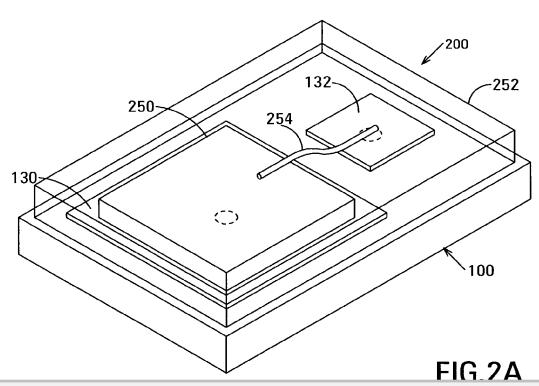
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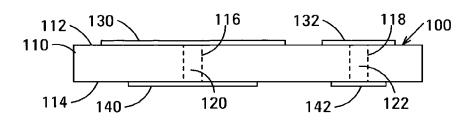


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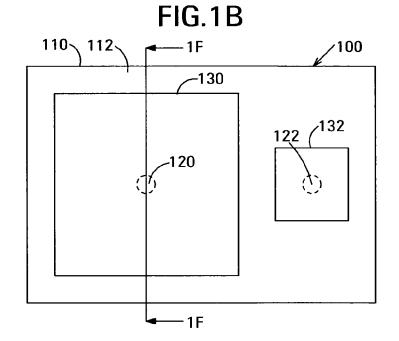
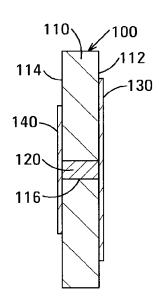


FIG.1C

FIG.1D



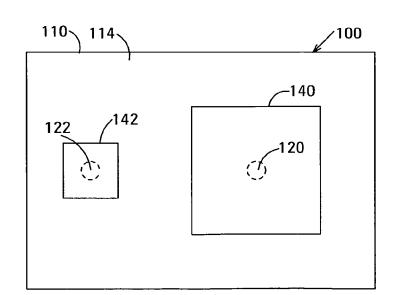


FIG.1F

FIG.1E



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